

TECHNICAL SPECIFICATIONS

EQ Station

Analog Input to Output

Connectors:	XLR balanced (pin 2+, pin 3-)
Input Impedance:	11/4 kohm (Balanced/unbalanced):
Output Impedance:	40/20 ohm (Balanced/unbalanced)
Max Input Level:	+15 to +30dBu with analog domain scaling
Max Output Level:	+15 to +24dBu balanced with analog domain scaling
Dynamic Range:	> 110dB (unweighted), BW: 20-20kHz, note 1
THD:	< -100dB @ BW: 20-20kHz
Frequency Response, +0/-0.1dB:	12 Hz - 20 kHz
Crosstalk:	< -110dB, 20 Hz to 20 kHz
AD and DA Conversion:	24 bit (Dual bit delta sigma sampling at 4.1/5.6/6.1/6.1 MHz)
AD and DA + Processing Delay Total:	1.54 ms

DSP

Internal sample rate:	48 kHz, high performance low jitter clock circuit
Internal processing:	Routing and processing 48 bit fixed point throughout
DSP power:	600 Million Instructions Per Second

Control Interface

Ethernet:	10/100 Mbits/s, Base-T (RJ45 XLR-housed EtherCon connector)
Ethernet passive HUB for network:	10/100 Mbits/s, Base-T (RJ45 XLR-housed EtherCon connector)
MIDI:	In/Out (5-pin DIN connector)
Motofader 64 remote:	Custom serial interface (9-pin D-Sub connector)

General

Display:	Color TFT, QVGA 320x240 pixels 450 cd/m2 Luminance
Finish:	Anodized aluminum face and side plates Plated and painted steel chassis
Dimensions:	19" x 3.50" x 11.5"
Mains voltage:	100 to 240 VAC, 50 to 60 Hz (auto-select)
Power consumption:	<45W
EMC Complies with:	EN 55103-1 and EN 55103-2 FCC part 15, Class B CISPR 22, Class B
Safety Certified to:	IEC 60065, EN 60065, UL 6500 and CSA E65 CSA File#LR108093
Operating/Storage Temperature:	0° C to 50° C (32° F to 122° F) / -30° C to 70° C (-22° F to 167° F)
Humidity:	Max. 90 % non-condensing
Warranty Parts and Labor:	1 year *

* Up to 2 pixel errors in the TFT display is NOT considered as a malfunction and does not invoke the warranty.

Digital I/O card

Connectors:	D-SUB, 25 pole (8 channels AES/EBU in/out)
Format:	AES/EBU (24bit)
Internal sample rates:	44.1kHz and 48kHz
Internal clock precision:	+/- 25 PPM
External sample rates:	44.1kHz and 48kHz +/- 200 PPM* or wide 30 – 50kHz
Jitter rejection filter (4 th order filter):	< -3dB @ 10 Hz, < -68dB @ 500 Hz, < -100dB @ 1.3 kHz
Jitter gain:	< 1 dB @ 2Hz
Interface jitter:	<1ns peak, BW: 700Hz – 100kHz
Digital output phase:	<1%
Input variation before sample slip:	-28%>+50%
Processing Delay digital I/O:	0.7 mS
Frequency response DIO:	20 Hz - 24 kHz @ 48 kHz 20 Hz - 22 kHz @ 44.1 kHz