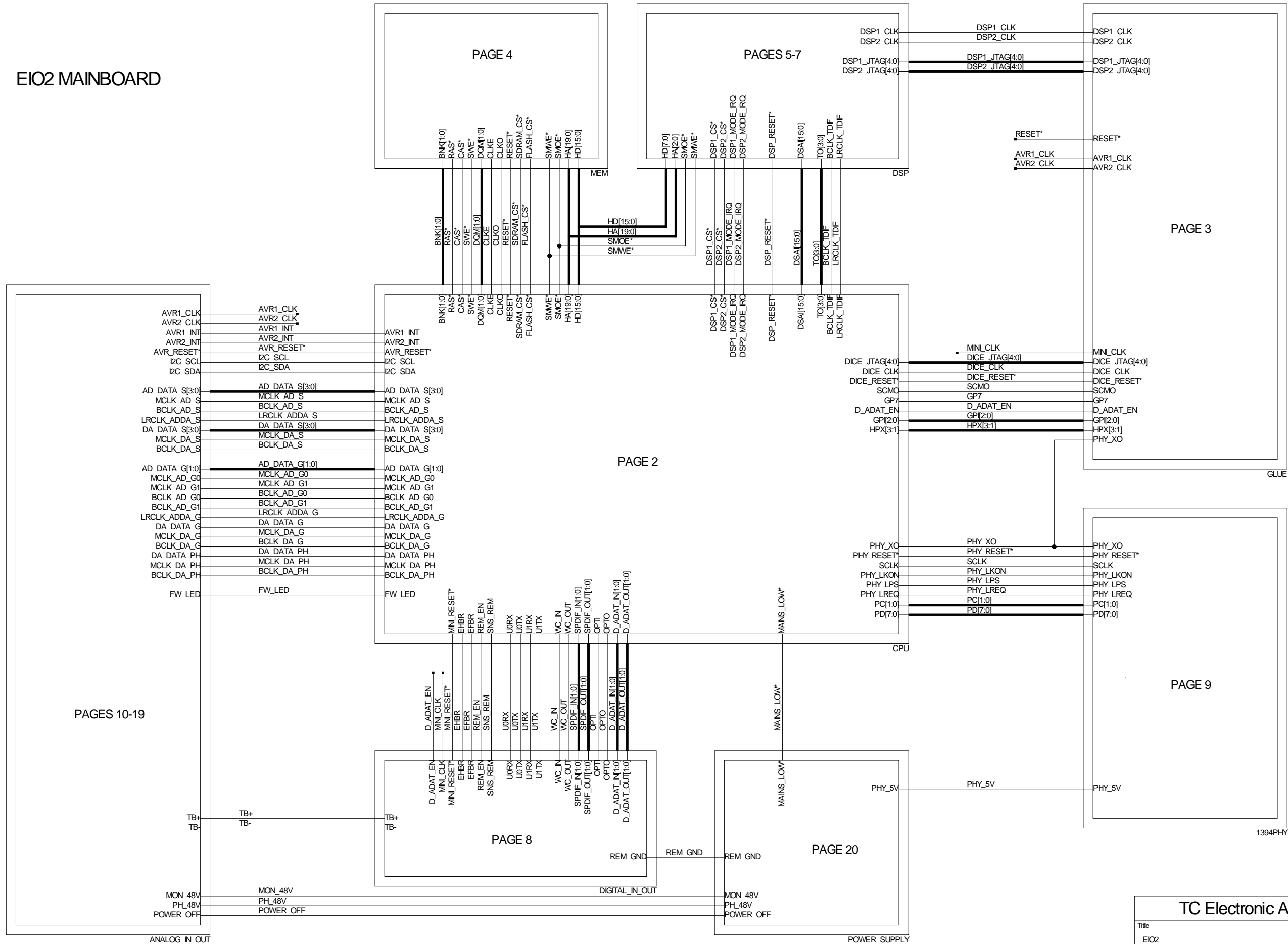
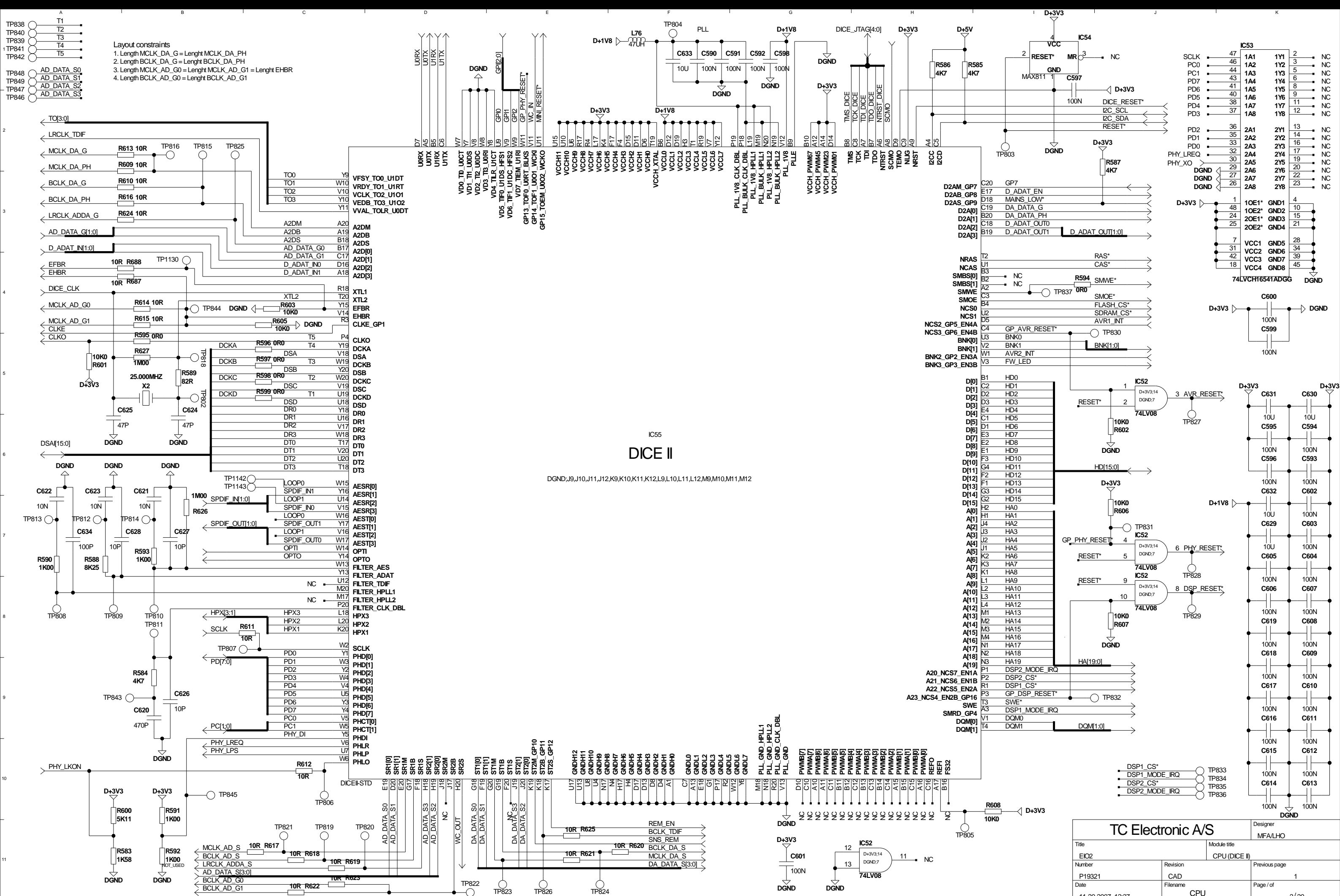


EIO2 MAINBOARD



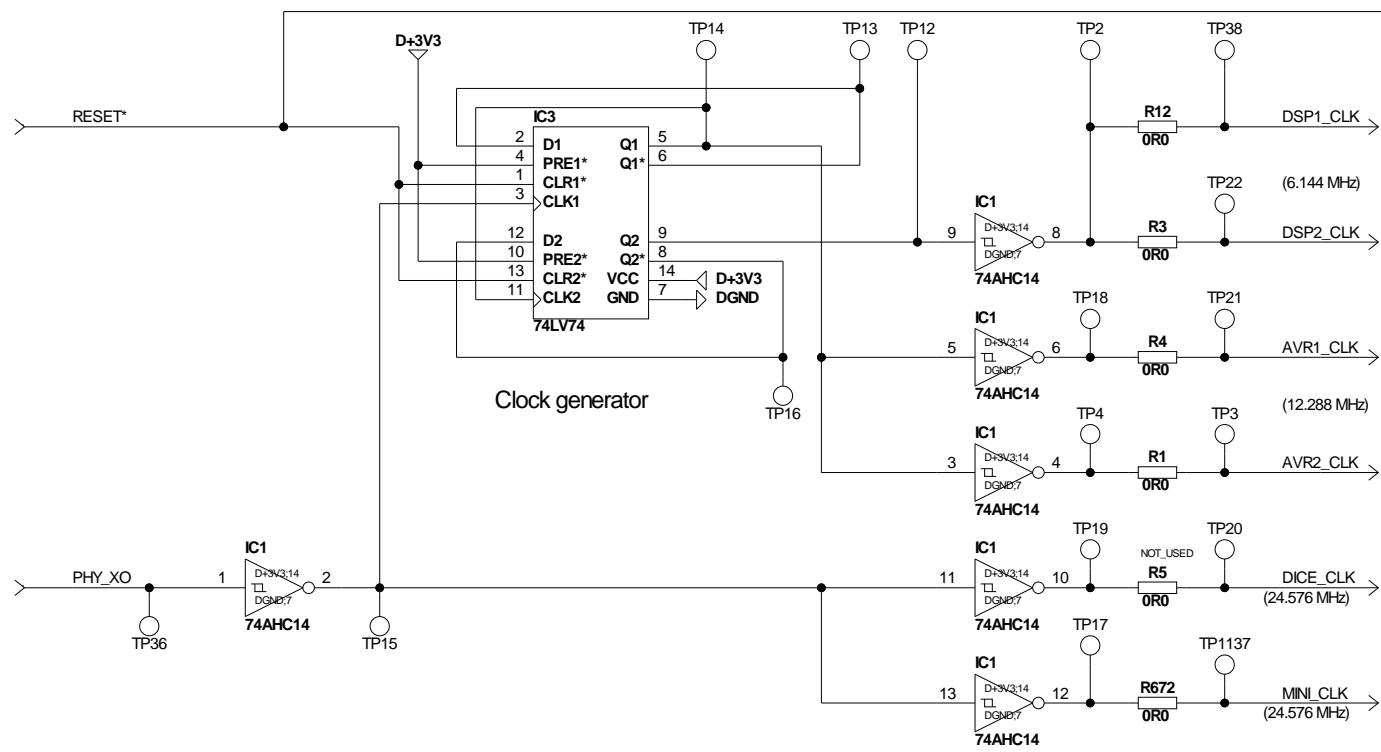
M1
P19321-C

TC Electronic A/S		Designer
		MFA/LHO/CGF
Title	EIO2	
Number	Revision	Previous page
P19321	CAD	NONE
Date	Filename	Page / of
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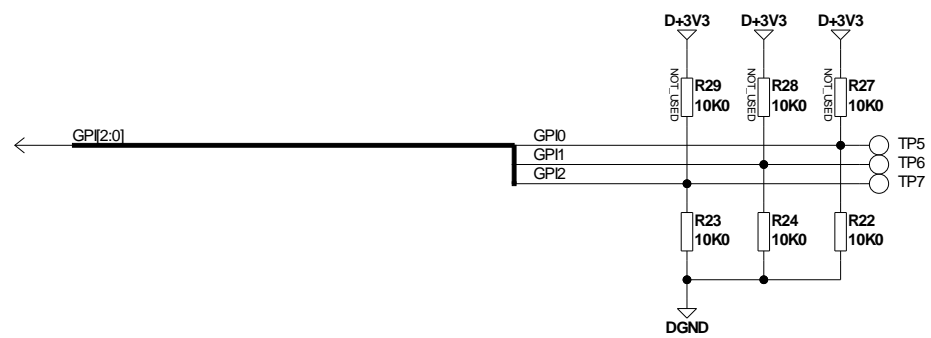


Layout constraints
 1. Length MCLK_DA_G = Lenght MCLK_DA_PH
 2. Length BCLK_DA_G = Lenght BCLK_DA_PH
 3. Length MCLK_AD_G0 = Lenght MCLK_AD_G1 = Lenght EHBR
 4. Length BCLK_AD_G0 = Lenght BCLK_AD_G1

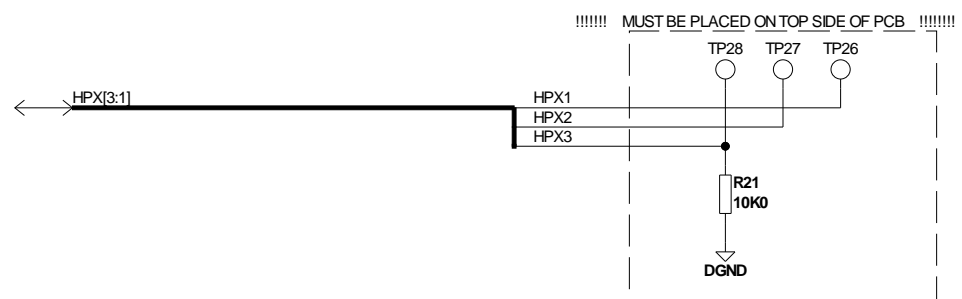
TC Electronic A/S		Designer	
EIO2		MFA/LHO	
Module title		CPU (DICE II)	
Number	Revision	Previous page	
P19321	CAD	1	
Date	Filename	Page / of	
11-20-2007_12:37	CPU	2 / 20	



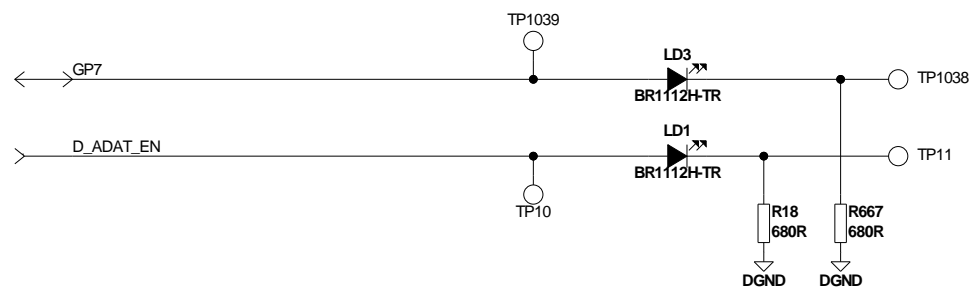
Layout request
Add net-name @ TP's for clock's and other "important" signals



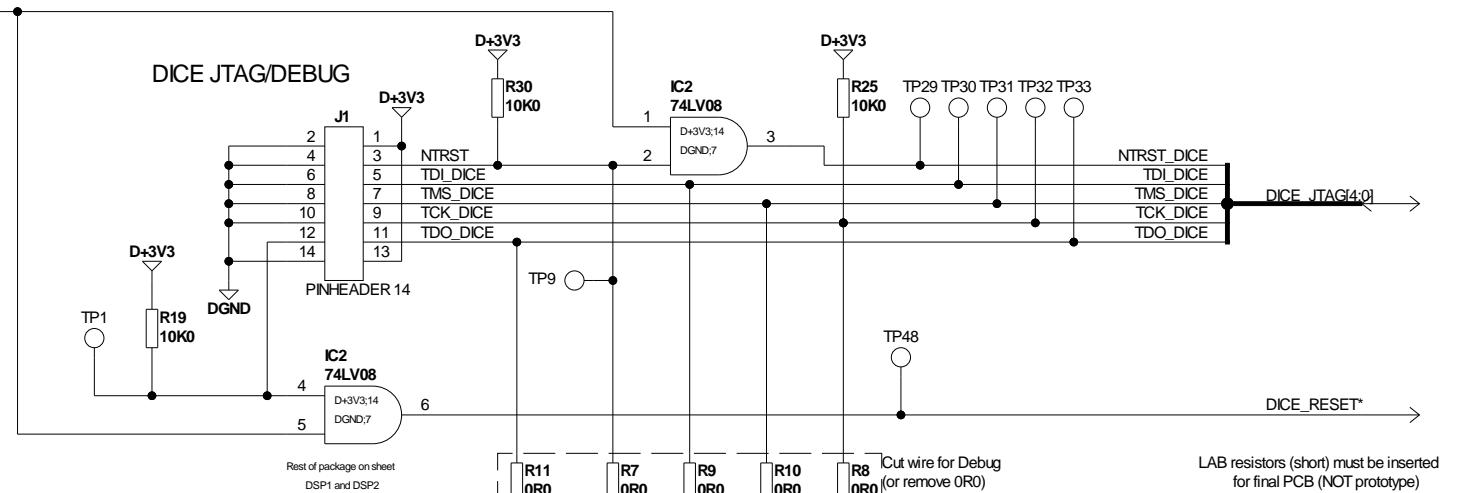
EIO2 Variant Control



HPLL external pins

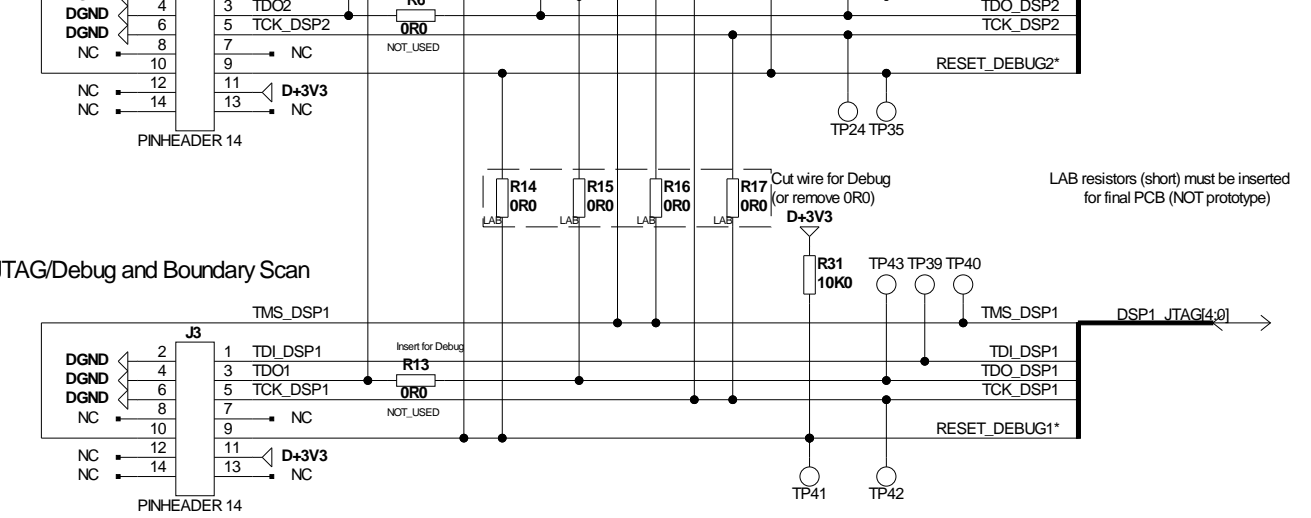


Debug LEDs

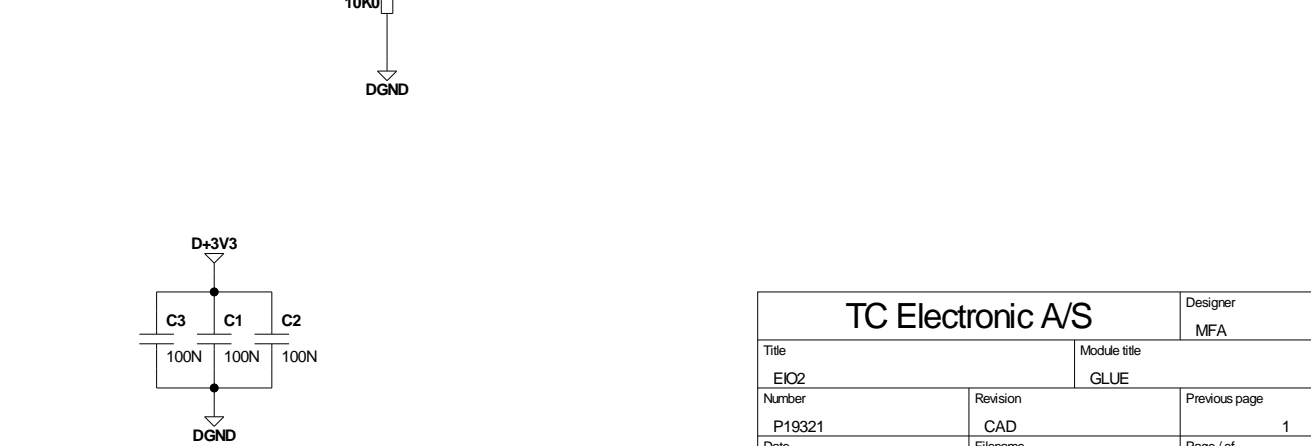


DSP2 JTAG/Debug

DSP1 JTAG/Debug and Boundary Scan

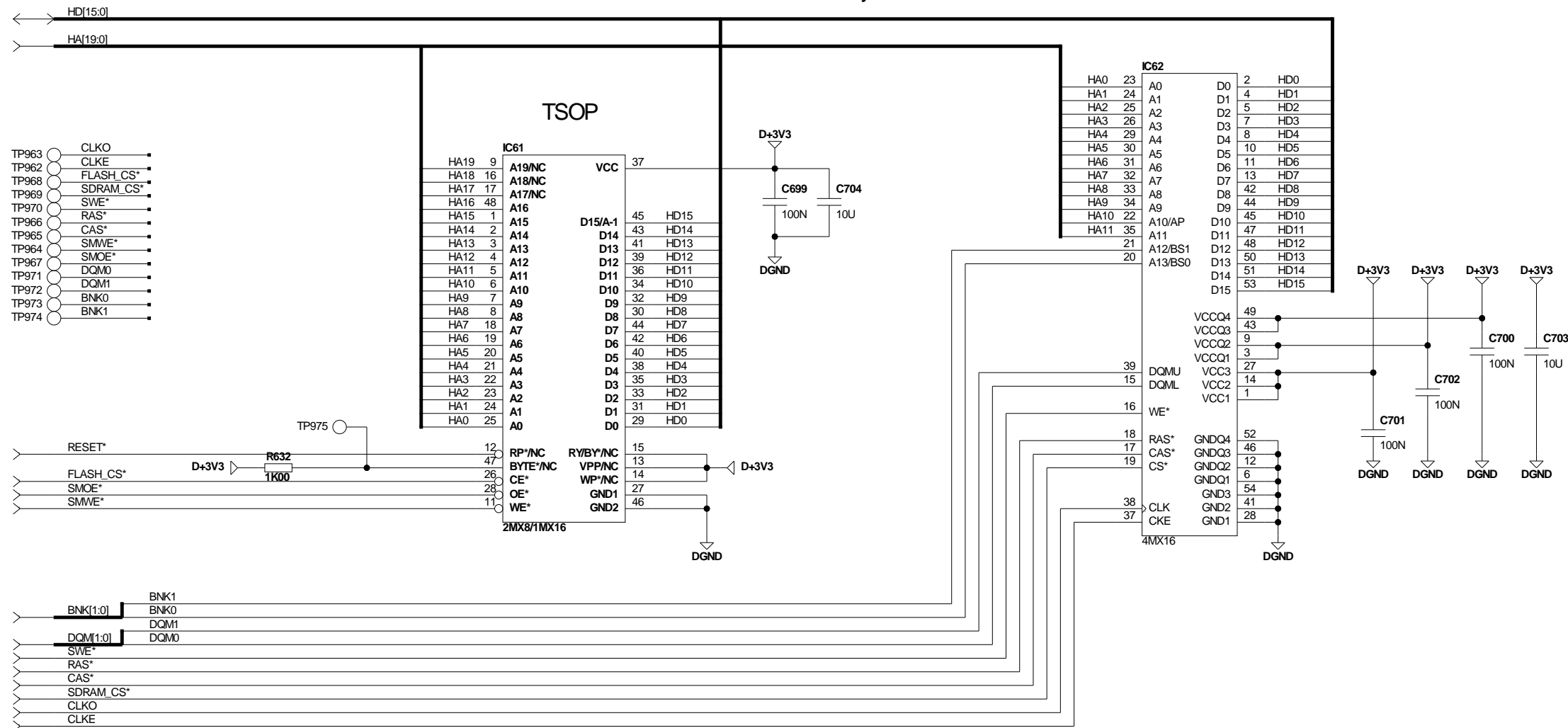


DICE SCAN MODE



TC Electronic A/S Title: EIO2 Number: P19321 Date: 11-20-2007_12:40		Designer: MFA Module title: GLUE
		Previous page: 1 Page / of: 3 / 20
Revision: CAD Filename: GLUE		Previous page: 1 Page / of: 3 / 20

Host Memory



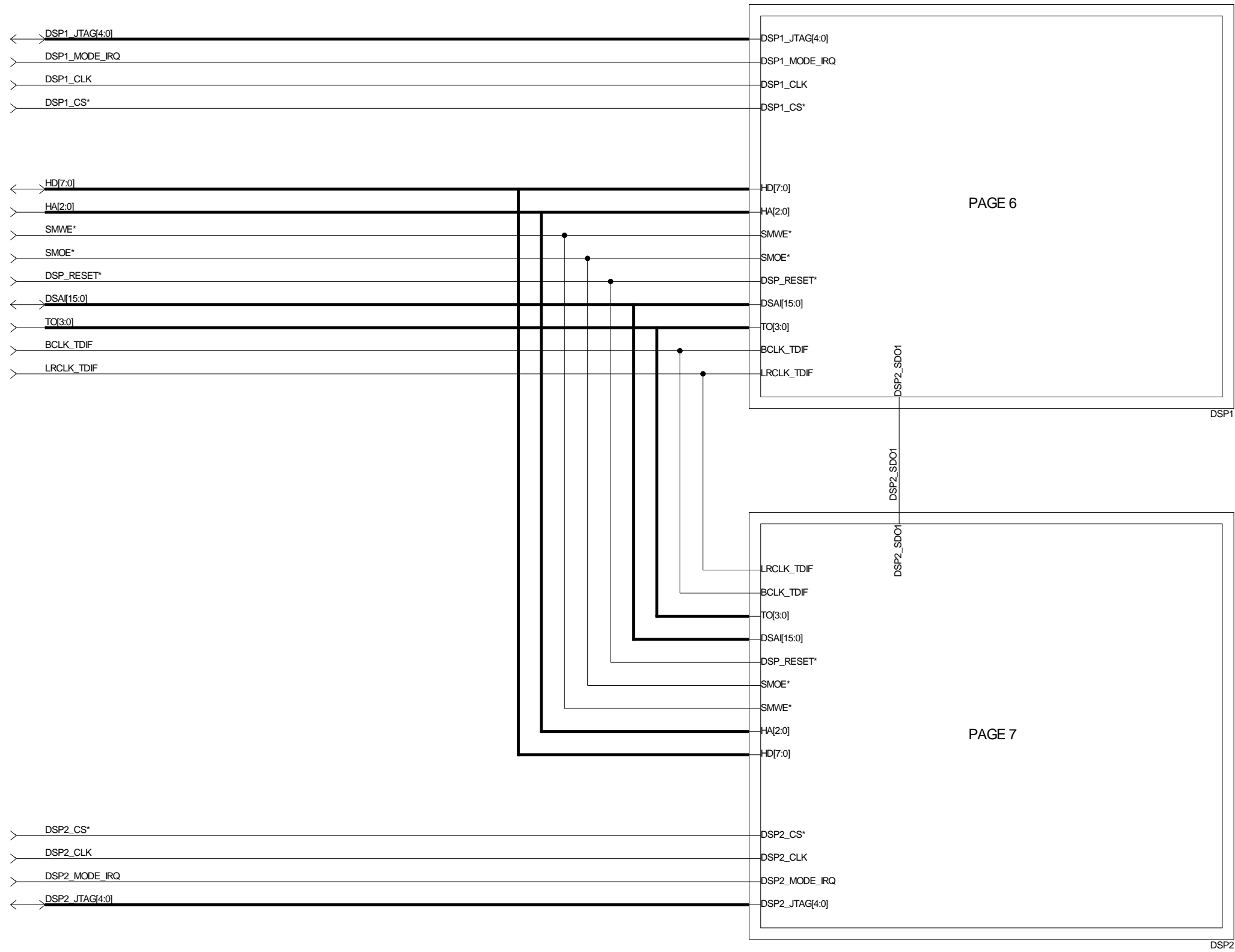
- TP963 ○ CLKO
- TP962 ○ CLKE
- TP968 ○ FLASH_CS*
- TP969 ○ SDRAM_CS*
- TP970 ○ SWE*
- TP966 ○ RAS*
- TP965 ○ CAS*
- TP964 ○ SMWE*
- TP967 ○ SMOE*
- TP971 ○ DQM0
- TP972 ○ DQM1
- TP973 ○ BNK0
- TP974 ○ BNK1

- RESET*
- FLASH_CS*
- SMOE*
- SMWE*

- BNK1
- BNK0
- DQM1
- DQM0
- SWE*
- RAS*
- CAS*
- SDRAM_CS*
- CLKO
- CLKE

- TP951 ○ HD0
- TP950 ○ HD1
- TP952 ○ HD2
- TP958 ○ HD3
- TP953 ○ HD4
- TP954 ○ HD5
- TP955 ○ HD6
- TP957 ○ HD7
- TP956 ○ HD8
- TP946 ○ HD9
- TP945 ○ HD10
- TP947 ○ HD11
- TP948 ○ HD12
- TP959 ○ HD13
- TP960 ○ HD14
- TP949 ○ HD15

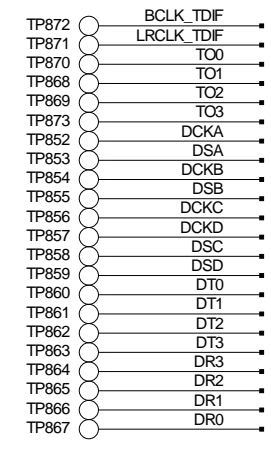
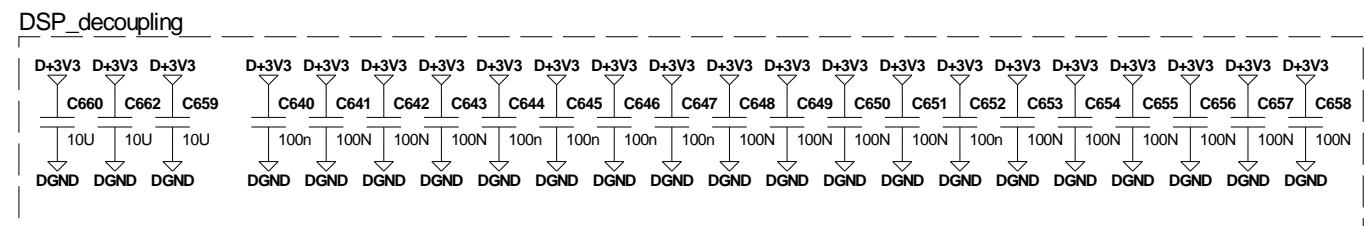
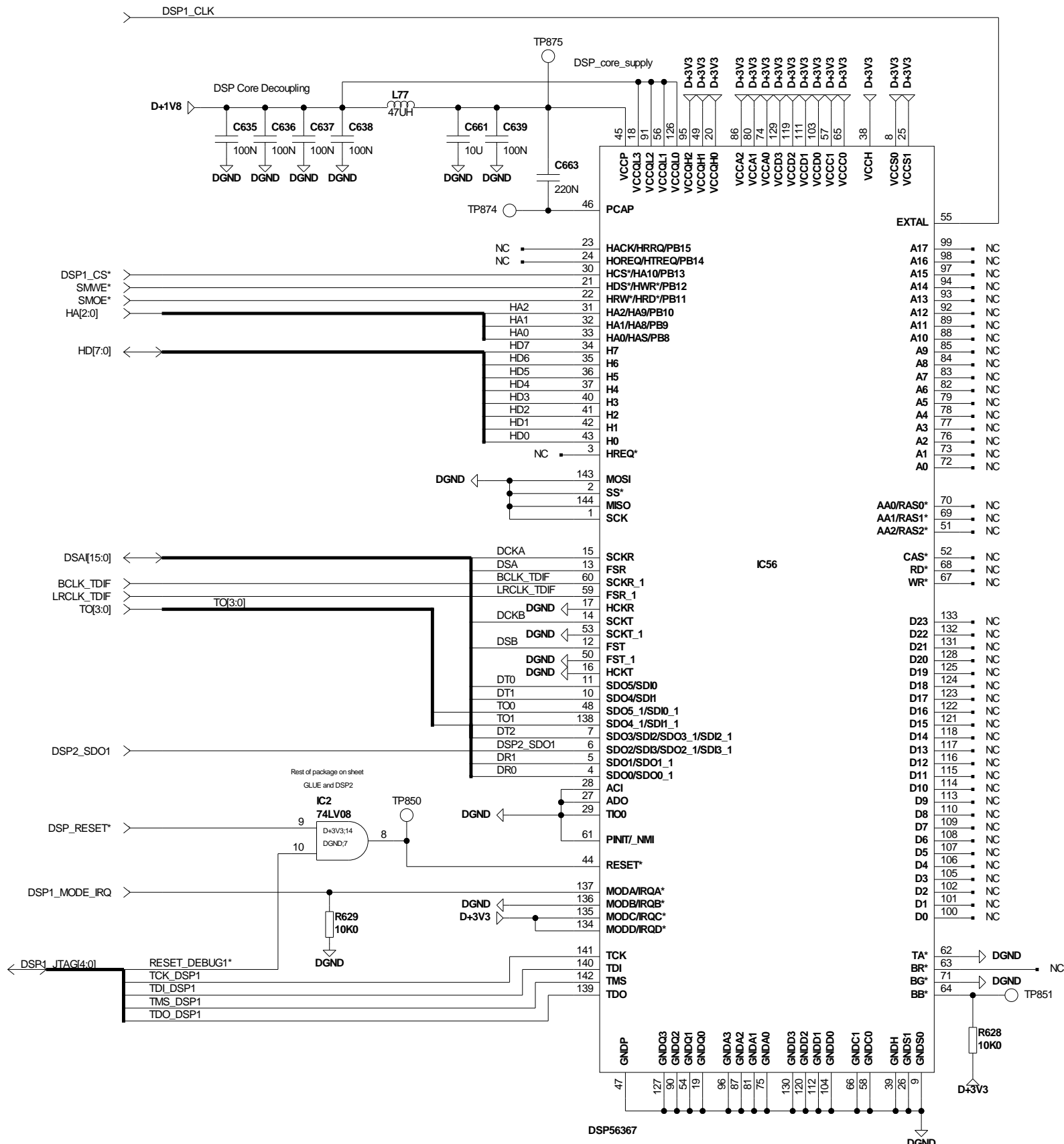
- TP935 ○ HA0
- TP934 ○ HA1
- TP936 ○ HA2
- TP942 ○ HA3
- TP937 ○ HA4
- TP938 ○ HA5
- TP939 ○ HA6
- TP941 ○ HA7
- TP940 ○ HA8
- TP930 ○ HA9
- TP929 ○ HA10
- TP931 ○ HA11
- TP932 ○ HA12
- TP943 ○ HA13
- TP944 ○ HA14
- TP933 ○ HA15
- TP926 ○ HA16
- TP927 ○ HA17
- TP928 ○ HA18
- TP961 ○ HA19

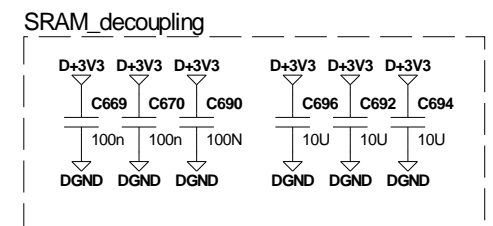
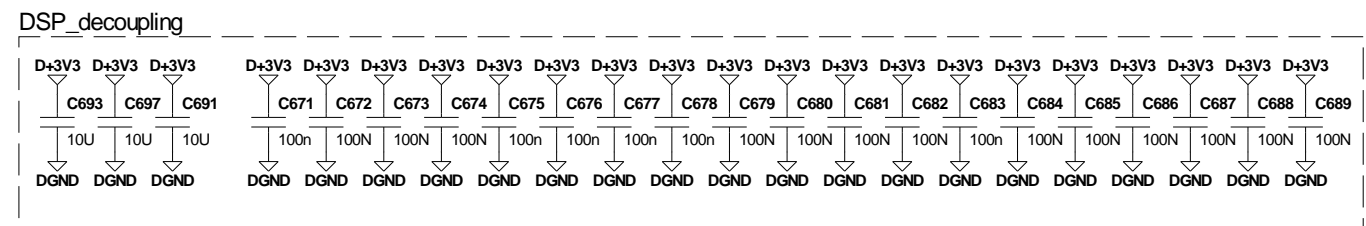
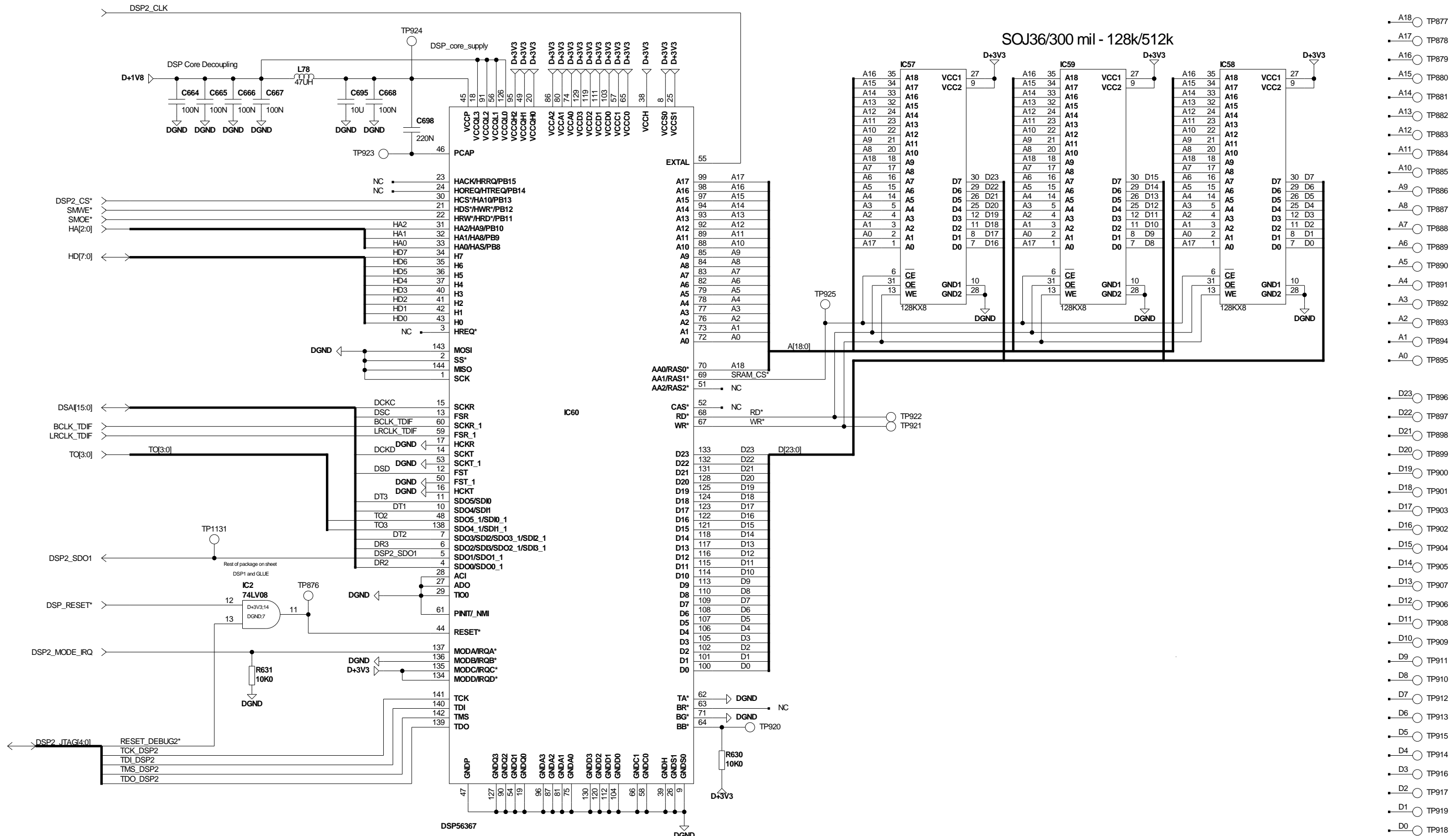


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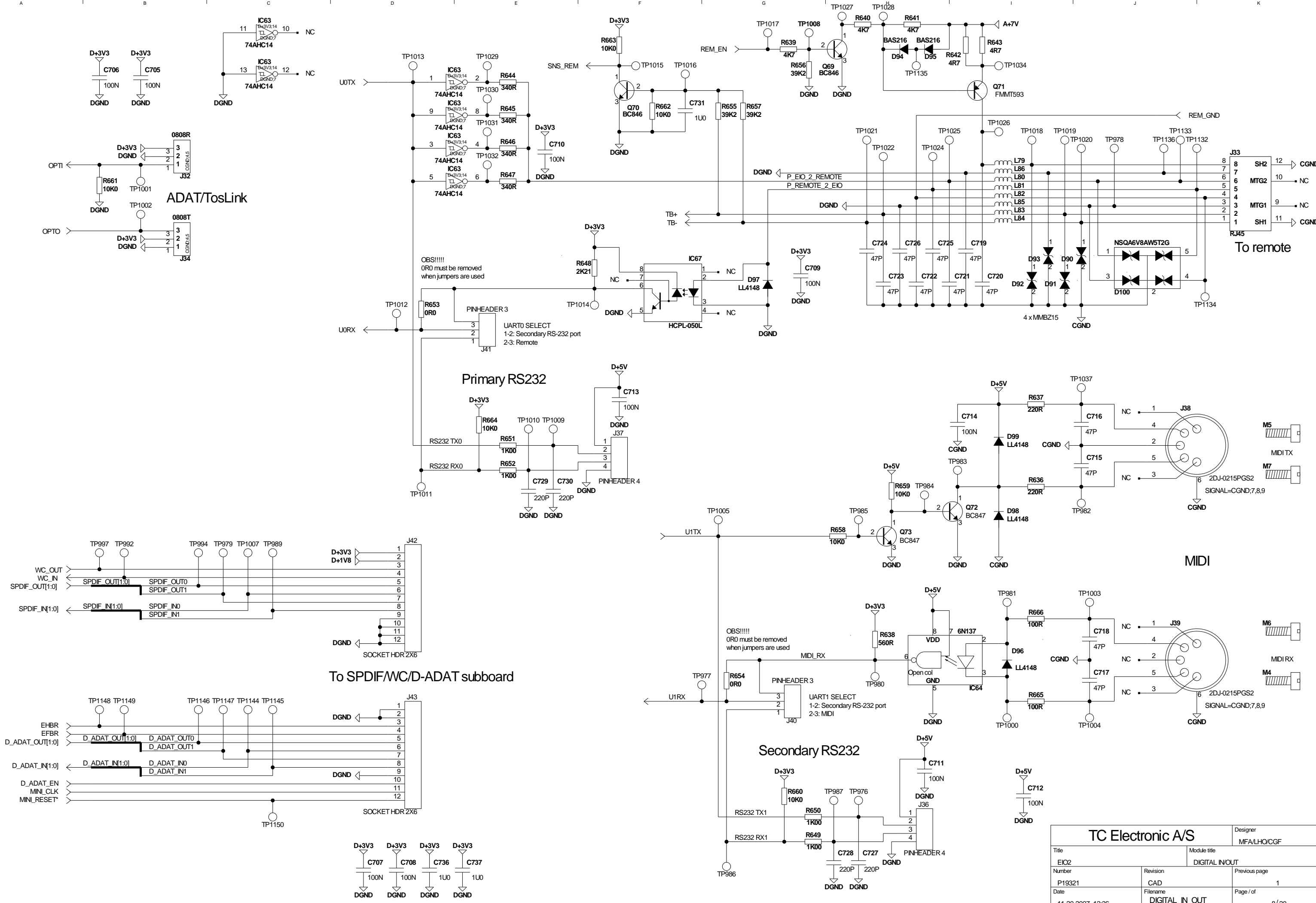
PAGE 7

TC Electronic A/S		Designer MFA
Title EIO2	Module title DSP	
Number P19321	Revision CAD	Previous page 1
Date 11-20-2007_12:36	Filename DSP	Page / of 5 / 20

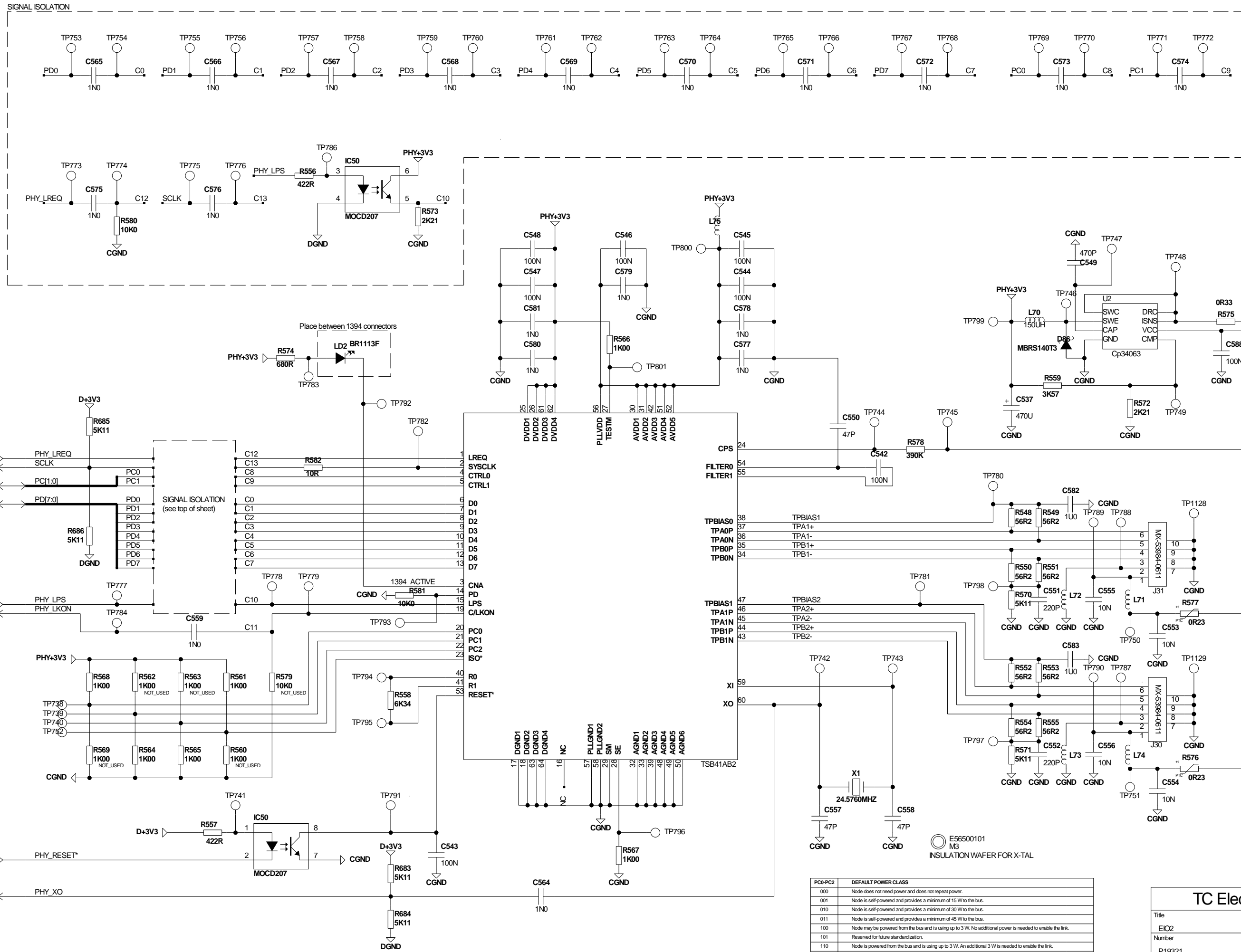




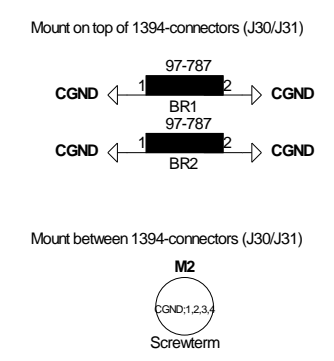
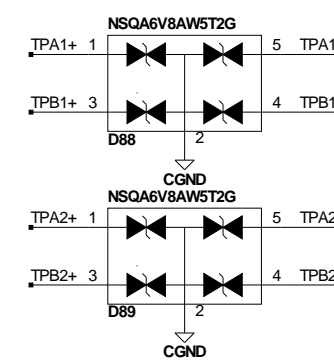
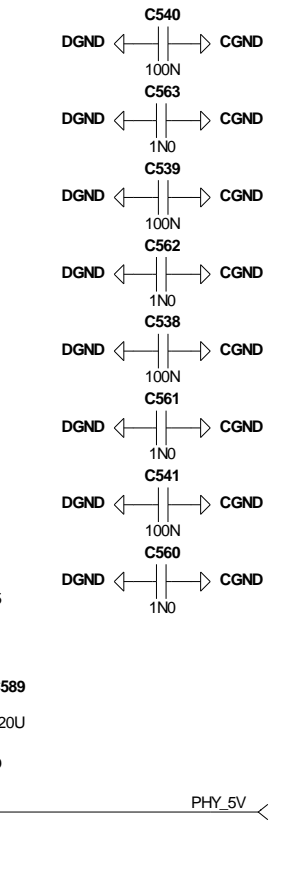
TC Electronic A/S		Designer	MFA/LHO/CGF
Title	Module title		DSP2
Number	Revision	Previous page	
P19321	CAD	5	
Date	Filename	Page / of	
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TC Electronic A/S		Designer	MFA/LHO/CGF
Title	EIO2	Module title	DIGITAL IN/OUT
Number	P19321	Revision	CAD
Date	11-20-2007_12:36	Filename	DIGITAL_IN_OUT
		Previous page	1
		Page / of	8 / 20

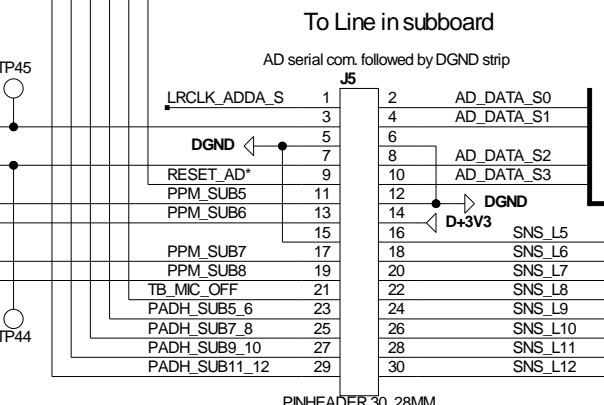
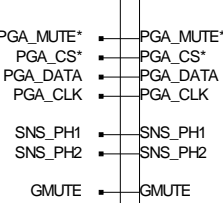
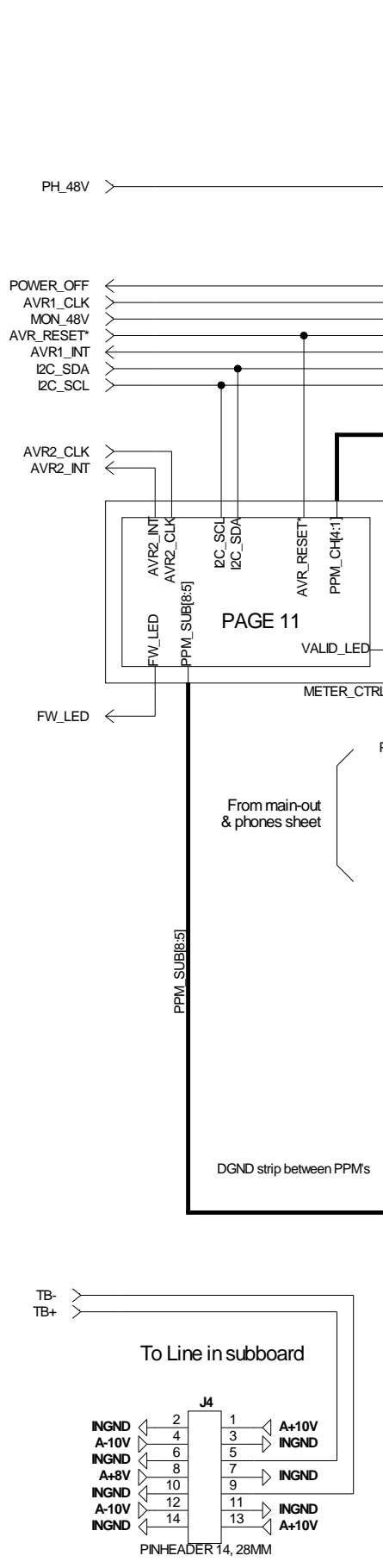
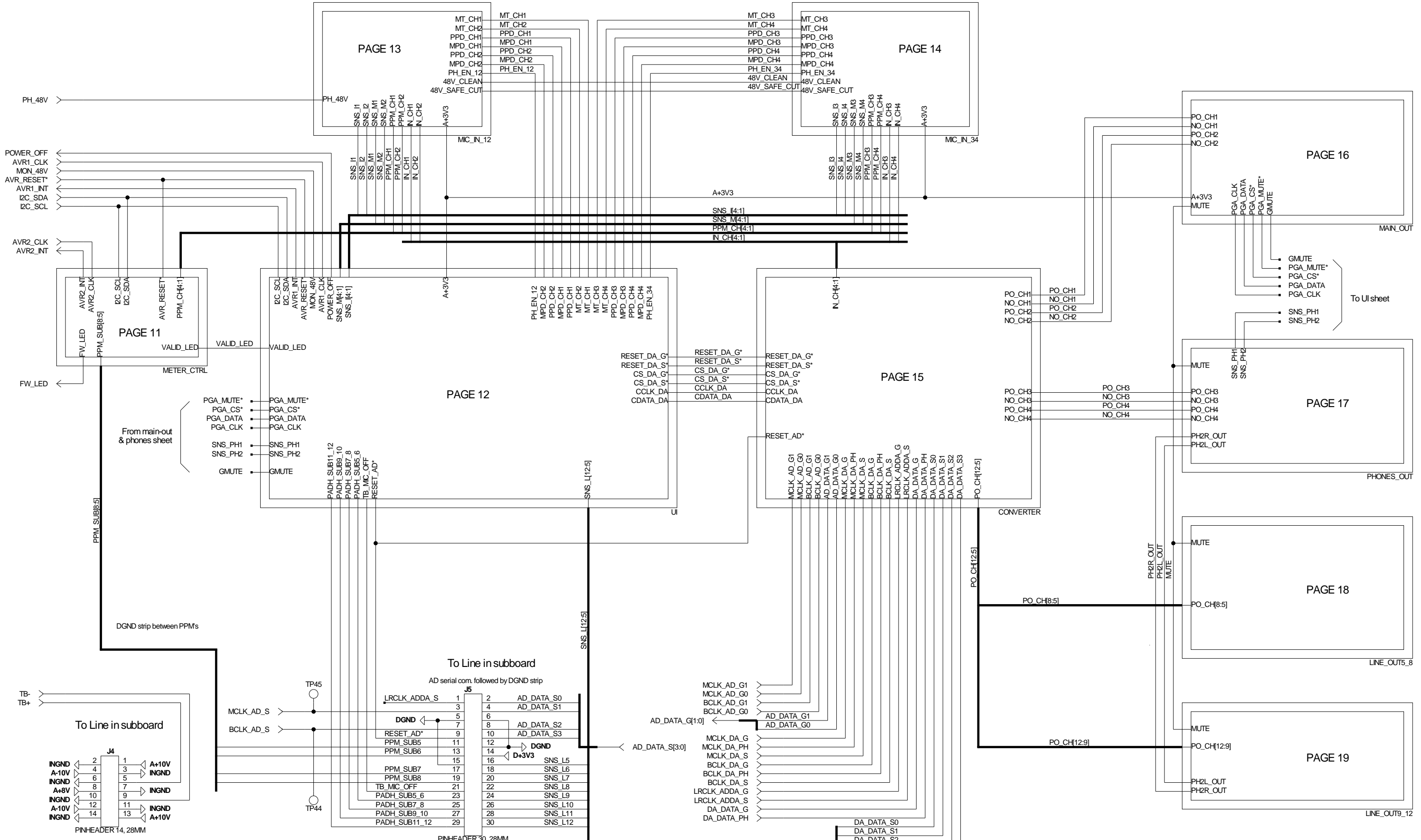


towards DICE towards PHY

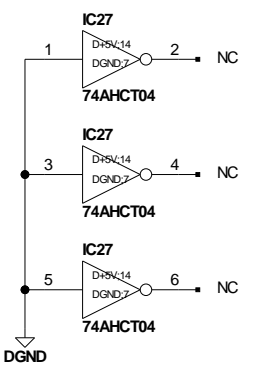
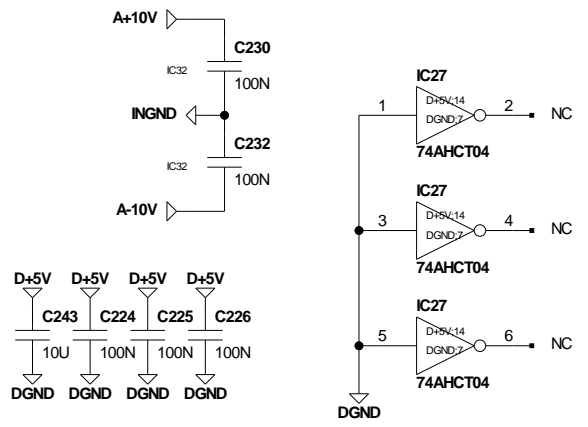
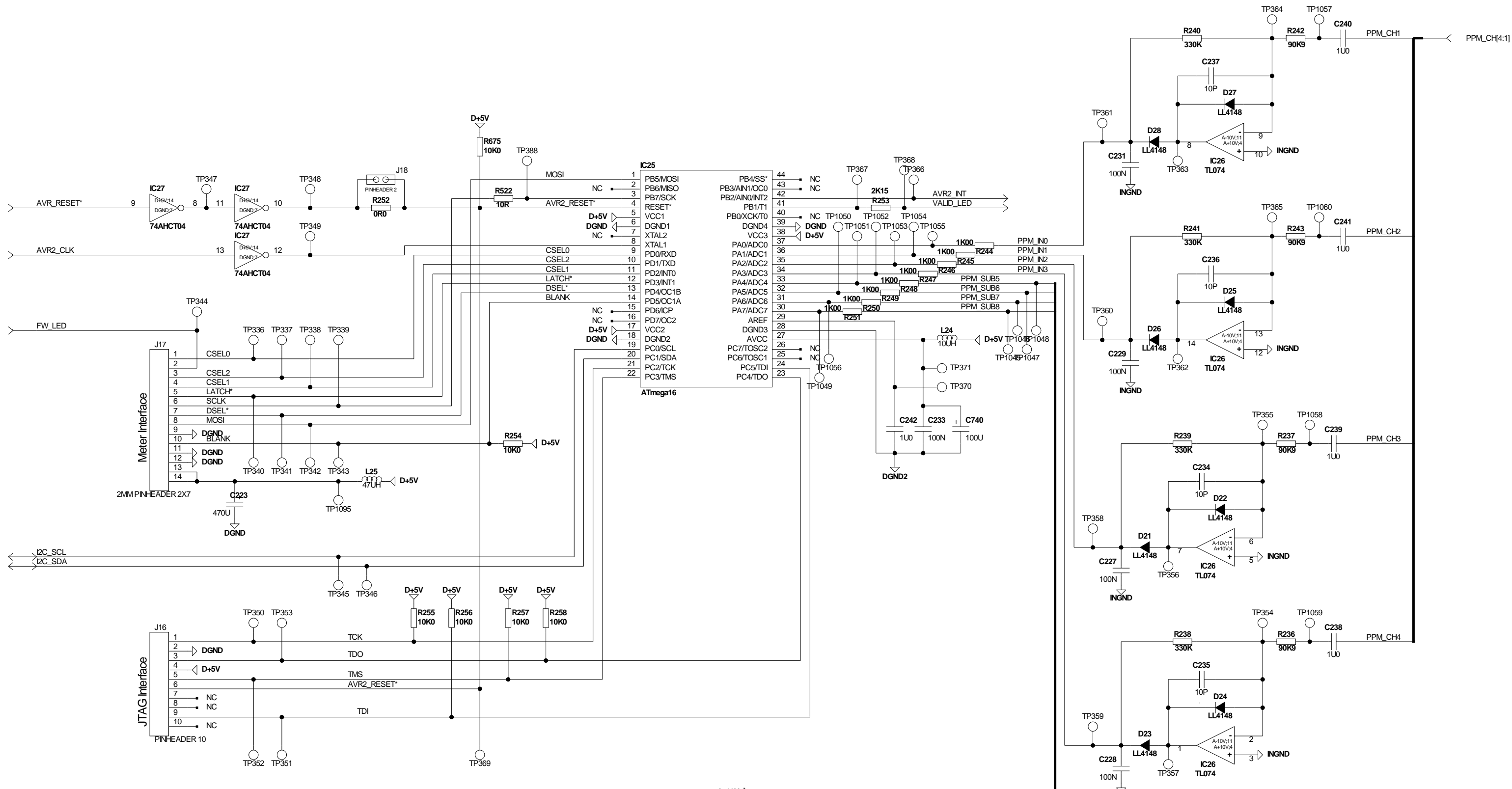


PC0-PC2	DEFAULT POWER CLASS
000	Node does not need power and does not repeat power.
001	Node is self-powered and provides a minimum of 15 W to the bus.
010	Node is self-powered and provides a minimum of 30 W to the bus.
011	Node is self-powered and provides a minimum of 45 W to the bus.
100	Node may be powered from the bus and is using up to 3 W. No additional power is needed to enable the link.
101	Reserved for future standardization.
110	Node is powered from the bus and is using up to 3 W. An additional 3 W is needed to enable the link.
111	Node is powered from the bus and is using up to 3 W. An additional 7 W is needed to enable the link.

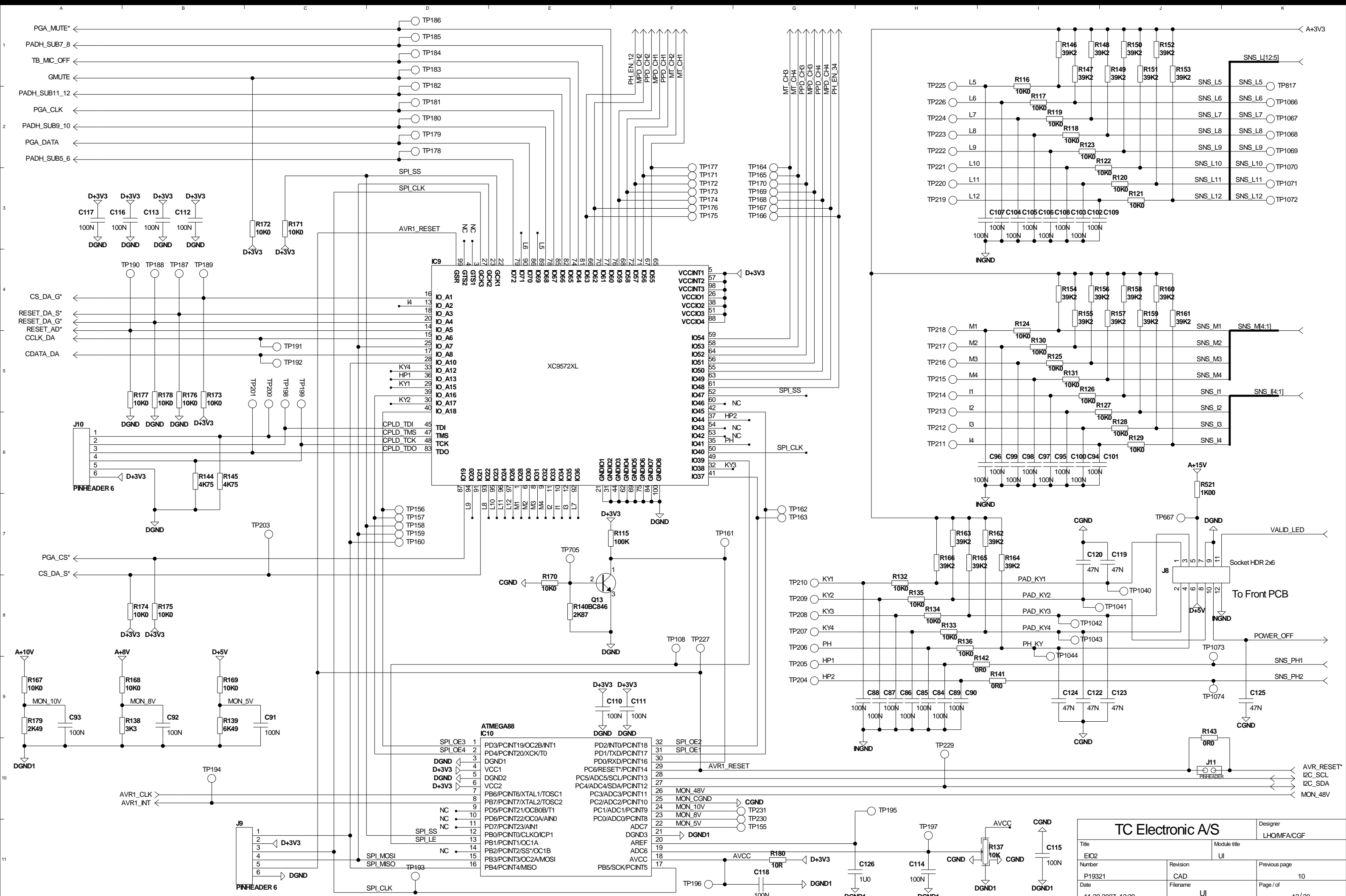
TC Electronic A/S		Designer
		LHOMFA/CGF
Title	Module title	
EIO2	FIREWIRE PHY	
Number	Revision	Previous page
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TC Electronic A/S		Designer MFA
Title EIQ2		Module title ANALOG IN/OUT TOP
Number P19321	Revision CAD	Previous page 1
Date 11-20-2007_12:40	Filename ANALOG_IN_OUT	Page / of 10 / 20

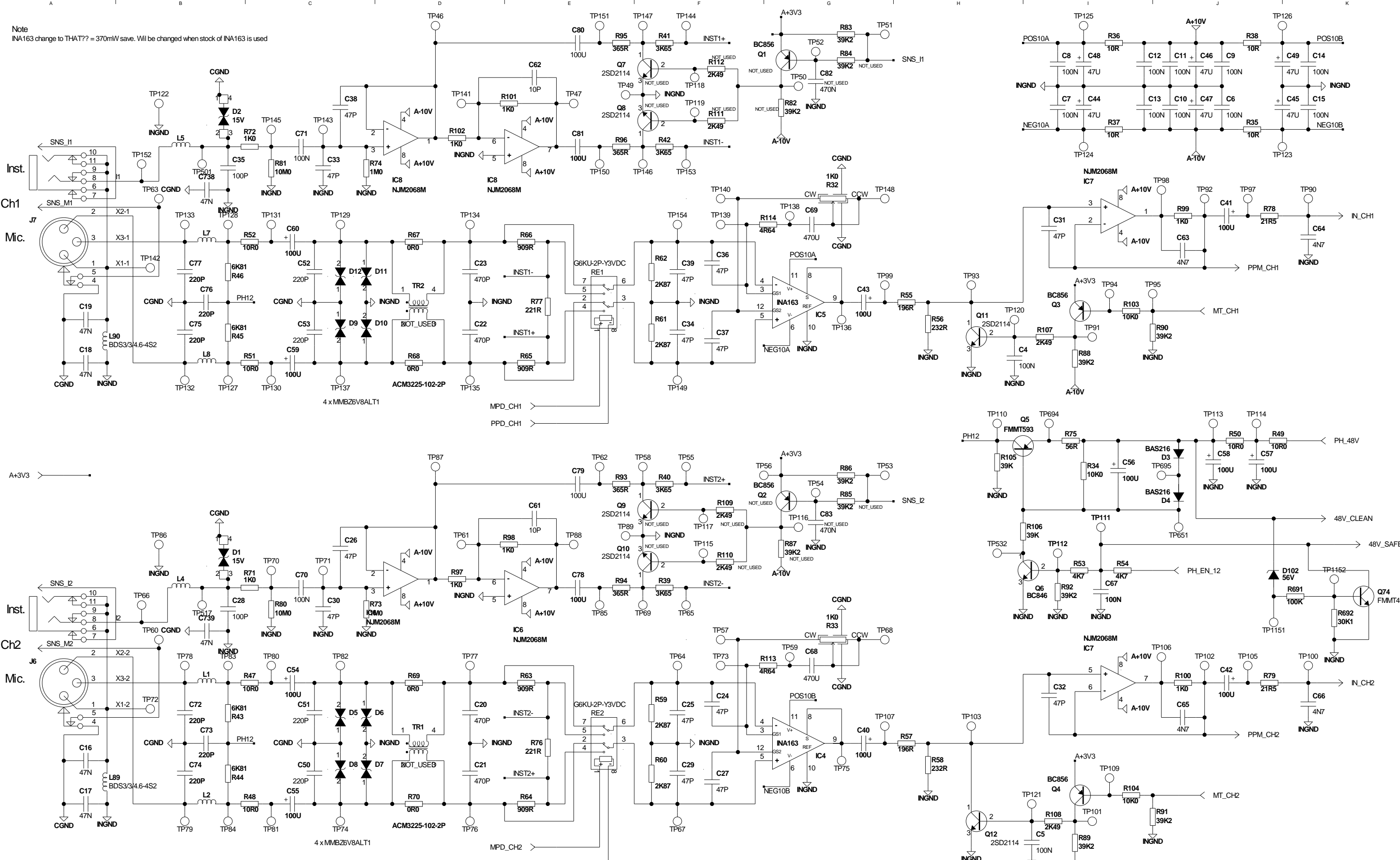


TC Electronic A/S		Designer MFALHOICGF
Title EIO2	Module title METER CONTROLLER	
Number P19321	Revision CAD	Previous page 10
Date 11-20-2007_12:39	Filename METER_CTRL	Page / of 11 / 20



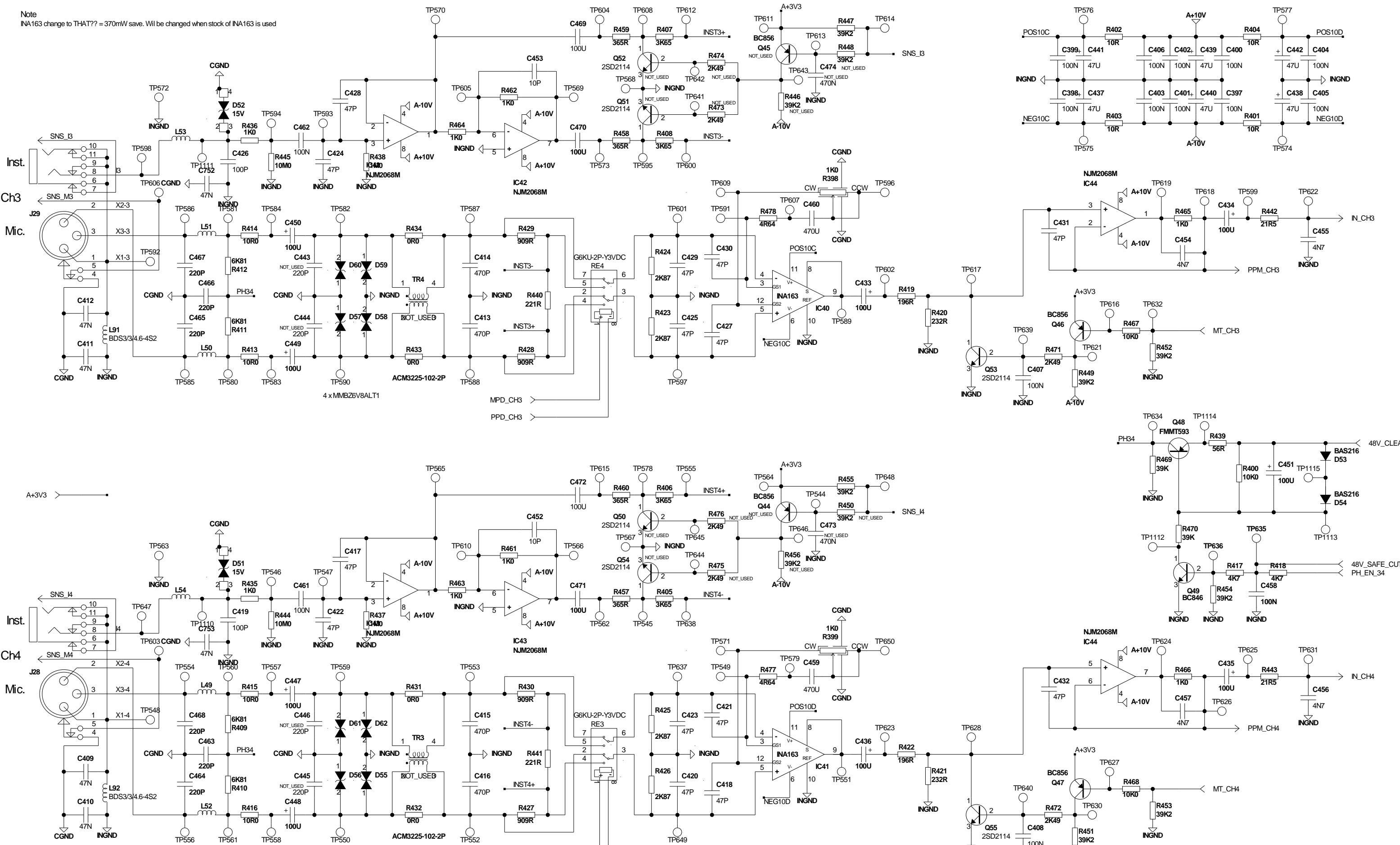
TC Electronic A/S		Designer	LHOMFA/CGF
Title	EIO2	Module title	UI
Number	P19321	Revision	CAD
Date	11-20-2007_12:39	Filename	UI
		Previous page	10
		Page / of	12 / 20

Note
 INA163 change to THAT?? = 370mW save. Will be changed when stock of INA163 is used

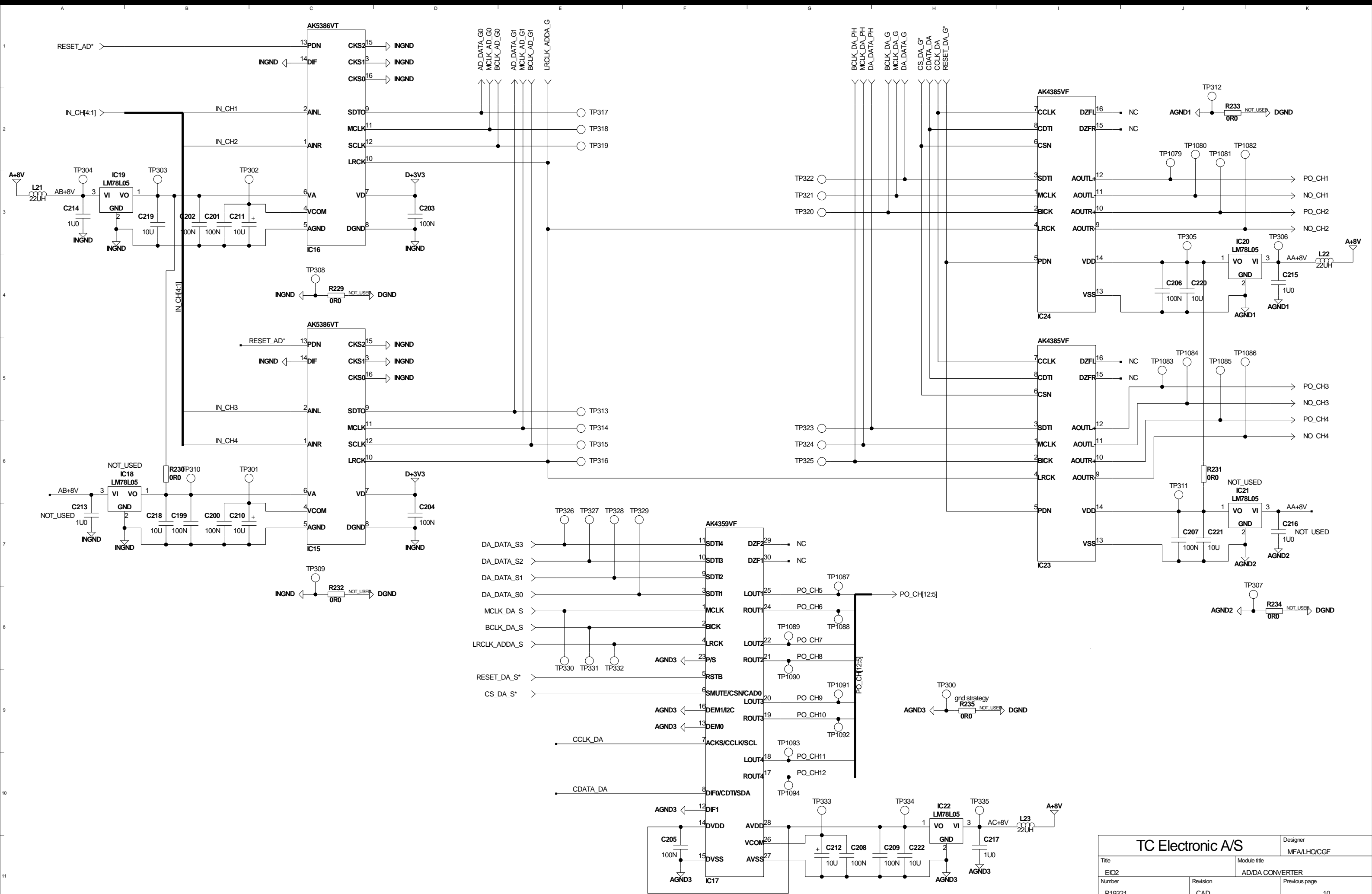


TC Electronic A/S		Designer	MFA
Title	EIO2	Module title	MIC/INSTR CH 1-2
Number	P19321	Revision	CAD
Date	11-20-2007 12:40	Filenam	MIC_IN_12
		Page / of	10
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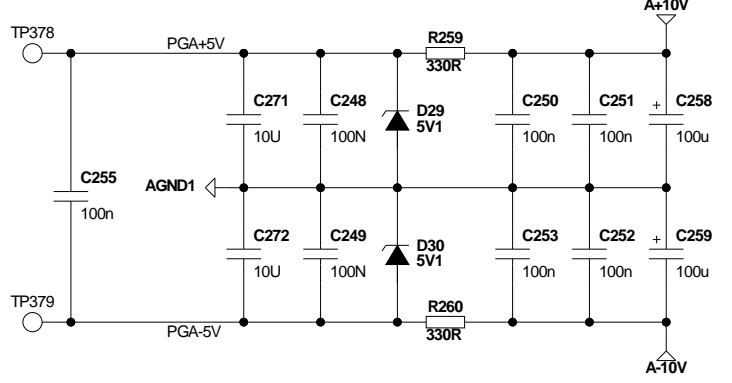
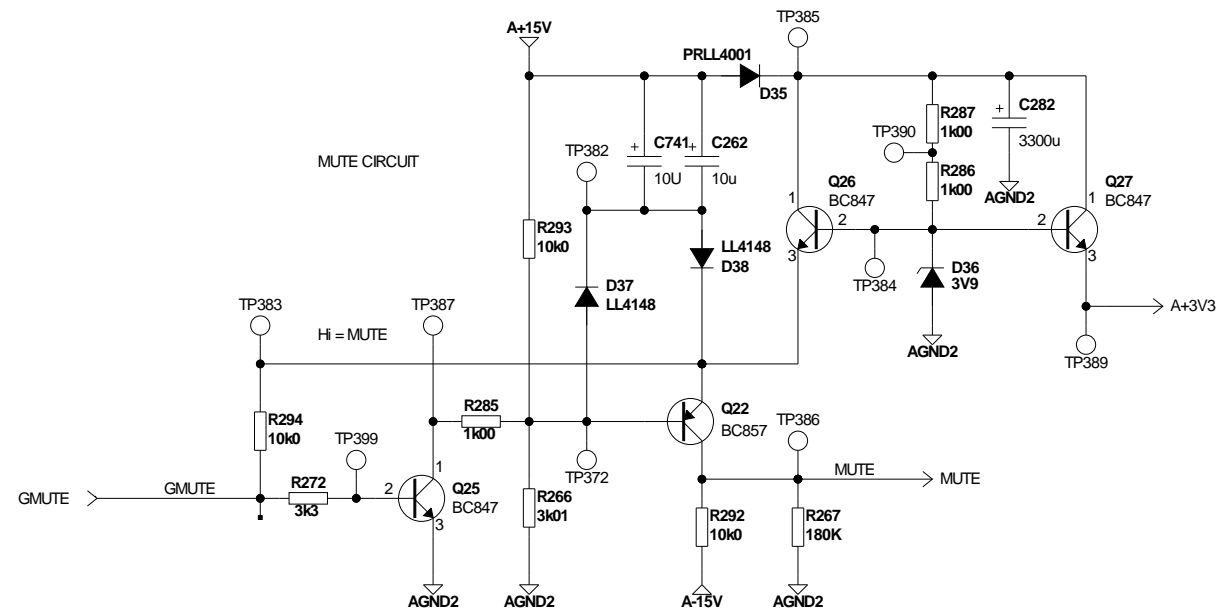
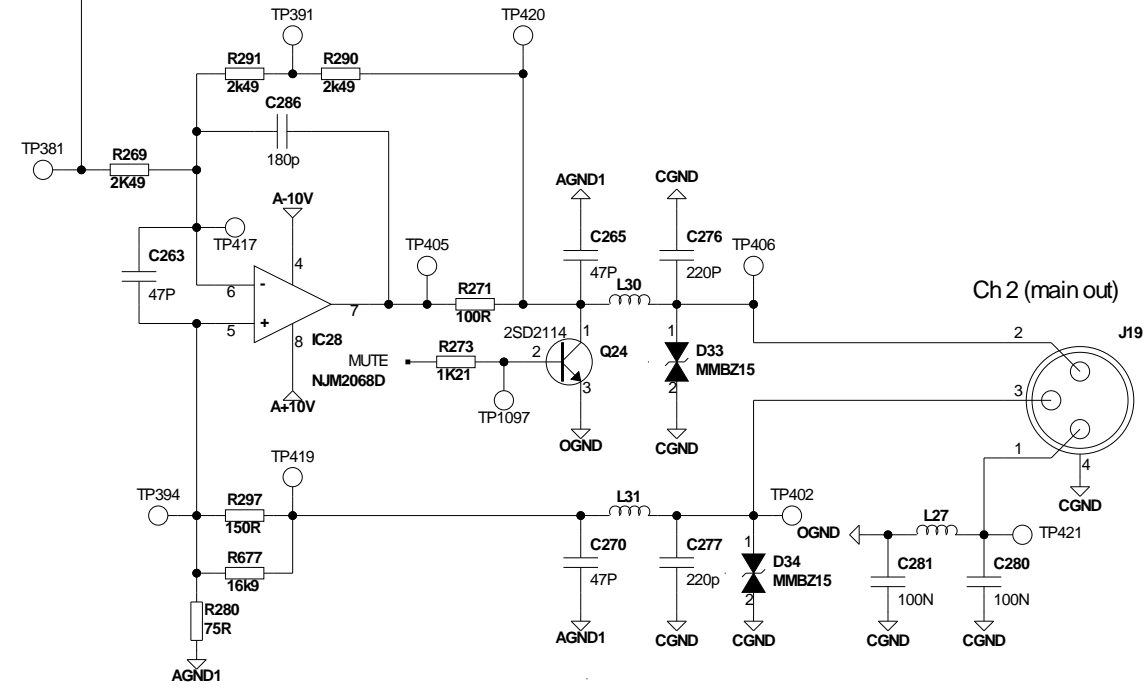
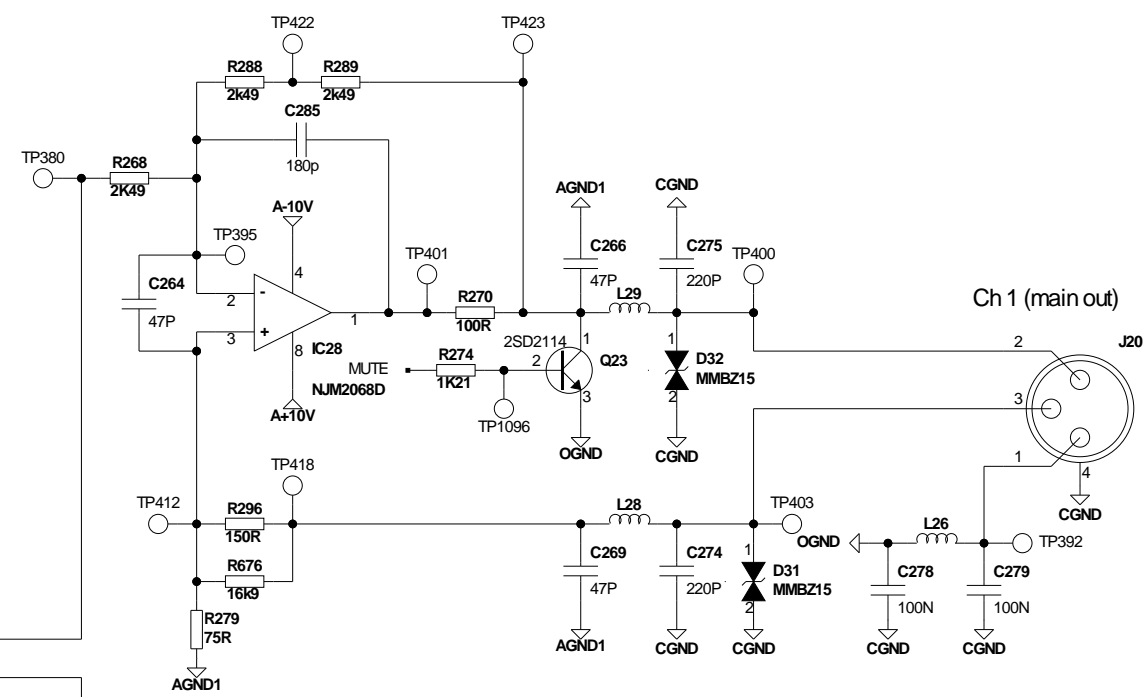
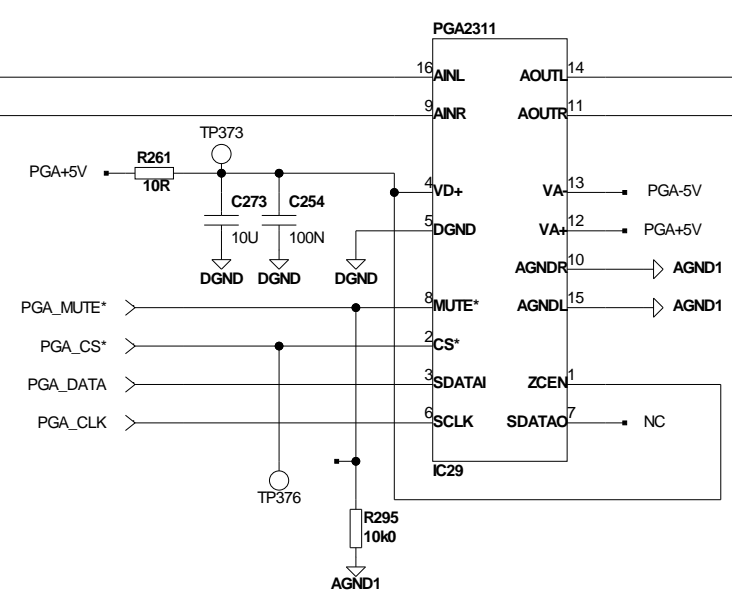
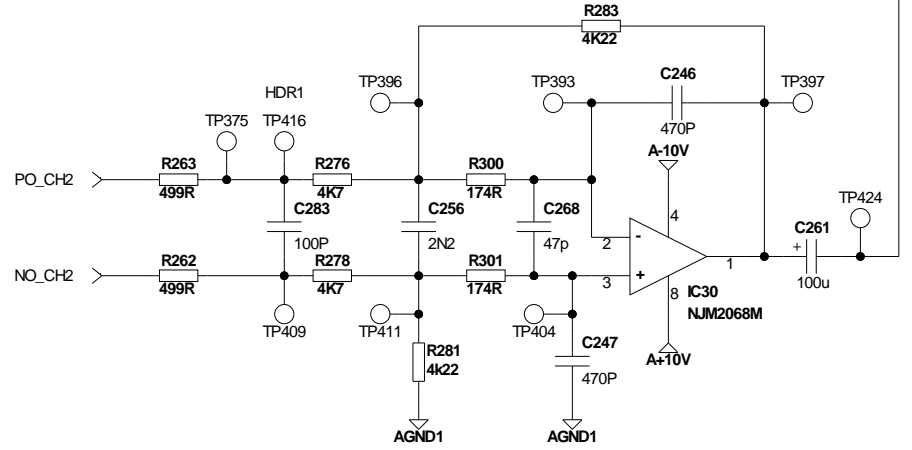
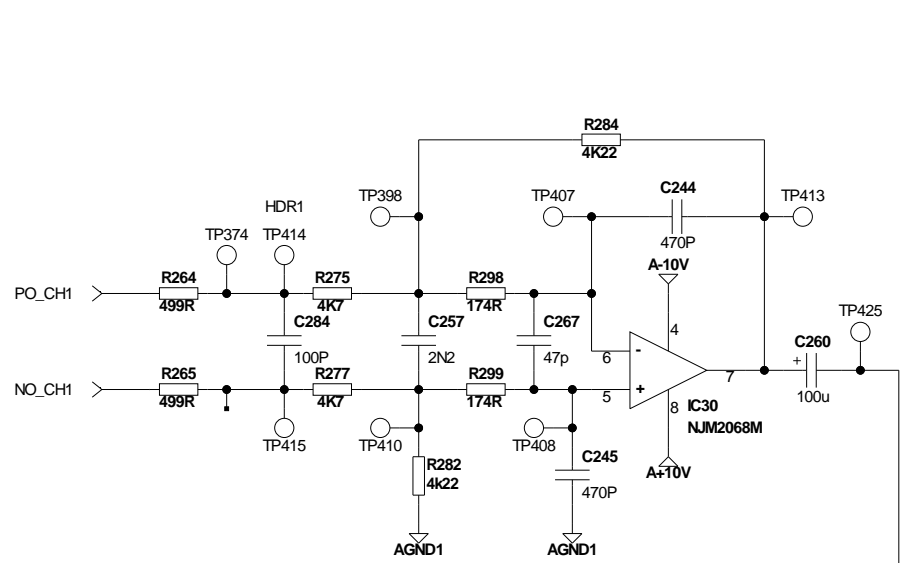
Note
 INA163 change to THAT?? = 370mW save. Will be changed when stock of INA163 is used



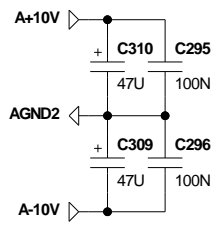
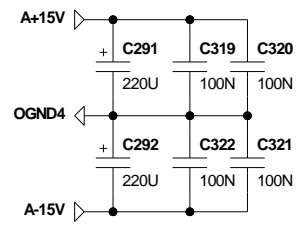
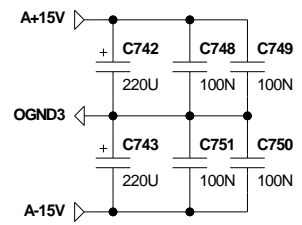
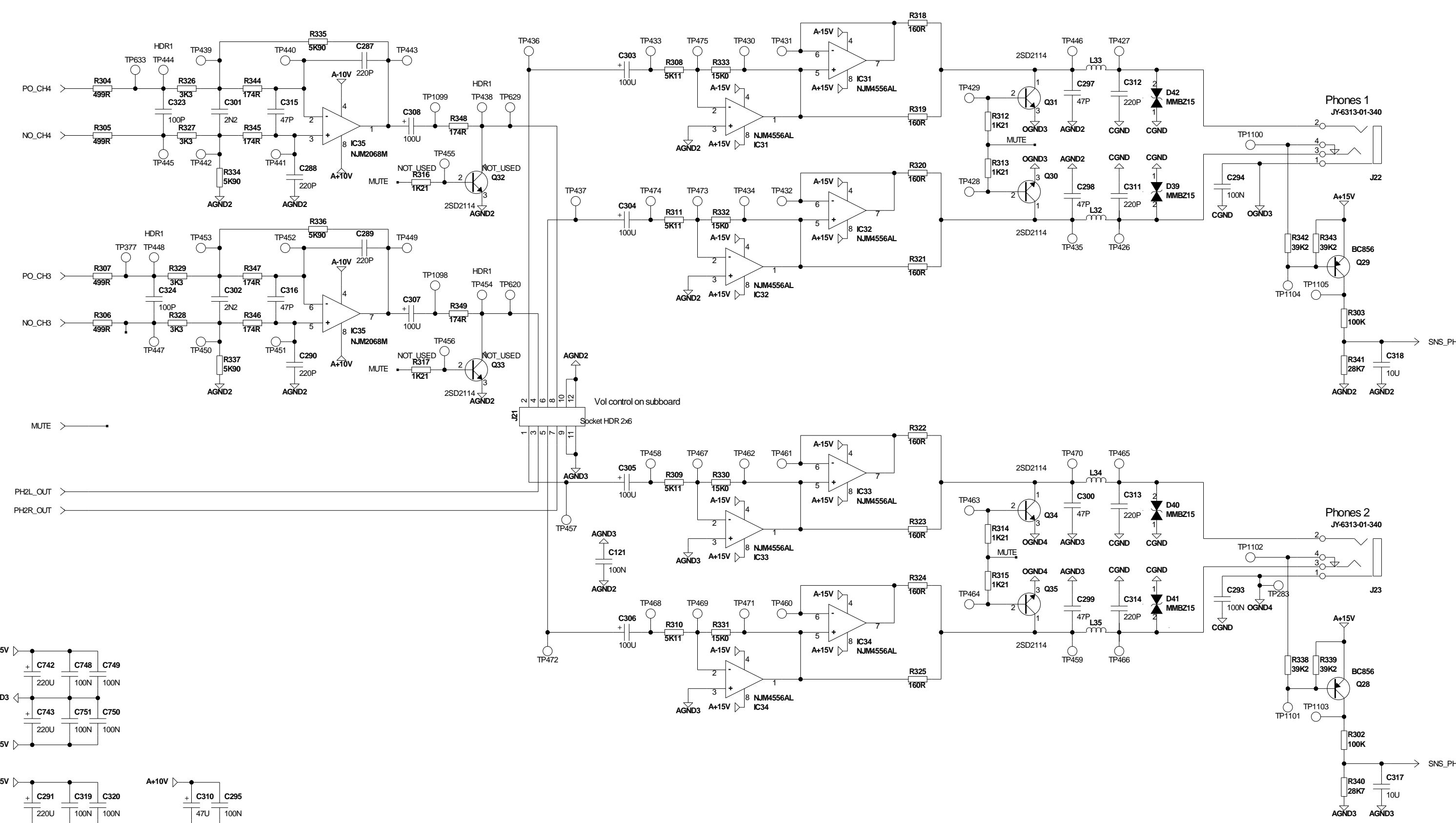
TC Electronic A/S		Designer
		MFA/LHO/CGF
Title	Module title	
EIO2	MIC INSTR CH 3-4	
Number	Revision	Previous page
P19321	CAD	10
Date	Filenam	Page / of
11-20-2007_12:38	MIC_IN_34	14 / 20



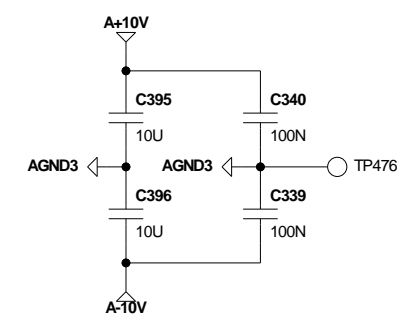
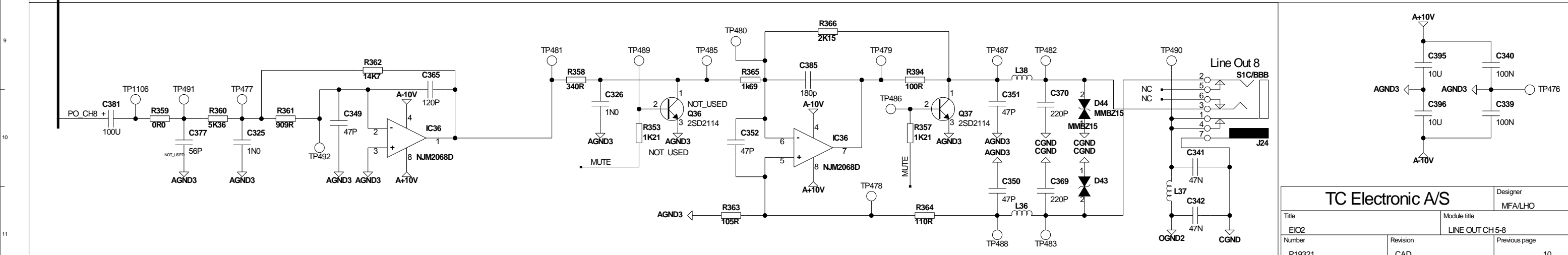
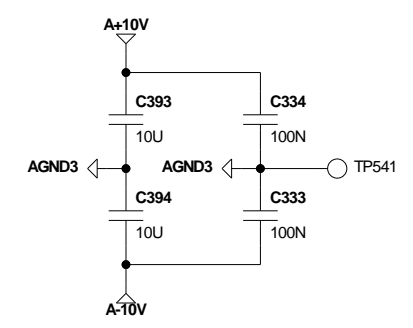
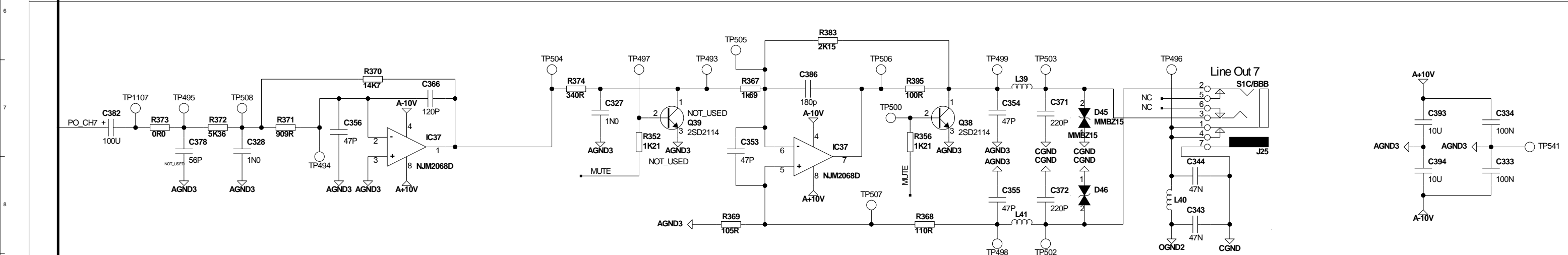
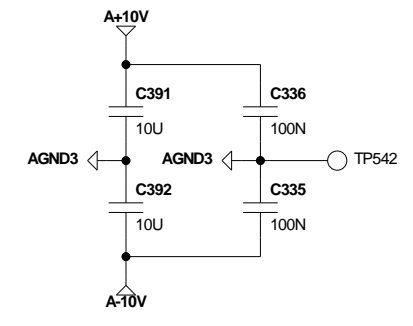
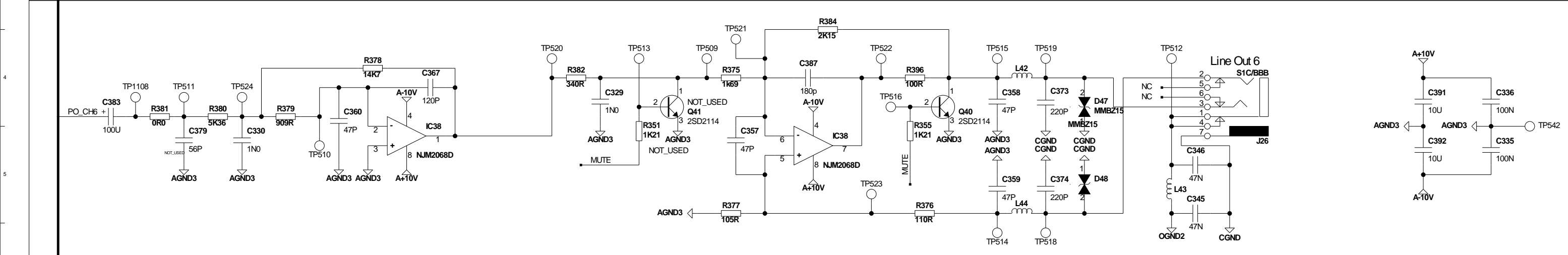
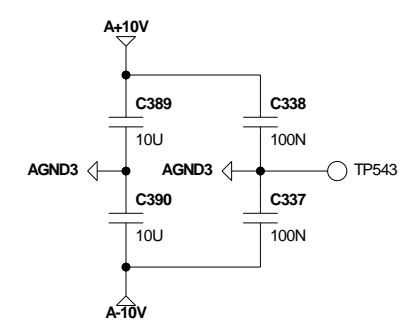
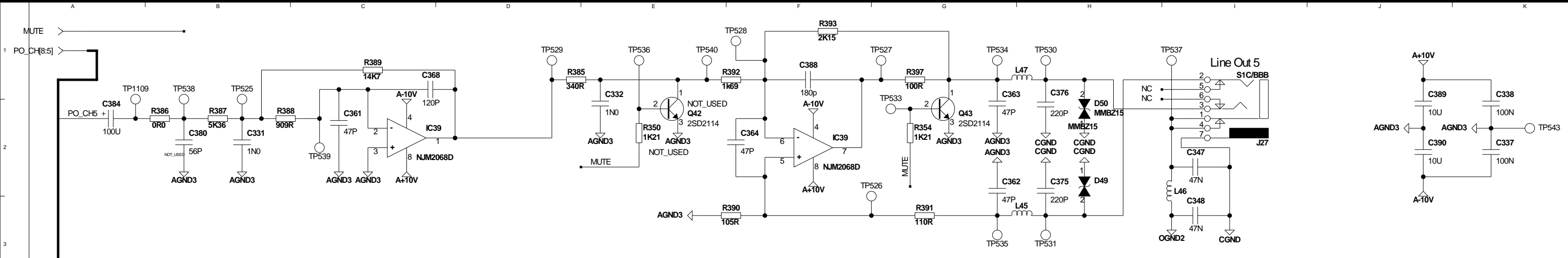
TC Electronic A/S		Designer MFA/LHO/CGF
Title EIO2	Module title AD/DA CONVERTER	
Number P19321	Revision CAD	Previous page 10
Date 11-20-2007_12:39	Filename CONVERTER	Page / of 15 / 20



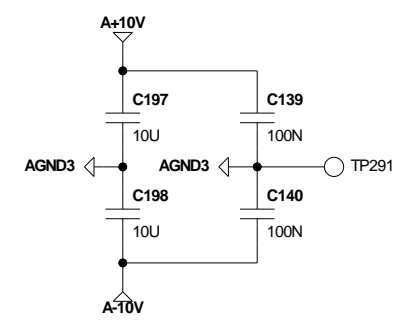
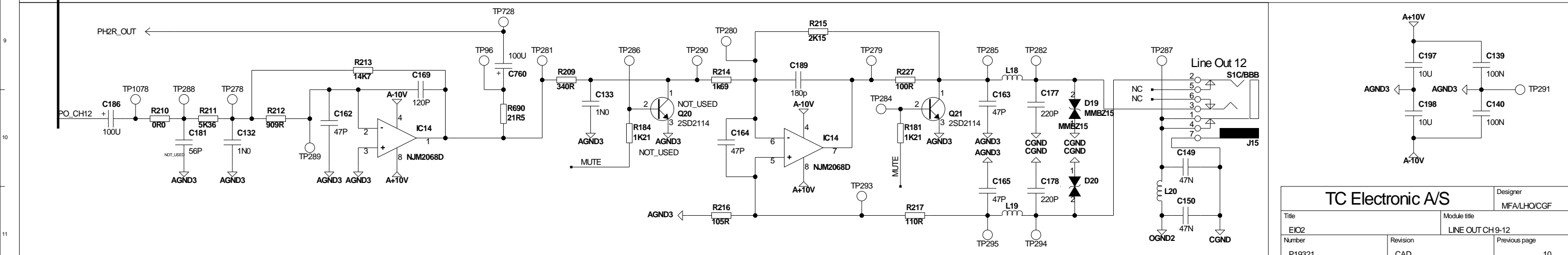
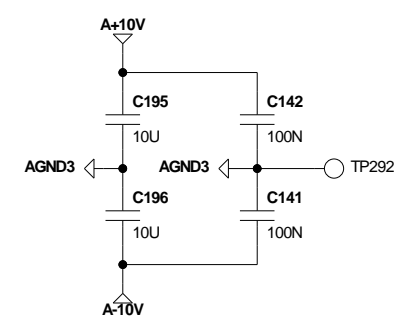
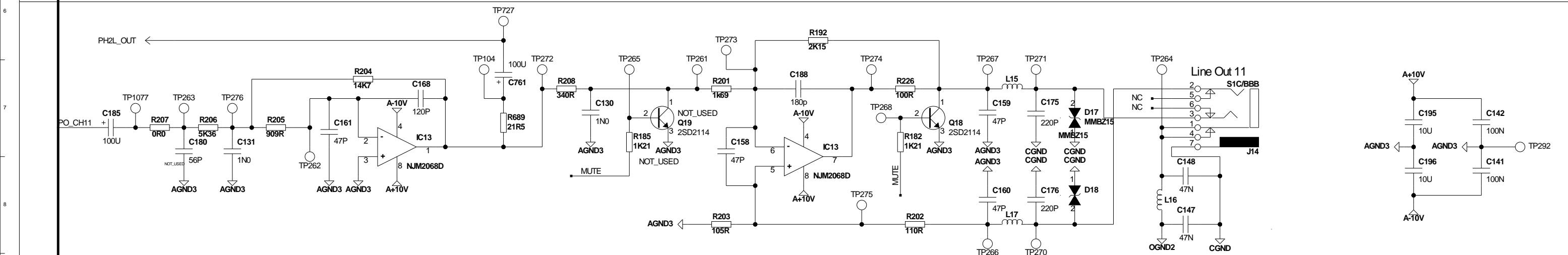
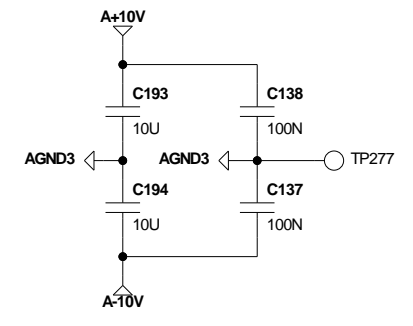
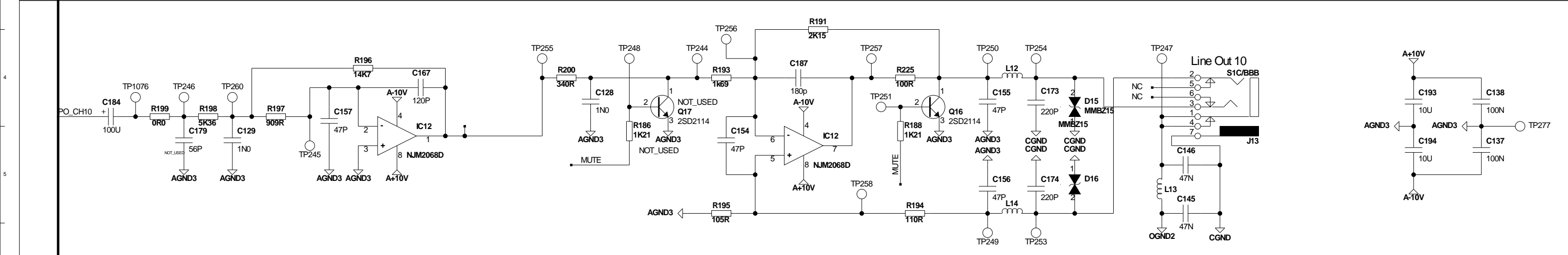
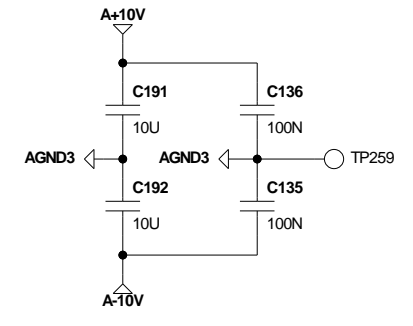
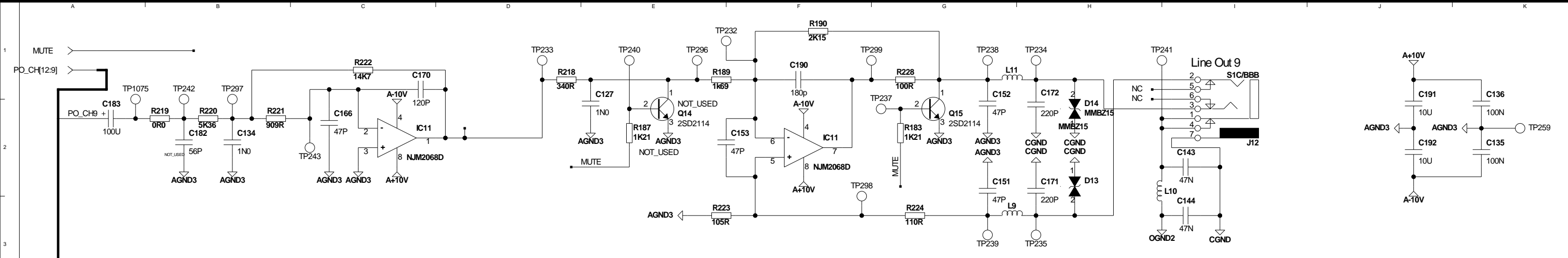
TC Electronic A/S		Designer MFA/LHO/CGF
Title EIO2	Module title MAIN OUTPUT	
Number P19321	Revision CAD	Previous page 10
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TC Electronic A/S		Designer MFA
Title EIQ2	Module title PHONES OUTPUT	
Number P19321	Revision CAD	Previous page 10
Date 11-20-2007_12:38	Filename PHONES_OUT	Page / of 17 / 20

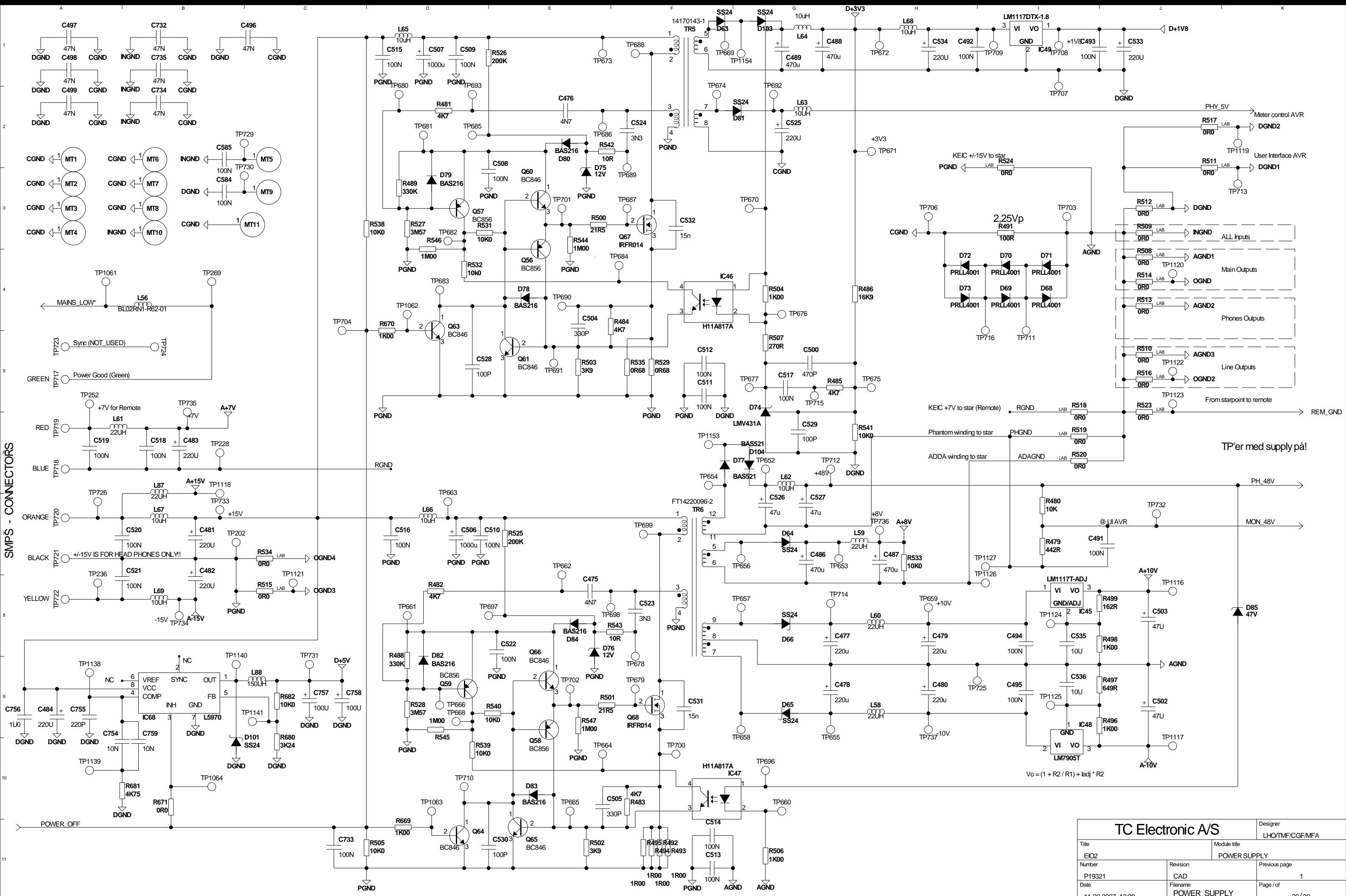


TC Electronic A/S		Designer MFA/LHO
Title EIO2	Module title LINE OUT CH 5-8	
Number P19321	Revision CAD	Previous page 10
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TC Electronic A/S		Designer MFA/LHO/CGF
Title EIO2	Module title LINE OUT CH 9-12	
Number P19321	Revision CAD	Previous page 10
Date 11-20-2007_12:39	Filename LINE_OUT_9_12	Page / of 19 / 20

SMP5 - CONNECTORS



TC Electronic A/S		Designer	LHO/TMF/CGF/MFA
Title	EIO2	Module title	POWER SUPPLY
Number	P19321	Revision	CAD
Date	11-20-2007_12:38	Filename	POWER_SUPPLY
		Previous page	1
		Page / of	20 / 20