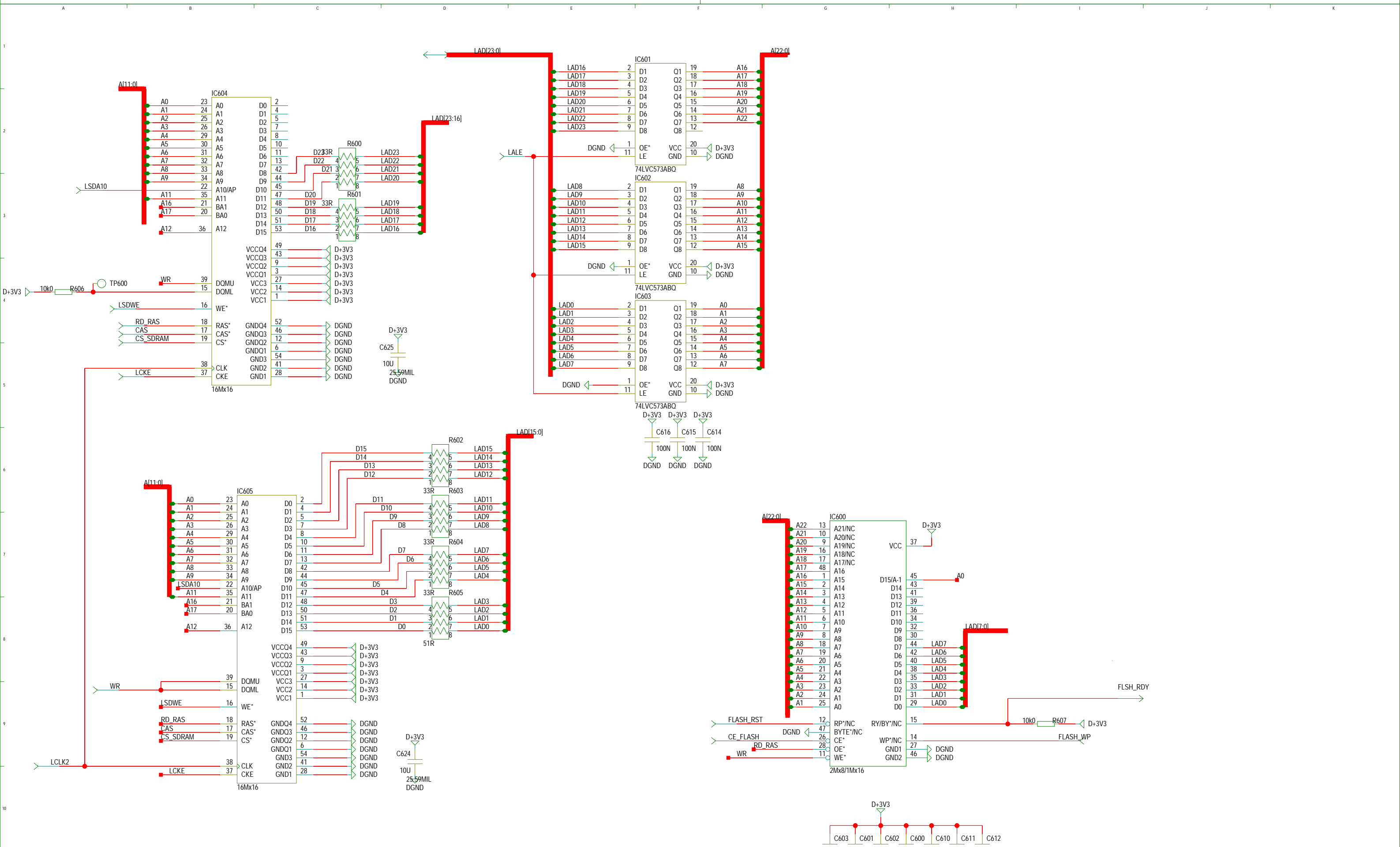
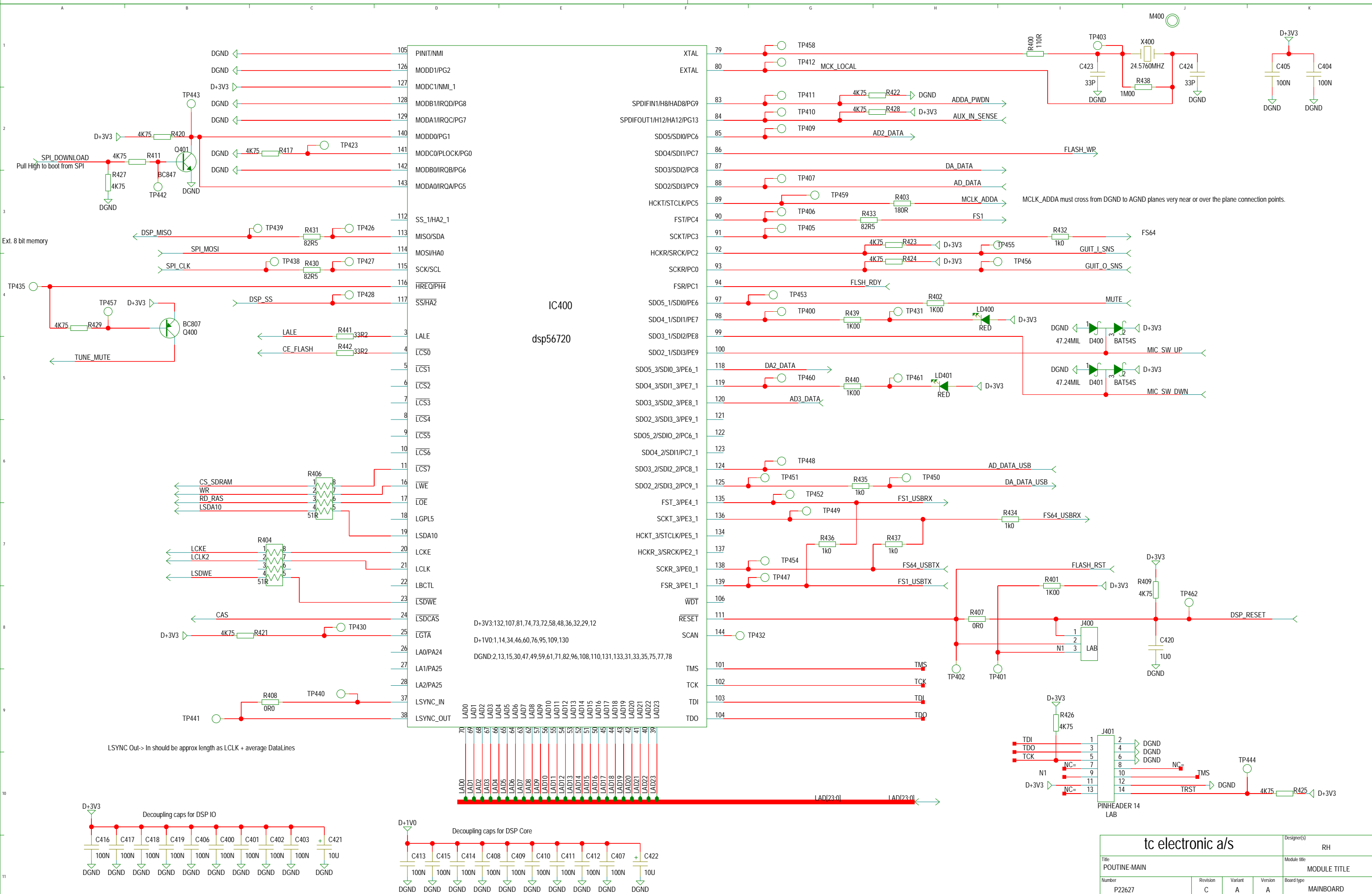


P22627-C
M1

tc electronic a/s				Designer(s) RH
Title POUTINE-MAIN				Module title MODULE TITLE
Number P22627	Revision C	Variant A	Version A	Board type MAINBOARD
Date 27-11-2012_16:30	Filename p22627-C			Page / of 1 / 8

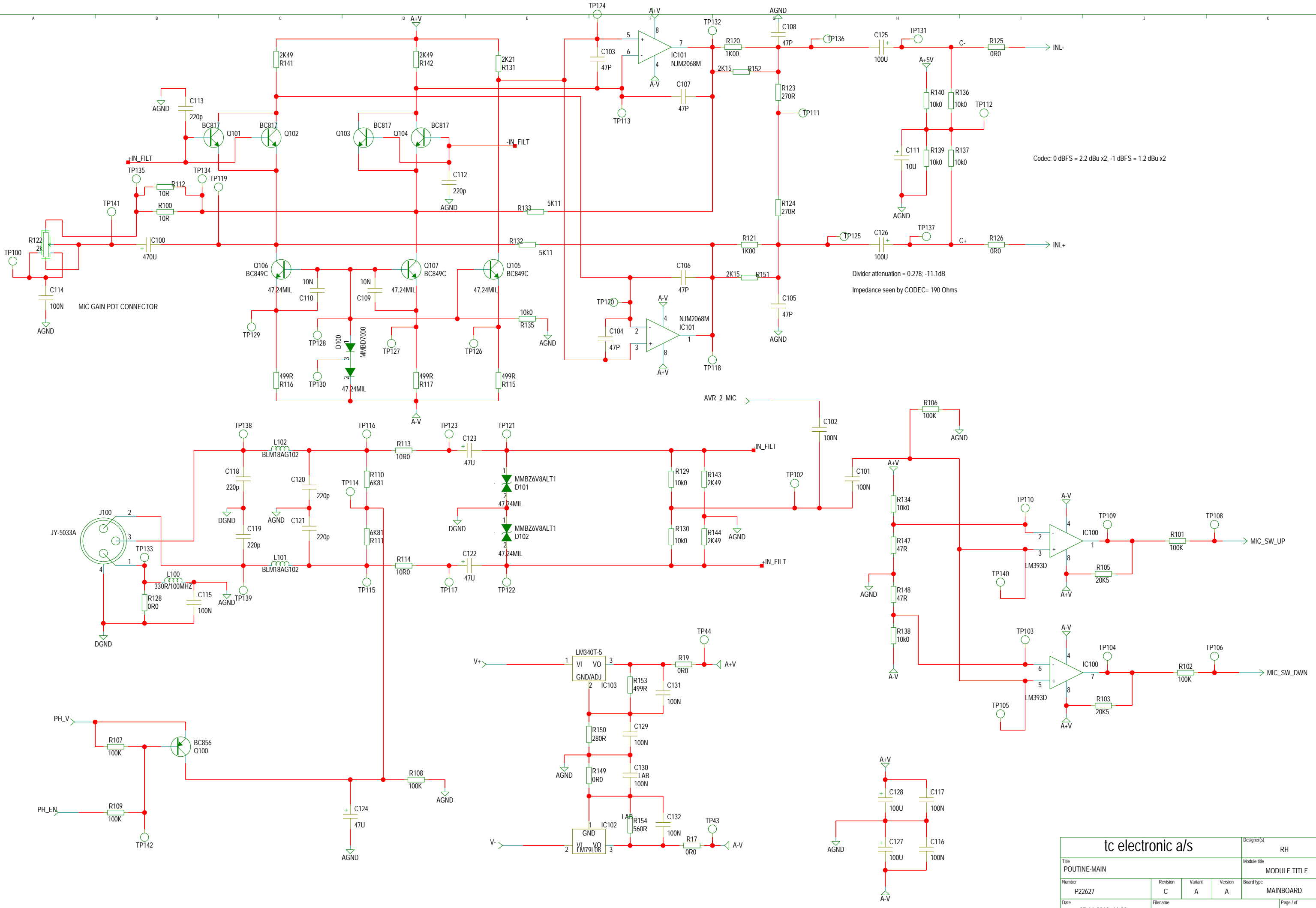




D+3V3: 132,107,81,74,73,72,58,48,36,32,29,12
 D+1V0: 1,14,34,46,60,76,95,109,130
 DGND: 2,13,15,30,47,49,59,61,71,82,96,108,110,131,133,31,33,35,75,77,78

LSYNC Out-> In should be approx length as LCLK + average DataLines

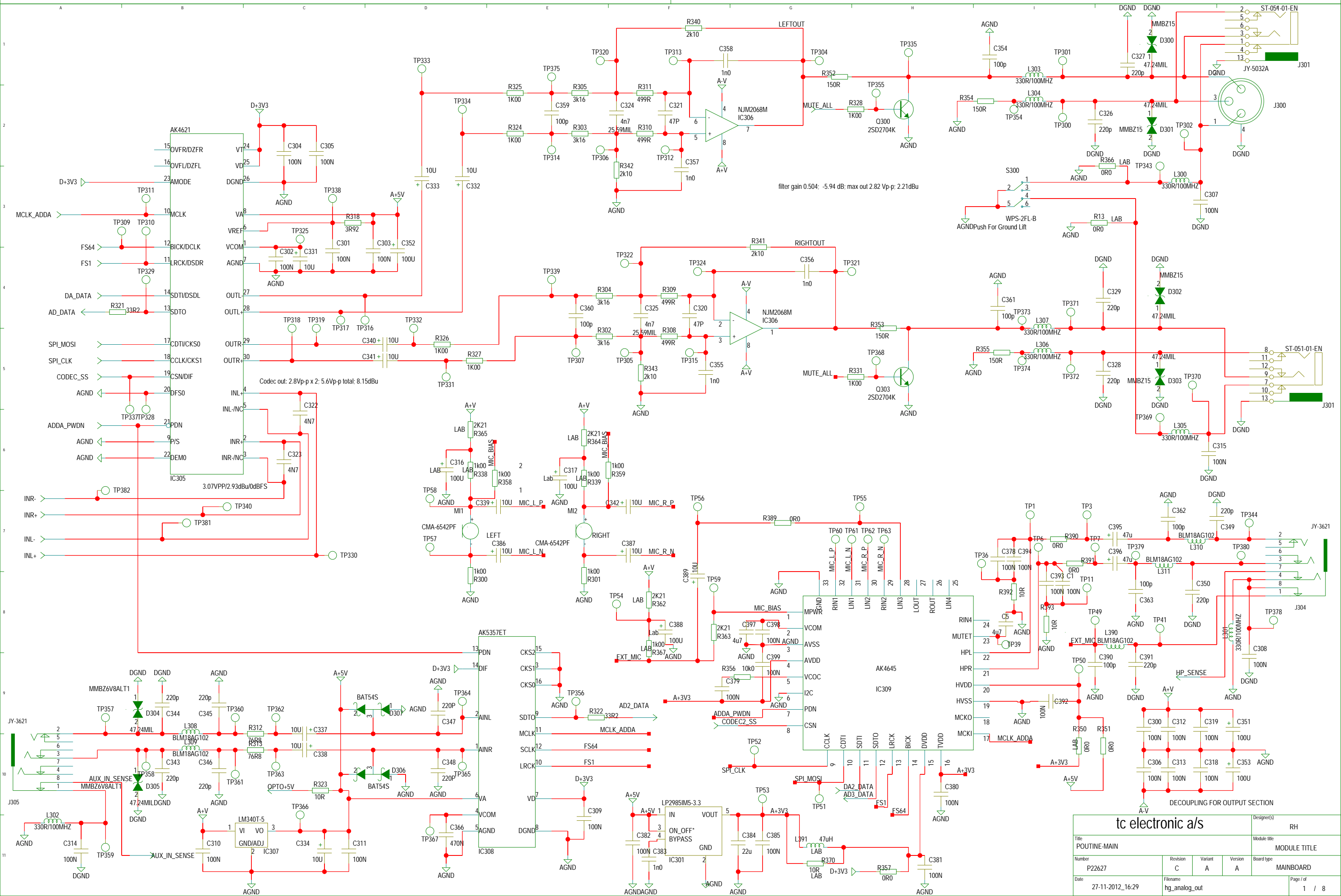
tc electronic a/s				Designer(s)	RH
Title POUTINE-MAIN				Module title MODULE TITLE	
Number P22627	Revision C	Variant A	Version A	Board type MAINBOARD	
Date 27-11-2012_16:29	Filename dsp720			Page / of 1 / 8	



Codec: 0 dBFS = 2.2 dBu x2, -1 dBFS = 1.2 dBu x2

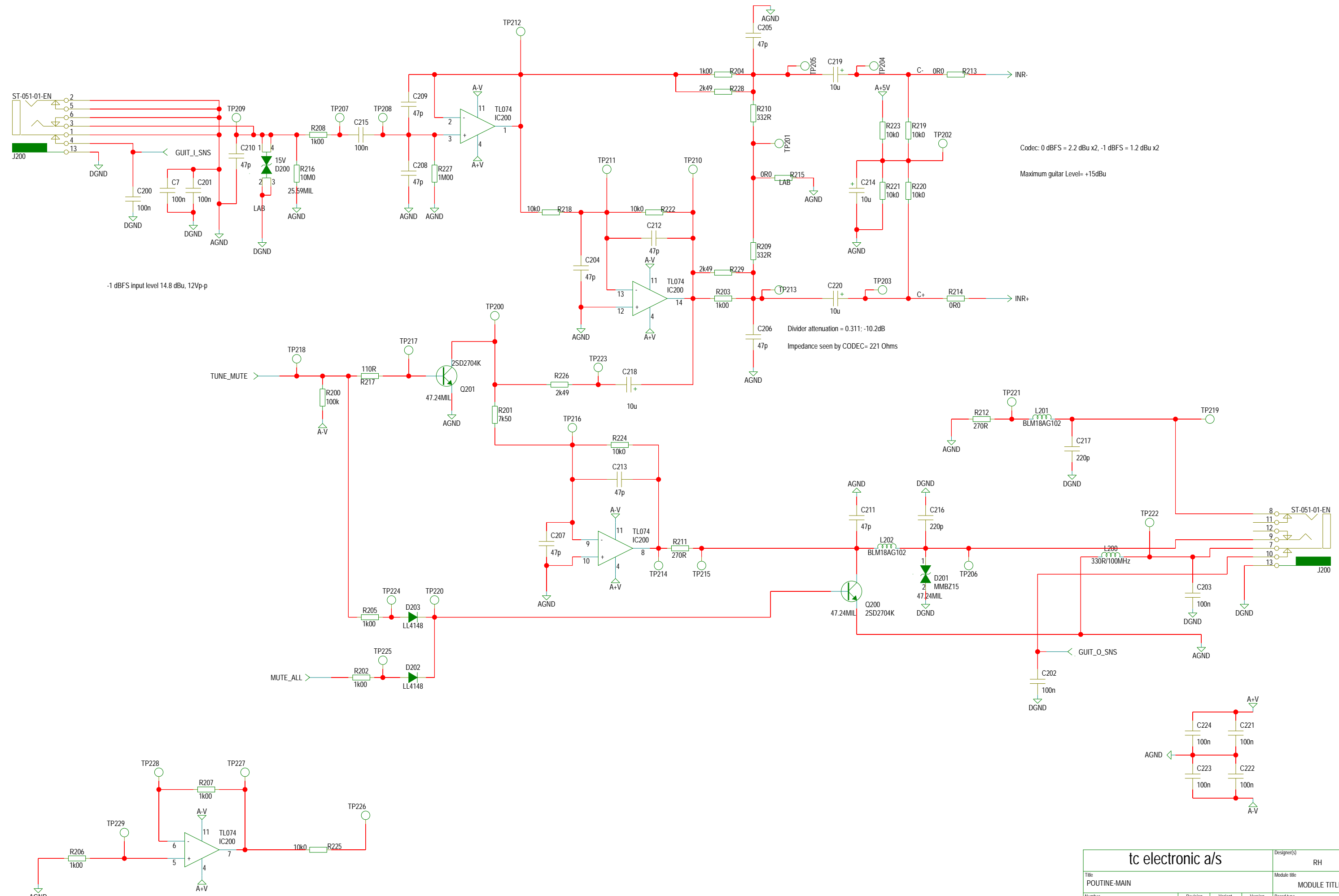
Divider attenuation = 0.278: -11.1dB
Impedance seen by CODEC = 190 Ohms

tc electronic a/s				Designer(s)	RH
Title POUTINE-MAIN				Module title MODULE TITLE	
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Date 27-11-2012_16:29	Filename g_mic_input			Page / of 1 / 8	



tc electronic a/s				Designer(s) RH
Title POUTINE-MAIN				Module title MODULE TITLE
Number P22627	Revision C	Variant A	Version A	Board type MAINBOARD
Date 27-11-2012_16:29	Filename hg_analog_out			Page / of 1 / 8

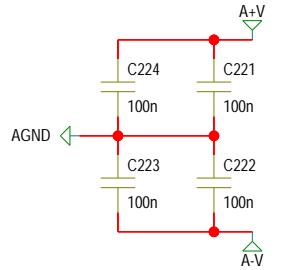
GUITAR INPUT



Codec: 0 dBFS = 2.2 dBu x2, -1 dBFS = 1.2 dBu x2
Maximum guitar Level= +15dBu

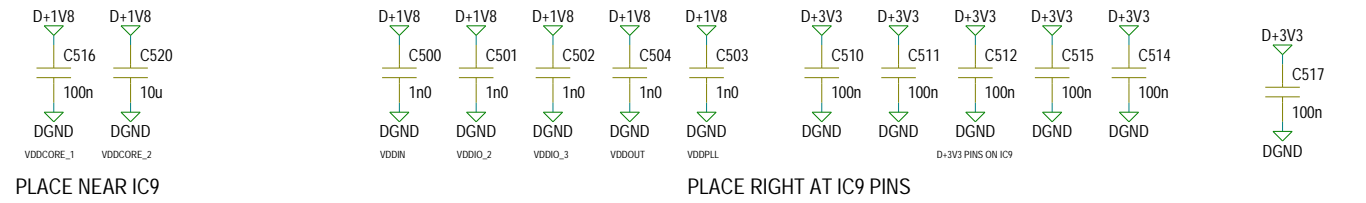
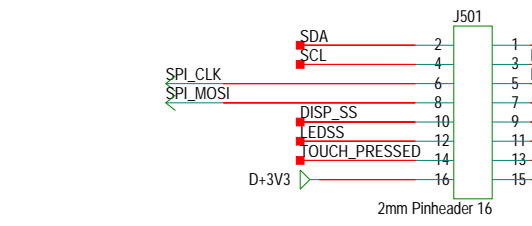
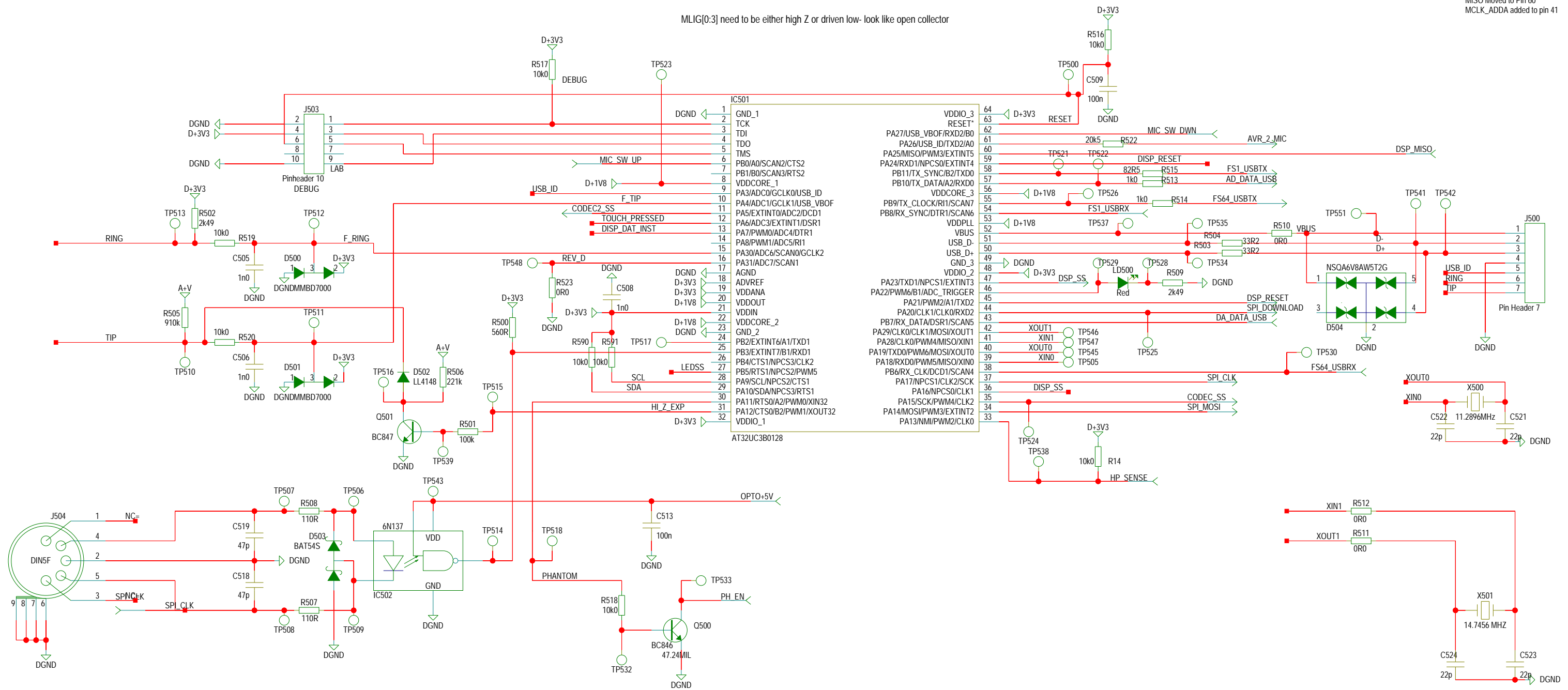
-1 dBFS input level 14.8 dBu, 12Vp-p

Divider attenuation = 0.311: -10.2dB
Impedance seen by CODEC= 221 Ohms

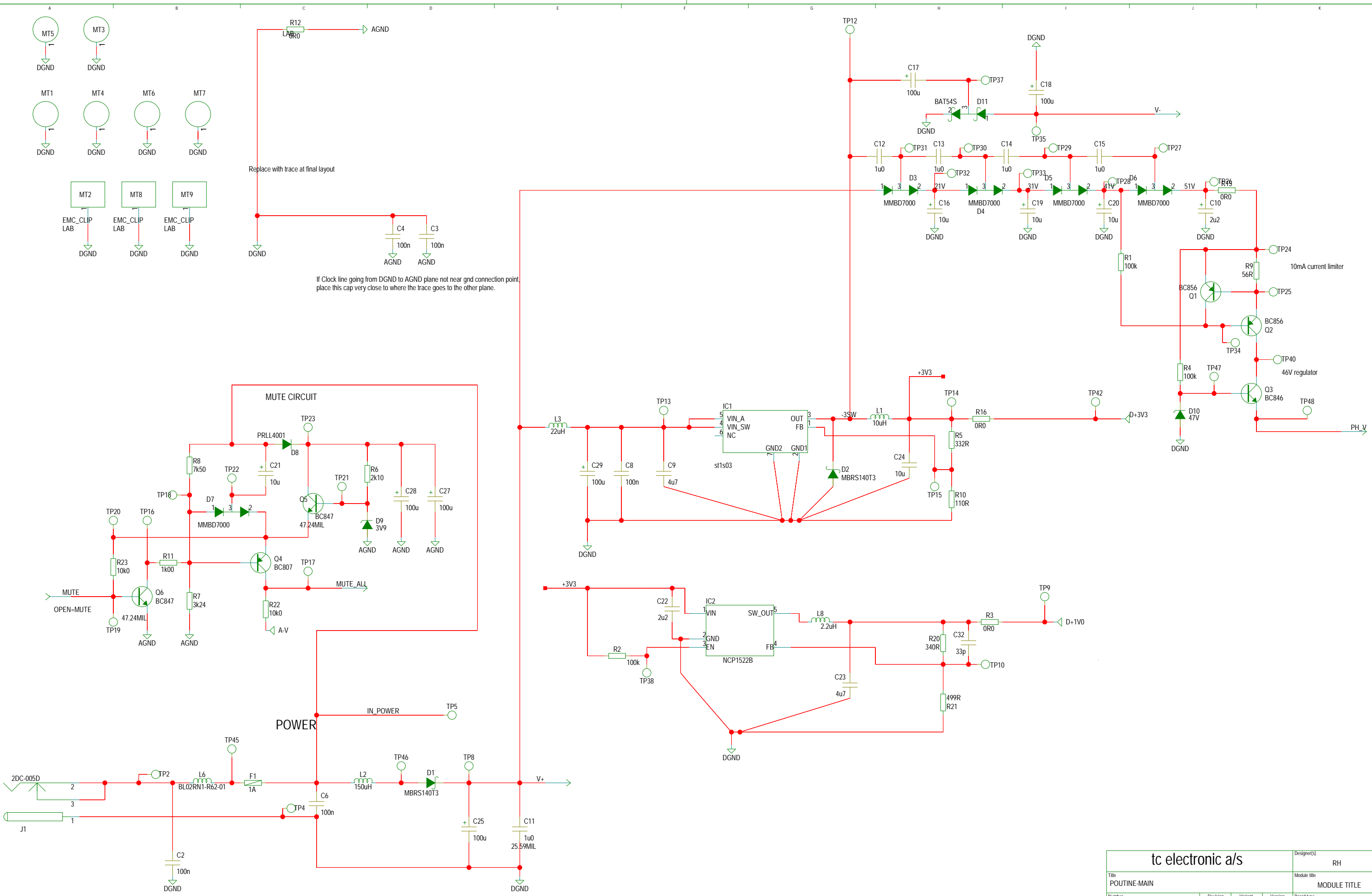


tc electronic a/s				Designer(s)	RH
Title POUTINE-MAIN				Module title MODULE TITLE	
Number P22627	Revision C	Variant A	Version A	Board type MAINBOARD	
Date 27-11-2012_16:29	Filename guitar_input			Page / of 1 / 8	

MLIG[0:3] need to be either high Z or driven low- look like open collector



tc electronic a/s				Designer(s) RH
Title POUTINE-MAIN				Module title MODULE TITLE
Number P22627	Revision C	Variant A	Version A	Board type MAINBOARD
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Replace with trace at final layout

If Clock line going from DGND to AGND plane not near gnd connection point, place this cap very close to where the trace goes to the other plane.

tc electronic a/s				Designer(s)	RH
Title POUTINE-MAIN				Module title MODULE TITLE	
Number P22627	Revision C	Variant A	Version A	Board type MAINBOARD	
Date 27-11-2012_16:30	Filename interface			Page / of 1 / 8	