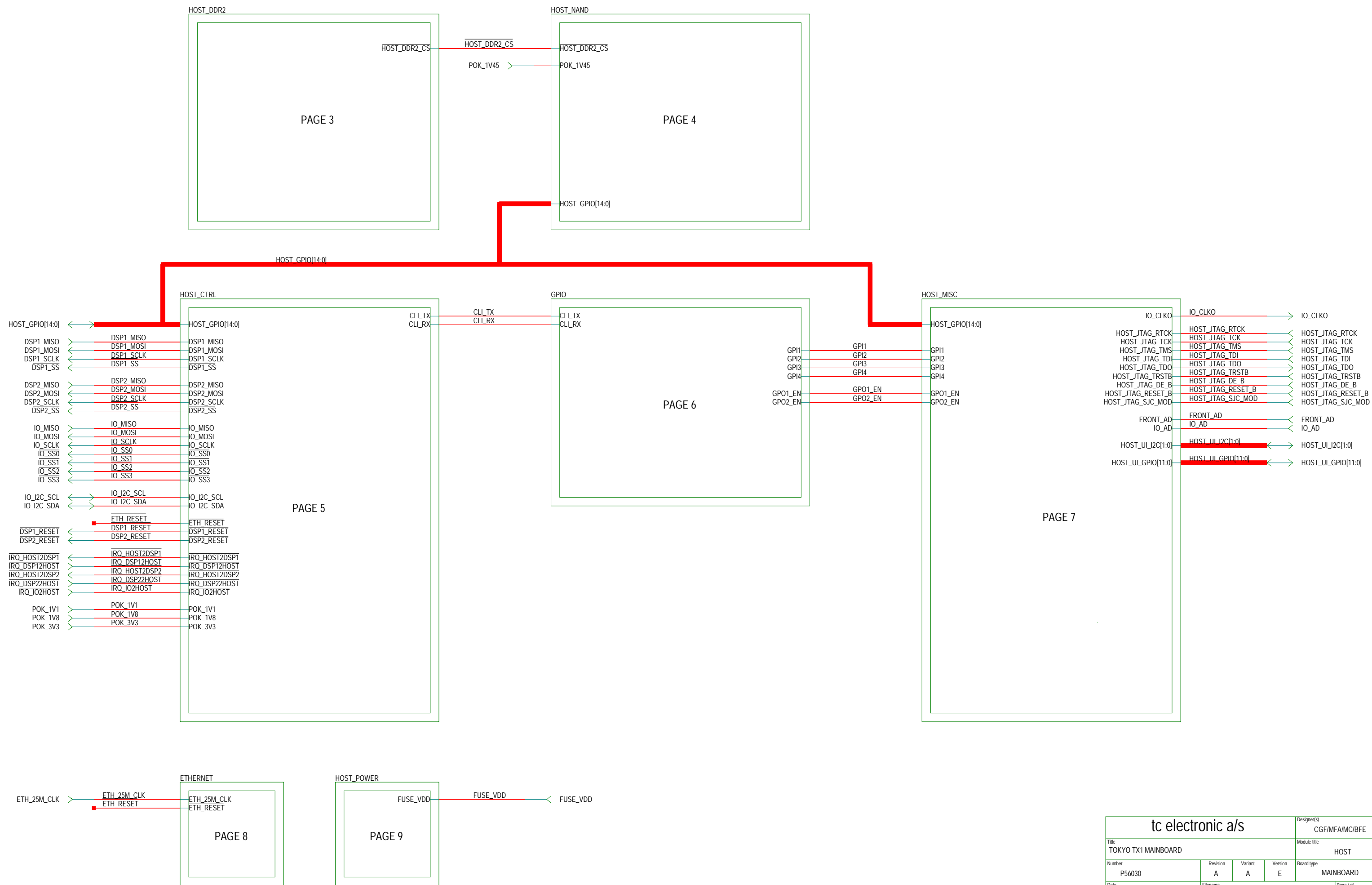
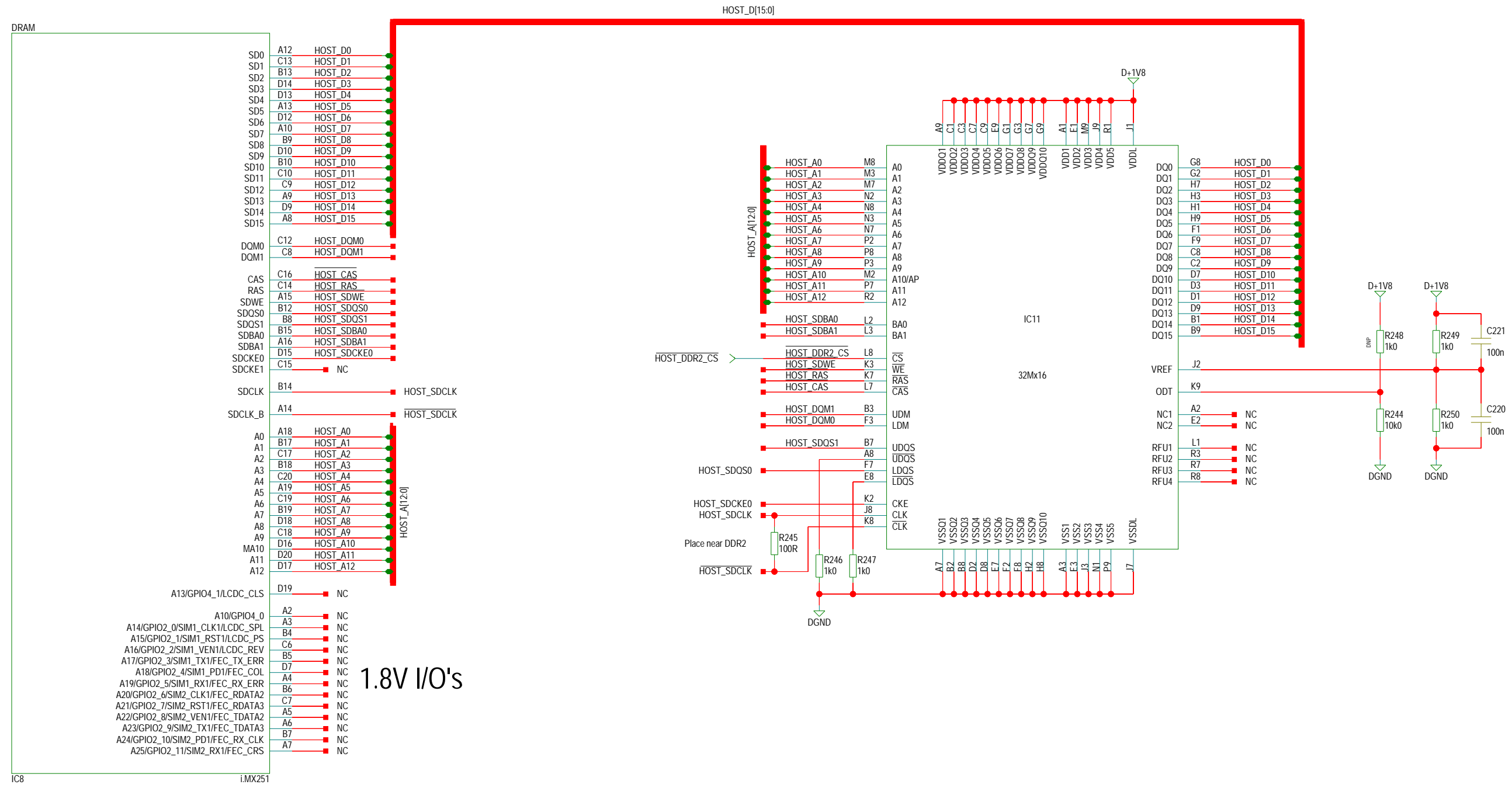


M1  
P56030-A

tc electronic a/s				Designer(s) CGF/MFA/MC/BFE
Title TOKYO TX1 MAINBOARD				Module title TOP
Number P56030	Revision A	Variant A	Version E	Board type MAINBOARD
Date 10-09-2012_14:02	Filename P56030-A	Page / of 1 / 20		



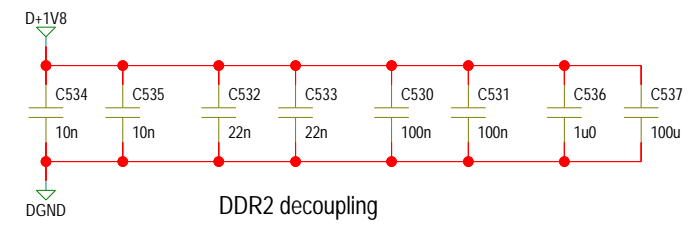
<b>tc electronic a/s</b>				Designer(s) CGF/MFA/MC/BFE	
Title TOKYO TX1 MAINBOARD				Module title HOST	
Number P56030	Revision A	Variant A	Version E	Board type MAINBOARD	
Date 10-09-2012_14:03	Filename HOST			Page / of 2 / 20	

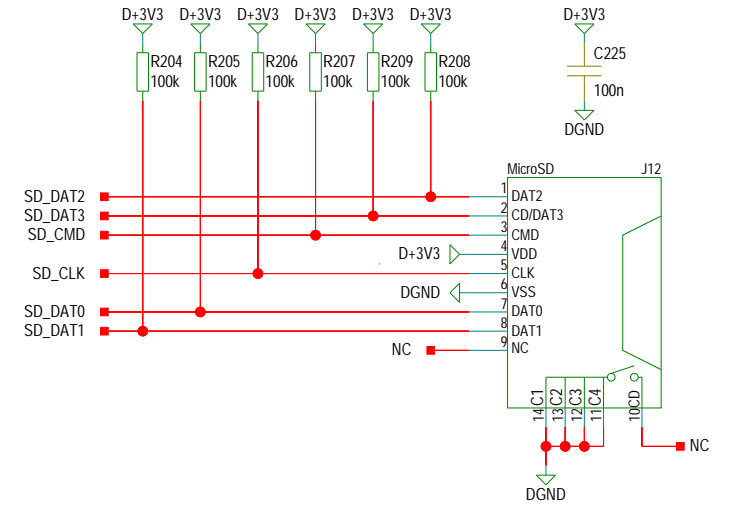
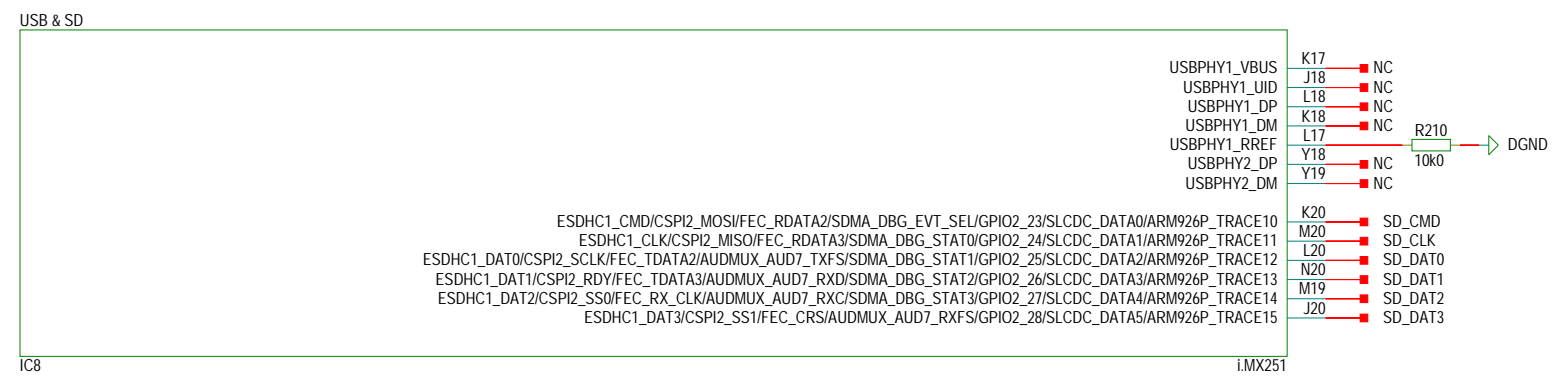
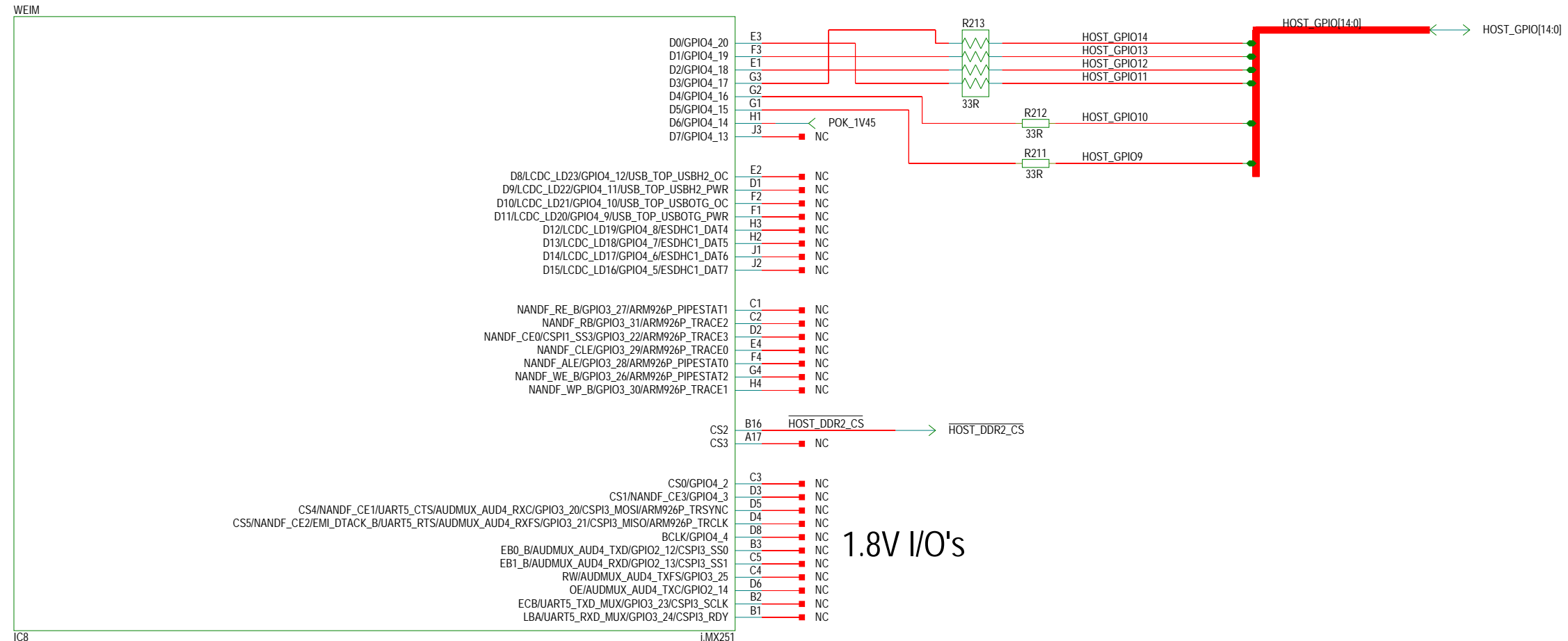


IC8 I.MX251

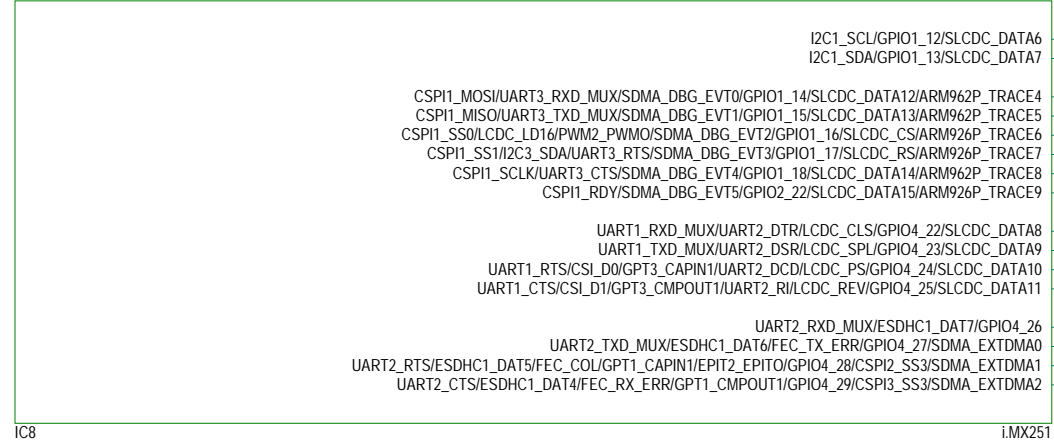
SD0	A12	HOST_D0
SD1	C13	HOST_D1
SD2	B13	HOST_D2
SD3	D14	HOST_D3
SD4	D13	HOST_D4
SD5	A13	HOST_D5
SD6	D12	HOST_D6
SD7	A10	HOST_D7
SD8	B9	HOST_D8
SD9	D10	HOST_D9
SD10	B10	HOST_D10
SD11	C10	HOST_D11
SD12	C9	HOST_D12
SD13	A9	HOST_D13
SD14	D9	HOST_D14
SD15	A8	HOST_D15
DOM0	C12	HOST_DQM0
DOM1	C8	HOST_DQM1
CAS	C16	HOST_CAS
RAS	C14	HOST_RAS
SDWE	A15	HOST_SDWE
SDQS0	B12	HOST_SDQS0
SDQS1	B8	HOST_SDQS1
SDBA0	B15	HOST_SDBA0
SDBA1	A16	HOST_SDBA1
SDCKE0	D15	HOST_SDCKE0
SDCKE1	C15	NC
SDCLK	B14	HOST_SDCLK
SDCLK_B	A14	HOST_SDCLK
A0	A18	HOST_A0
A1	B17	HOST_A1
A2	C17	HOST_A2
A3	B18	HOST_A3
A4	C20	HOST_A4
A5	A19	HOST_A5
A6	C19	HOST_A6
A7	B19	HOST_A7
A8	D18	HOST_A8
A9	C18	HOST_A9
MA10	D16	HOST_A10
A11	D20	HOST_A11
A12	D17	HOST_A12
A13/GPIO4_1/LCDC_CLS	D19	NC
A10/GPIO4_0	A2	NC
A14/GPIO2_0/SIM1_CLK1/LCDC_SPL	A3	NC
A15/GPIO2_1/SIM1_RST1/LCDC_PS	B4	NC
A16/GPIO2_2/SIM1_VEN1/LCDC_REV	C6	NC
A17/GPIO2_3/SIM1_TX1/FEC_TX_ERR	B5	NC
A18/GPIO2_4/SIM1_PD1/FEC_COL	D7	NC
A19/GPIO2_5/SIM1_RX1/FEC_RX_ERR	A4	NC
A20/GPIO2_6/SIM2_CLK1/FEC_RDATA2	B6	NC
A21/GPIO2_7/SIM2_RST1/FEC_RDATA3	C7	NC
A22/GPIO2_8/SIM2_VEN1/FEC_TDATA2	A5	NC
A23/GPIO2_9/SIM2_TX1/FEC_TDATA3	A6	NC
A24/GPIO2_10/SIM2_PD1/FEC_RX_CLK	B7	NC
A25/GPIO2_11/SIM2_RX1/FEC_CRS	A7	NC

1.8V I/O's

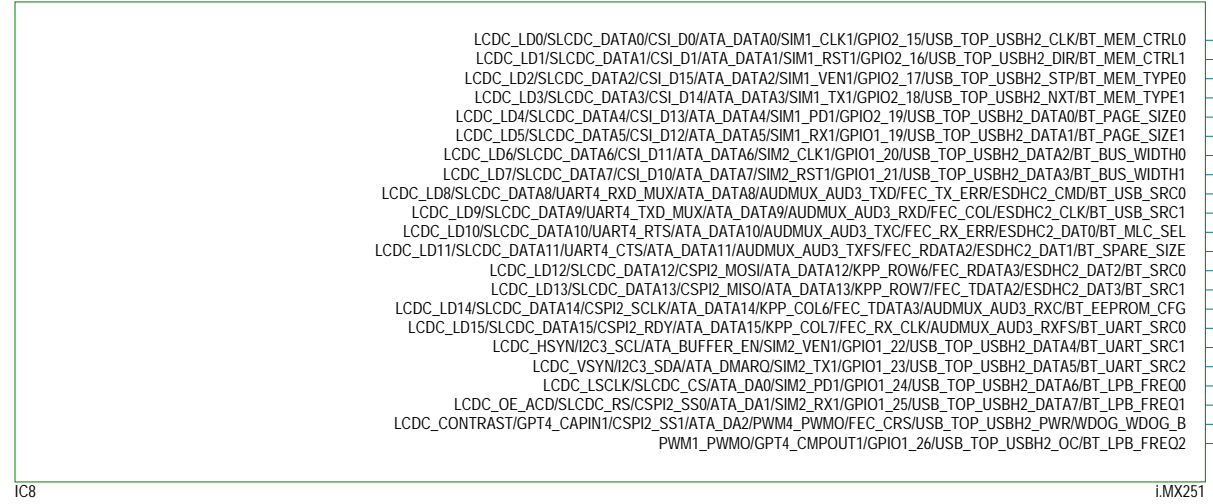




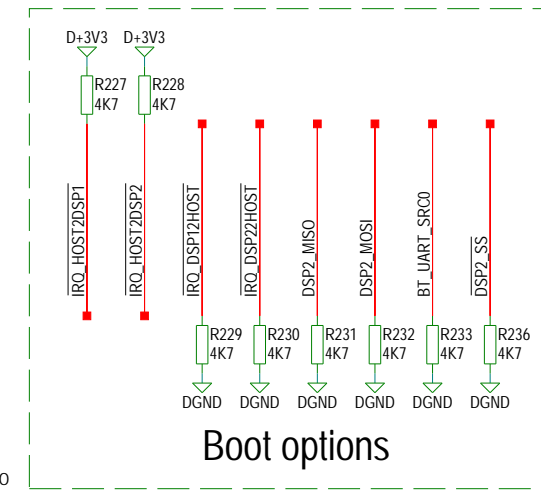
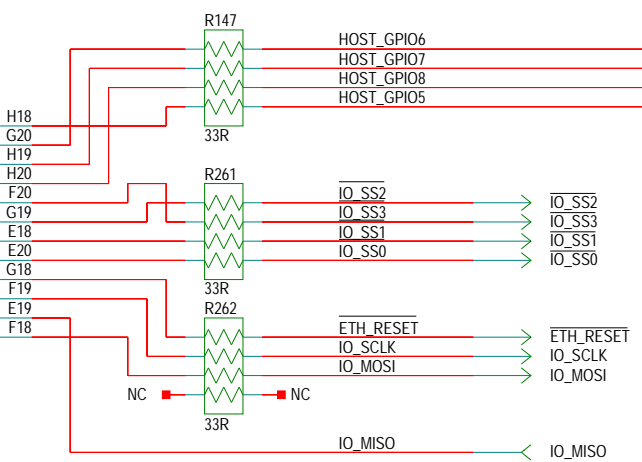
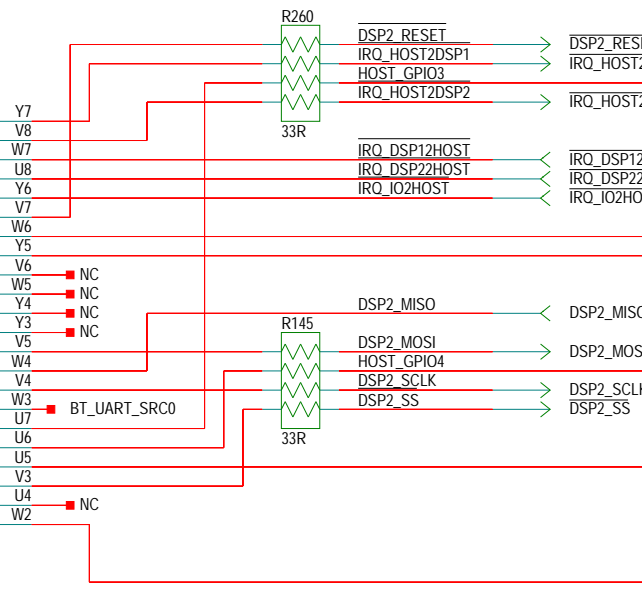
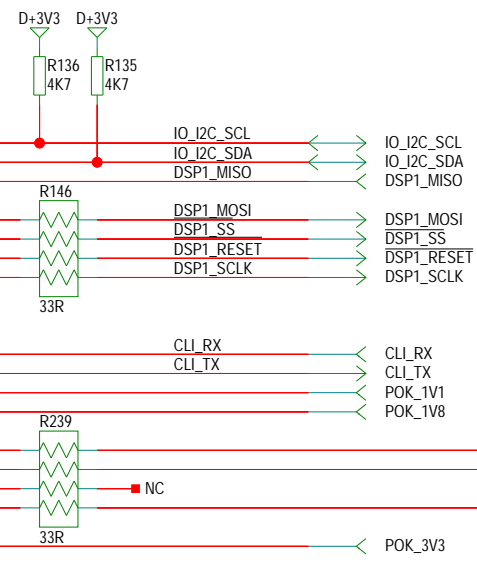
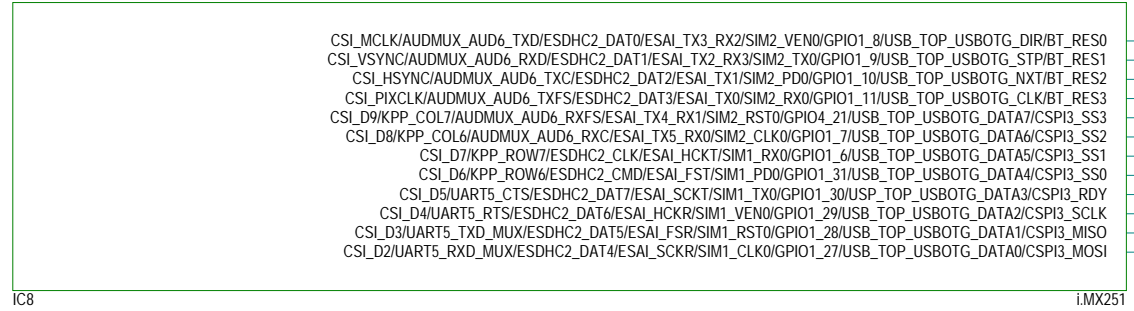
UART & SPI & I2C



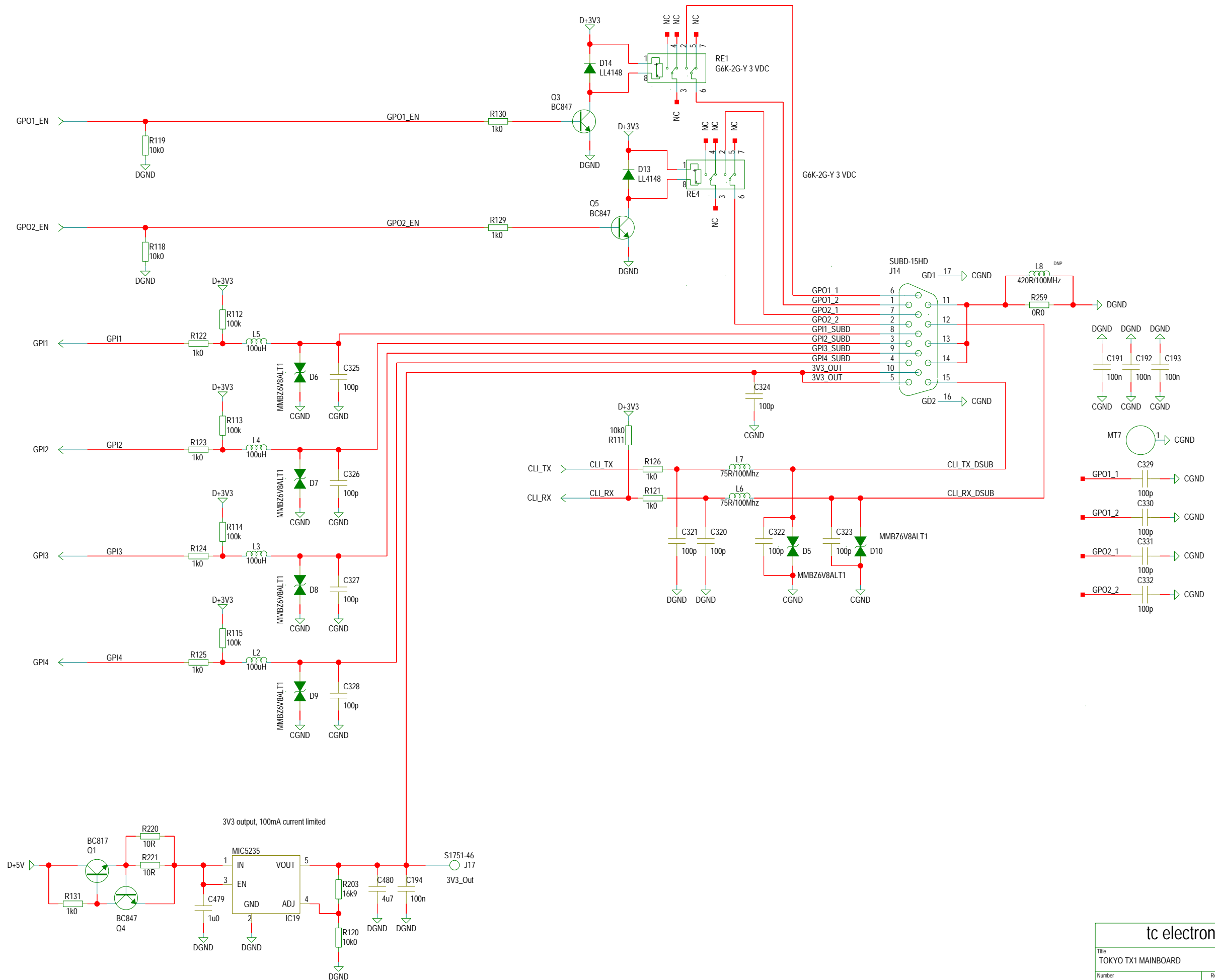
IPU



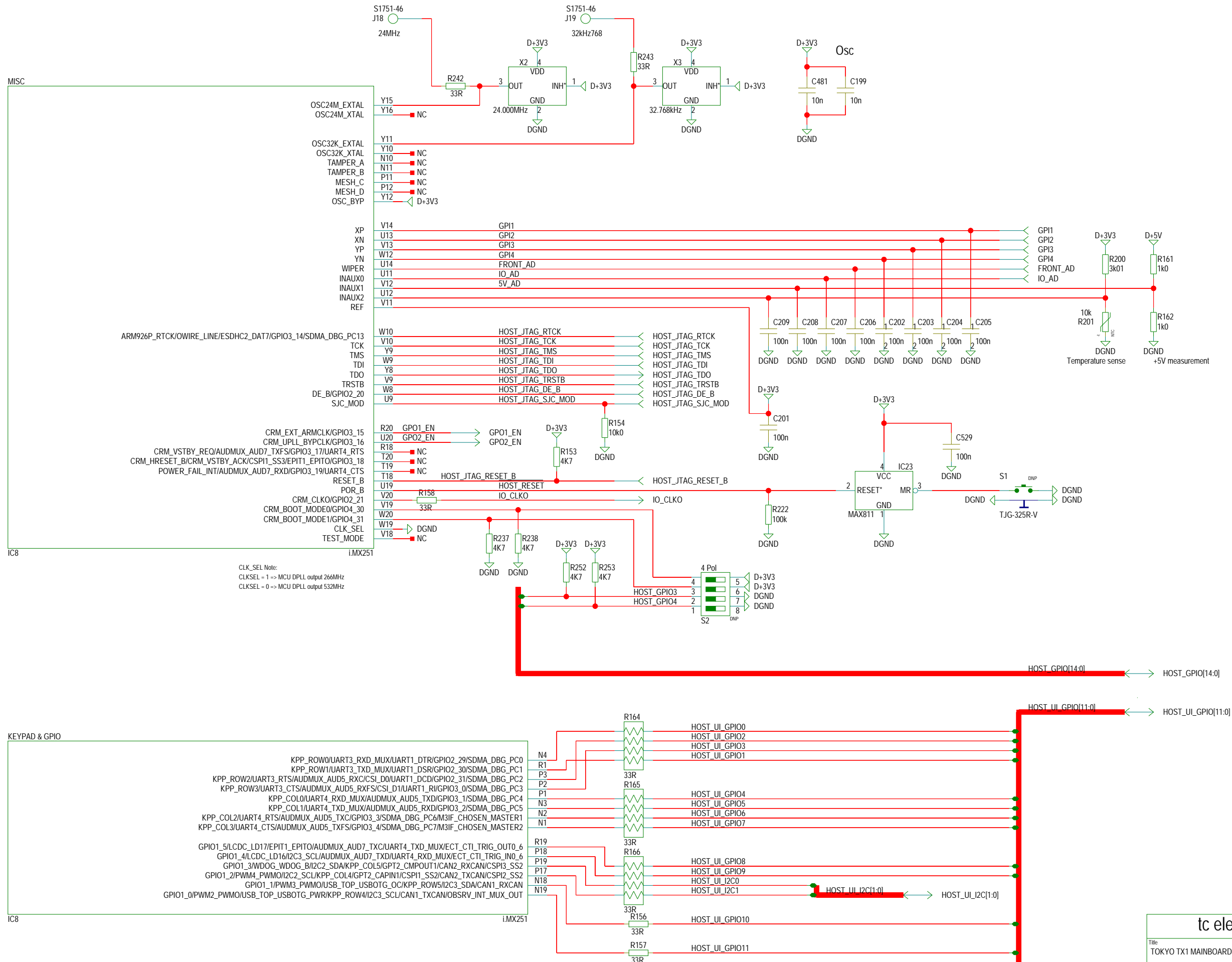
CSI



tc electronic a/s				Designer(s) CGF/MFA/MC/BFE
Title TOKYO TX1 MAINBOARD				Module title HOST CTRL
Number P56030	Revision A	Variant A	Version E	Board type MAINBOARD
Date 10-09-2012_14:03	Filename HOST_CTRL			Page / of 5 / 20

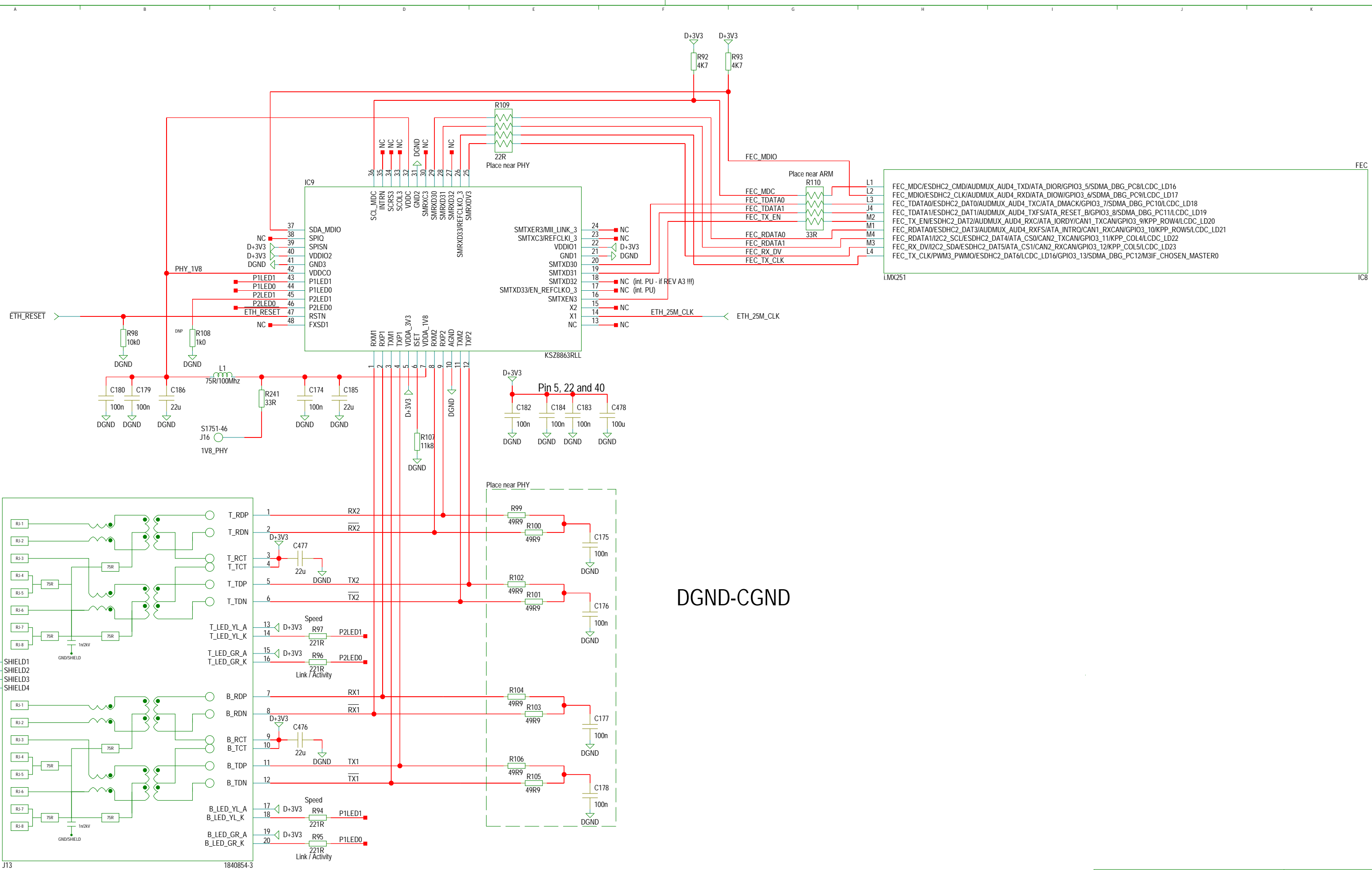


tc electronic a/s				Designer(s) CGF/MFA/MC/BFE
Title TOKYO TX1 MAINBOARD				Module title EXTERNAL GPIO
Number P56030	Revision A	Variant A	Version E	Board type MAINBOARD
Date 10-09-2012_14:03	Filename GPIO	Page / of 6 / 20		



CLK\_SEL Note:  
CLKSEL = 1 => MCU DPLL output 266MHz  
CLKSEL = 0 => MCU DPLL output 532MHz

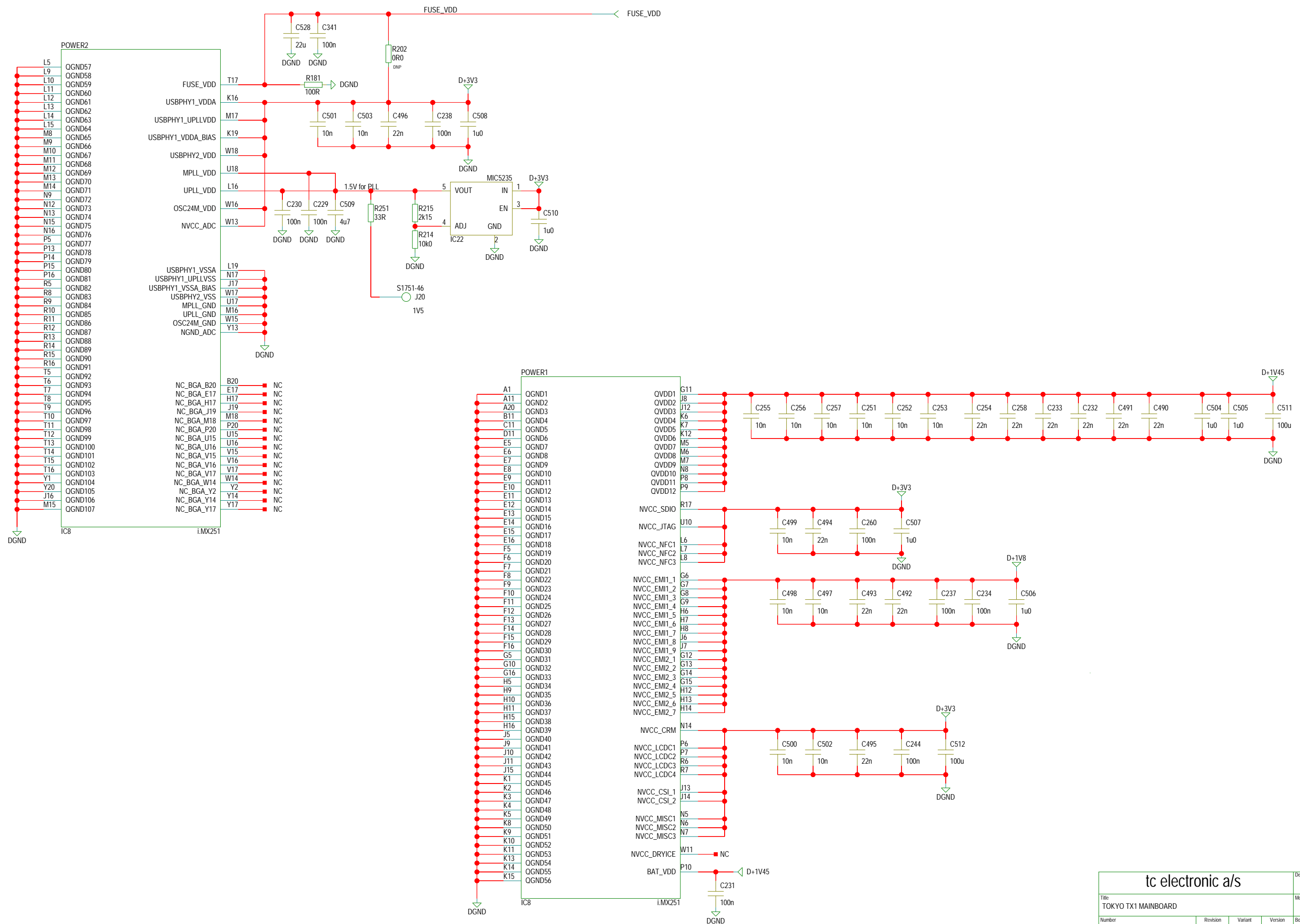
tc electronic a/s				Designer(s) CGF/MFA/MC/BFE
Title TOKYO TX1 MAINBOARD				Module title HOST MISC
Number P56030	Revision A	Variant A	Version E	Board type MAINBOARD
Date 10-09-2012_14:03	Filename HOST_MISC		Page / of 7 / 20	



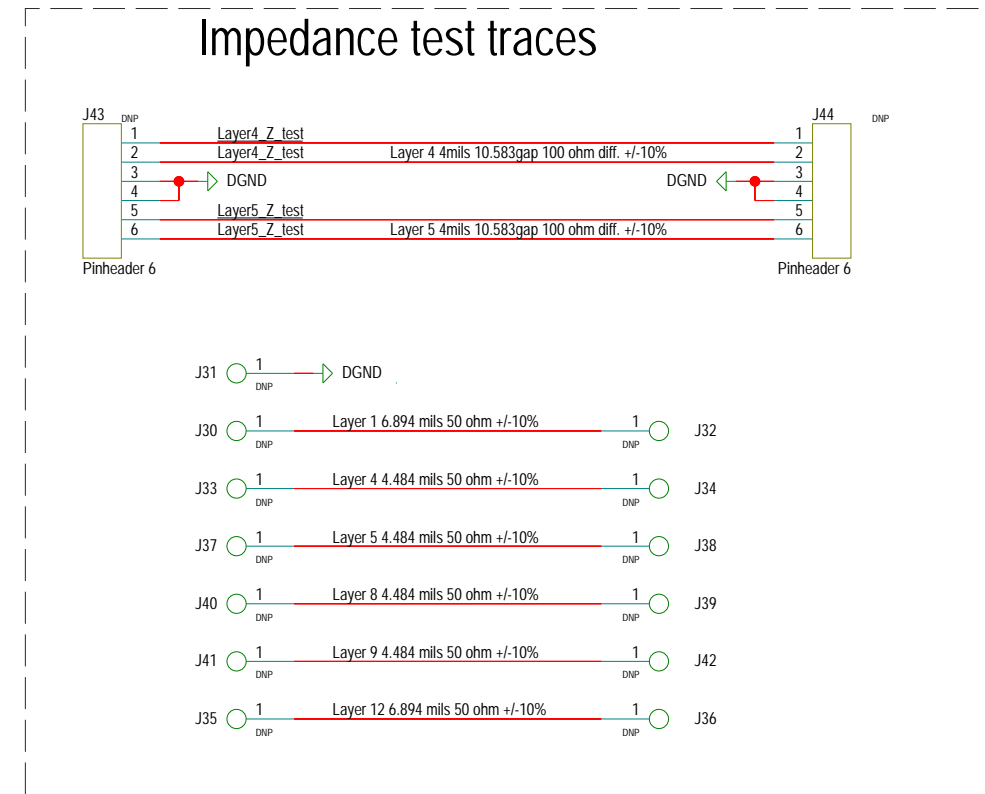
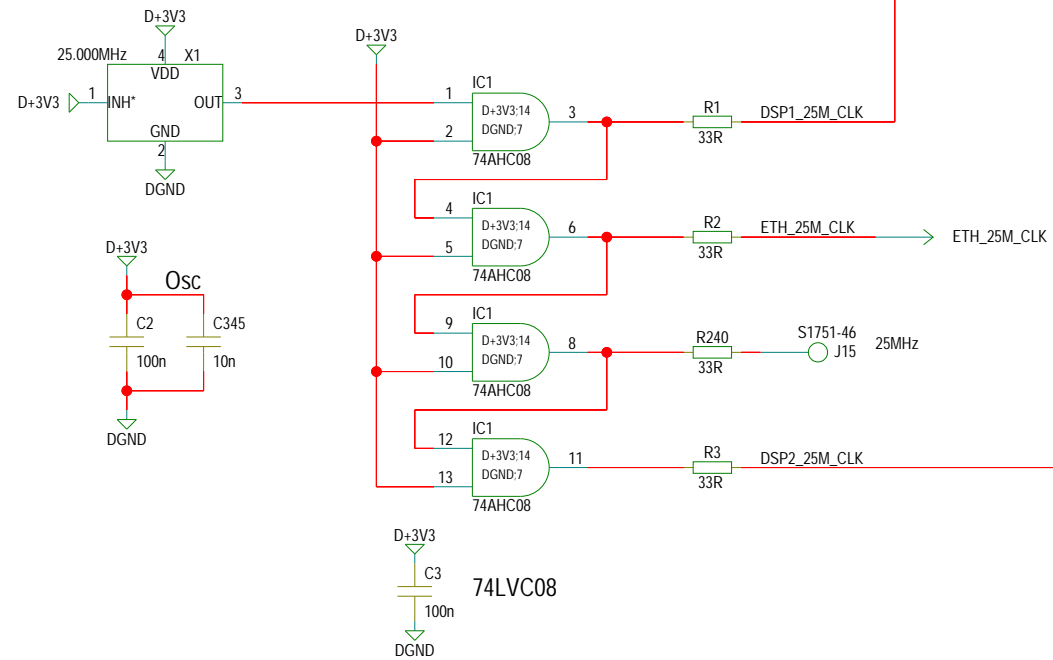
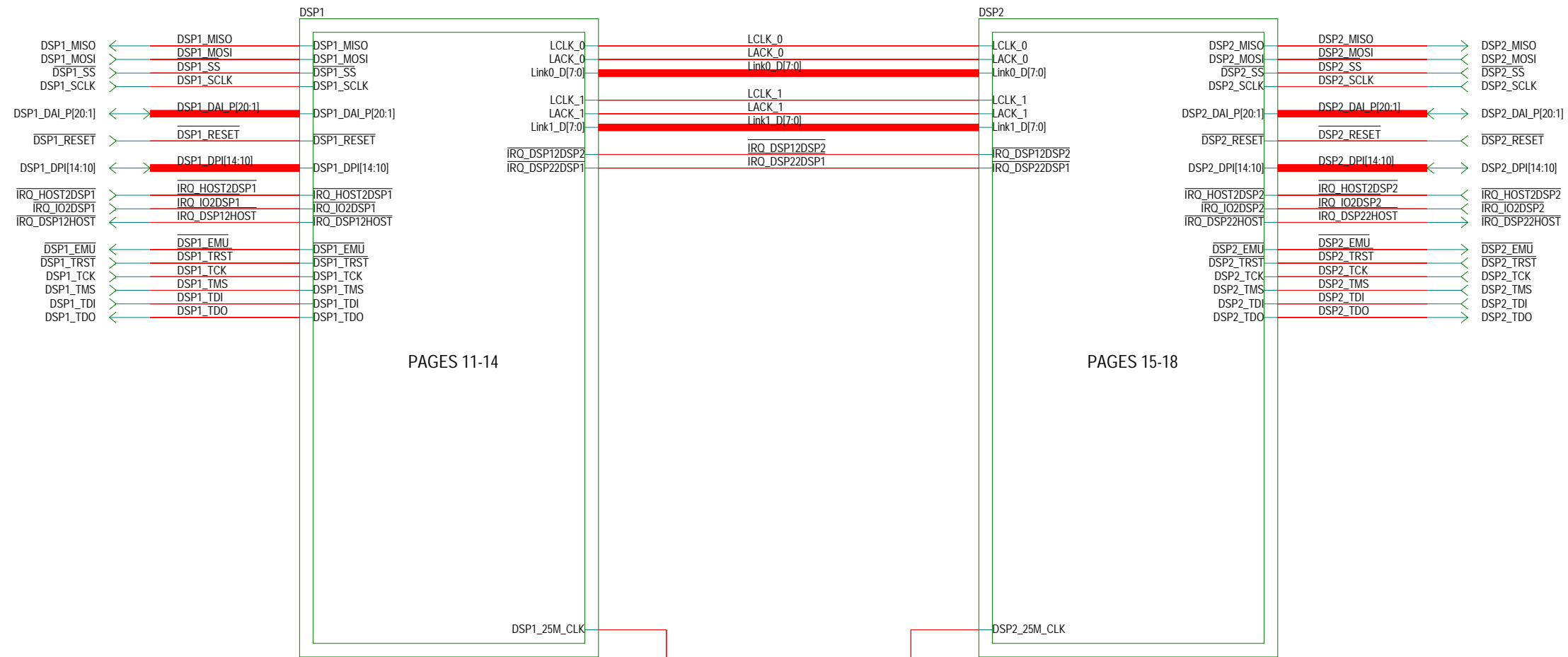
DGND-CGND

tc electronic a/s				Designer(s) CGF/MFA/MC/BFE
Title TOKYO TX1 MAINBOARD				Module title HOST ETHERNET
Number P56030	Revision A	Variant A	Version E	Board type MAINBOARD
Date 10-09-2012_14:03	Filename ETHERNET			Page / of 8 / 20

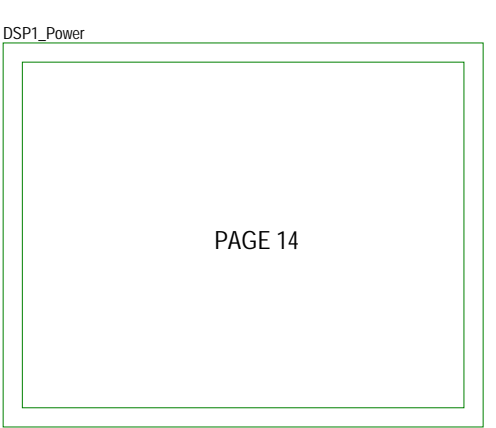
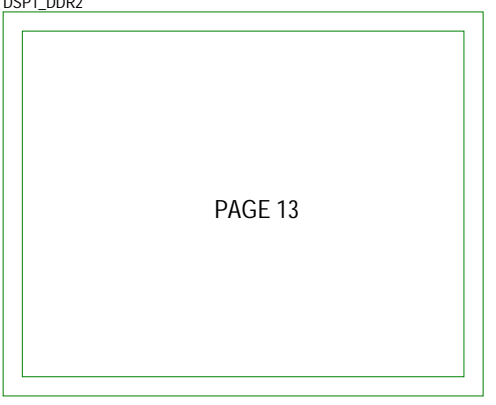
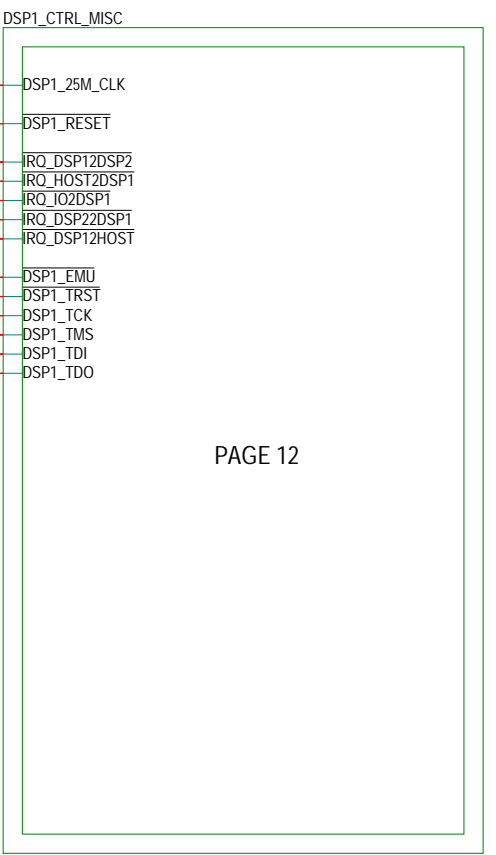
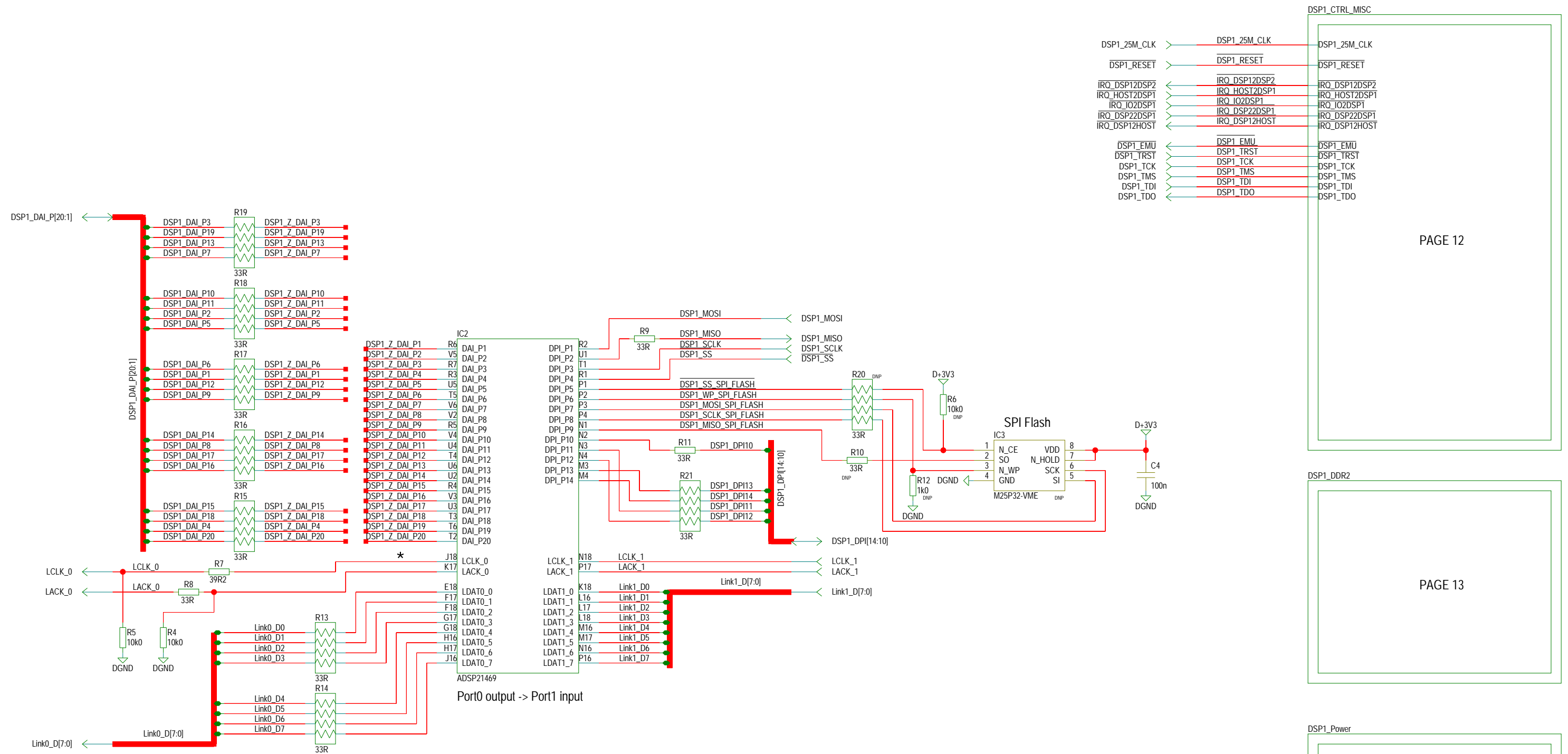




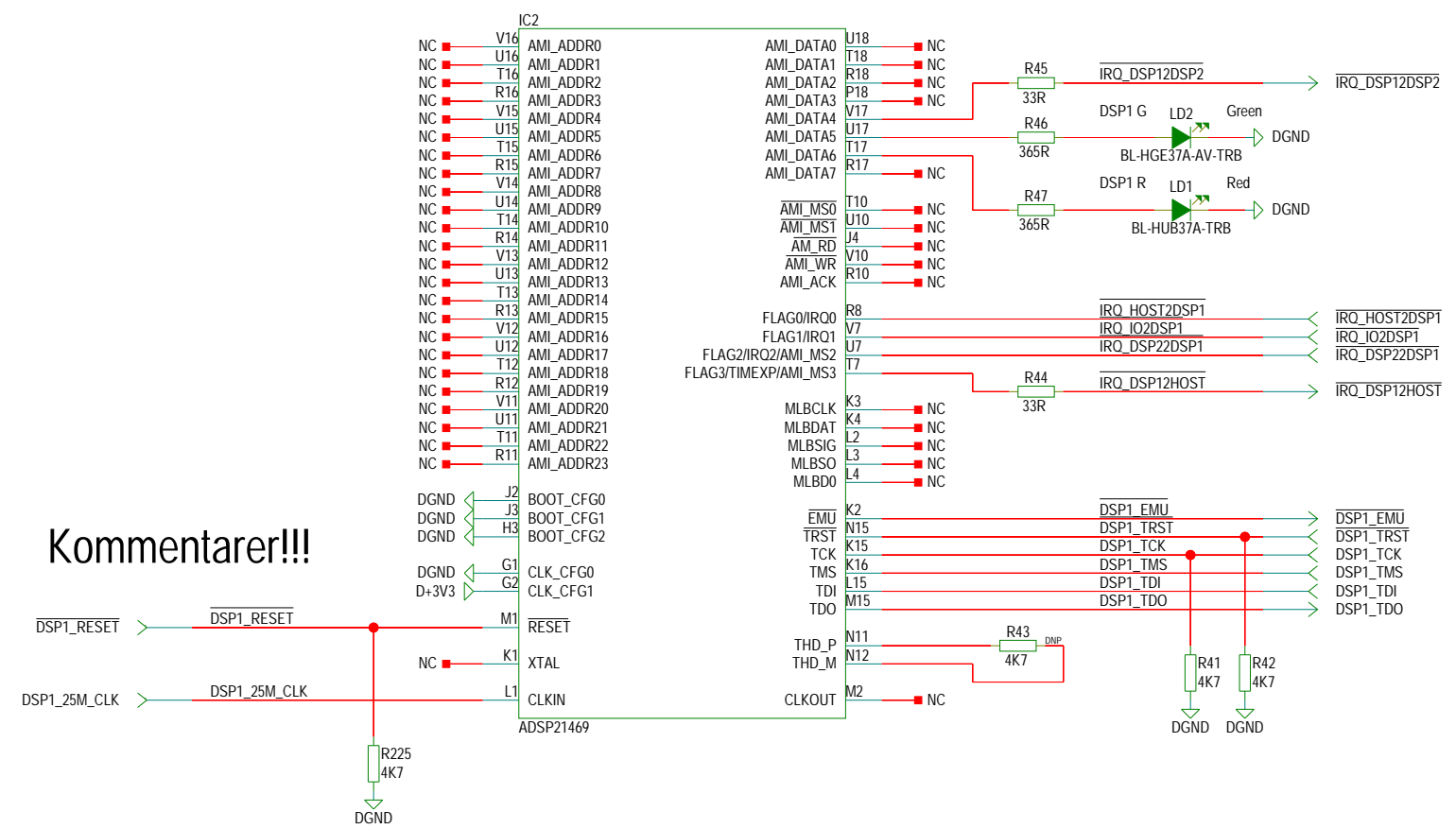
tc electronic a/s				Designer(s) CGF/MFA/MC/BFE
Title TOKYO TX1 MAINBOARD				Module title HOST POWER
Number P56030	Revision A	Variant A	Version E	Board type MAINBOARD
Date 10-09-2012_14:03	Filename HOST_POWER			Page / of 9 / 20

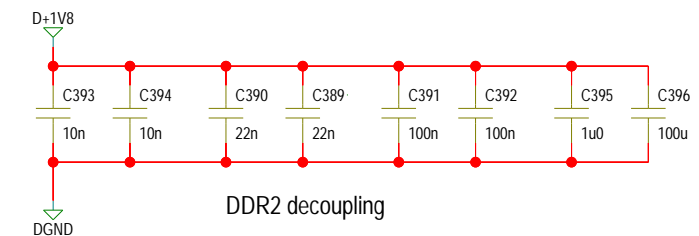
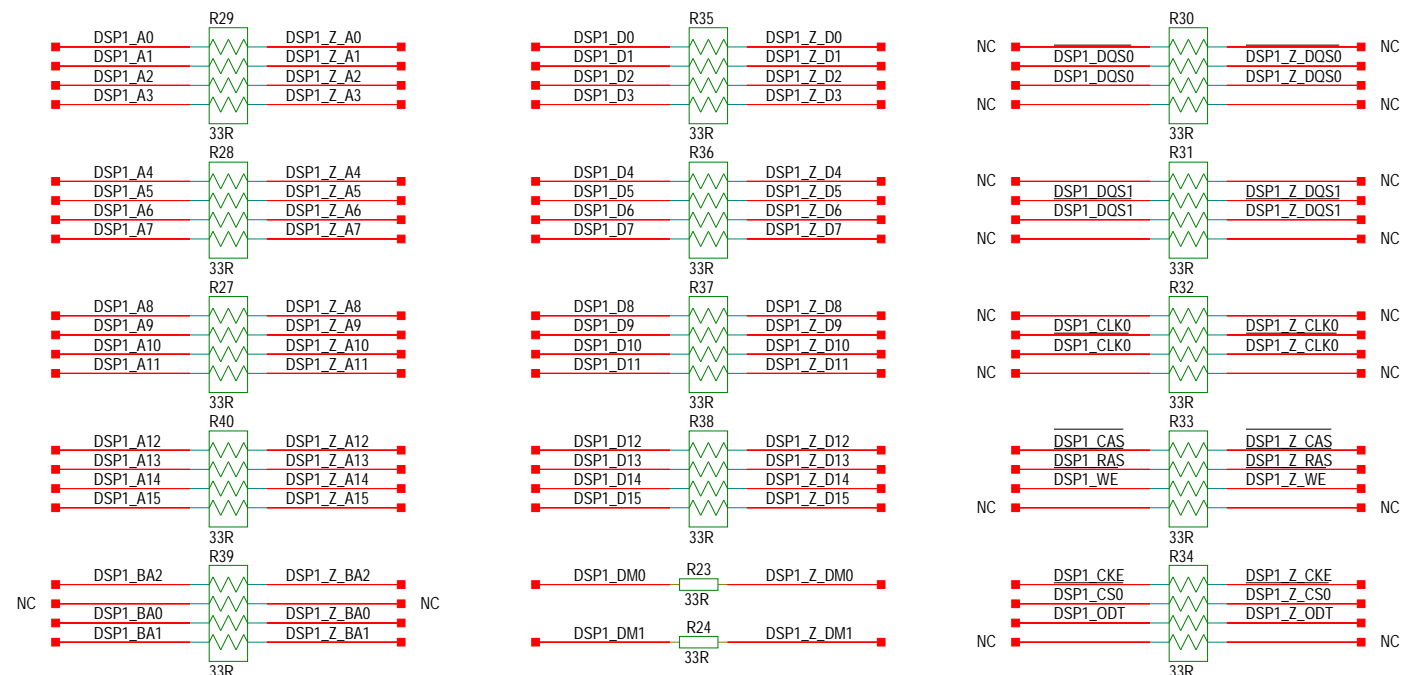
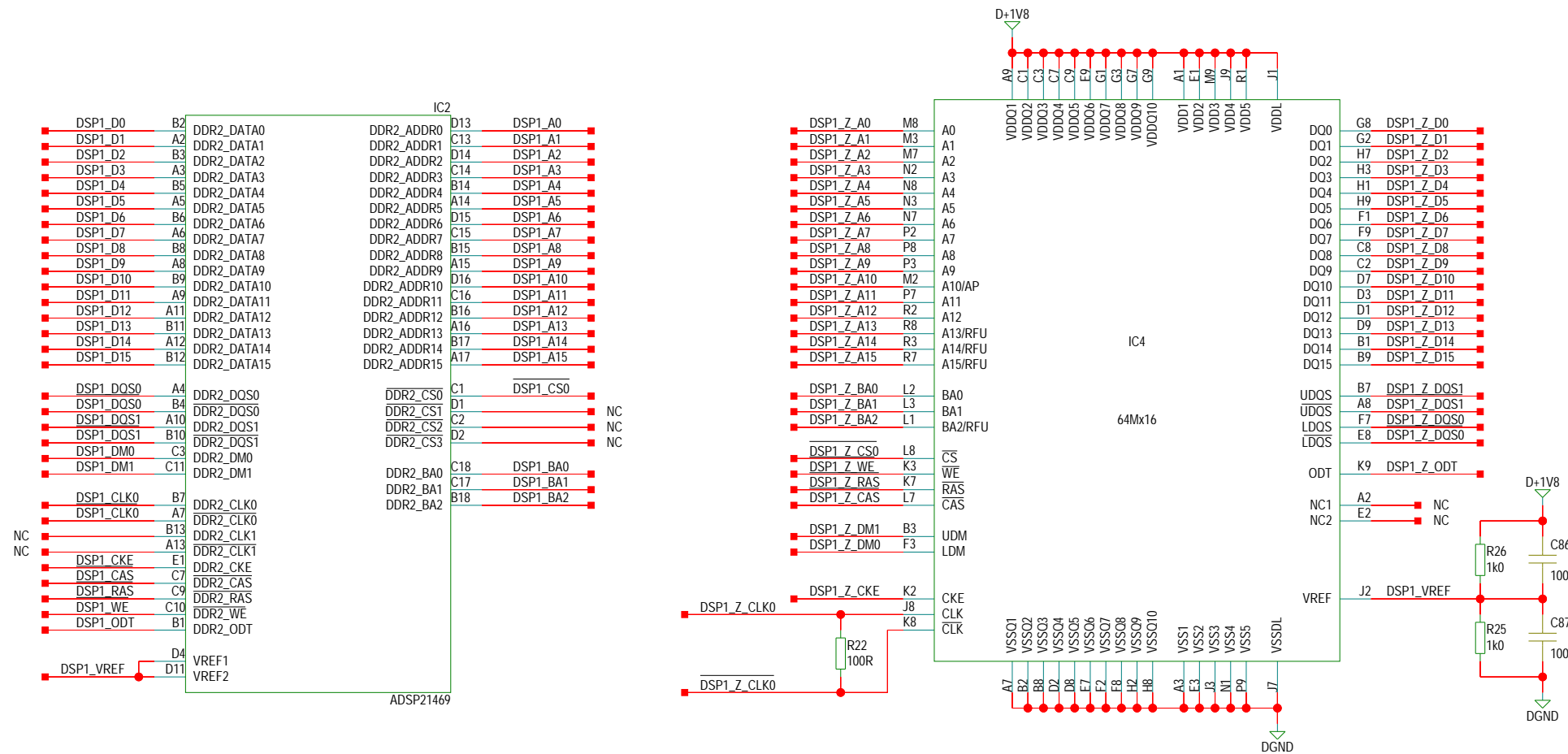


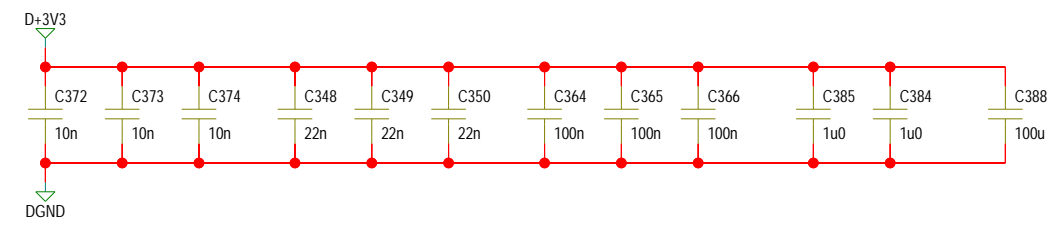
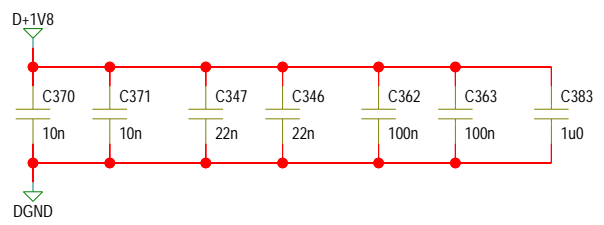
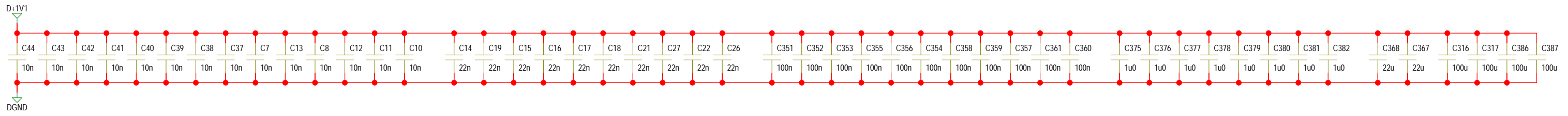
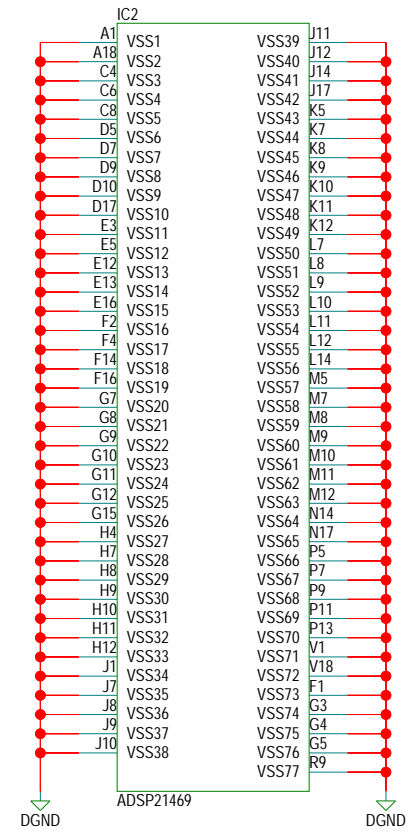
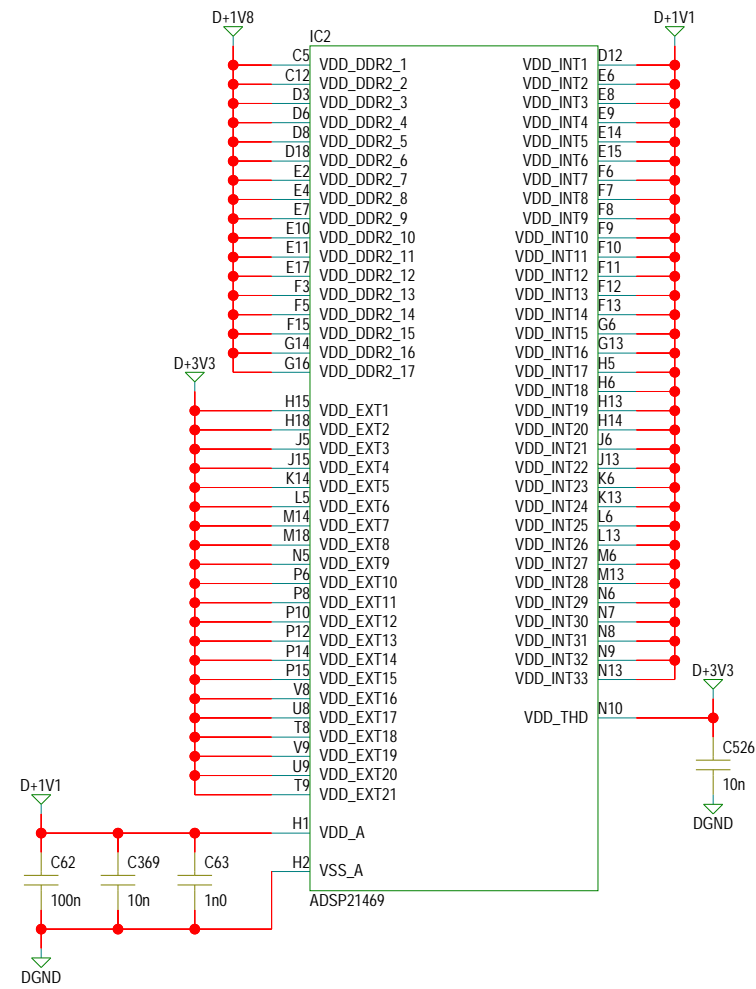
tc electronic a/s				Designer(s) CGF/MFA/MC/BFE	
Title TOKYO TX1 MAINBOARD				Module title DSP	
Number P56030	Revision A	Variant A	Version E	Board type MAINBOARD	
Date 10-09-2012_14:03	Filename DSP			Page / of 10 / 20	



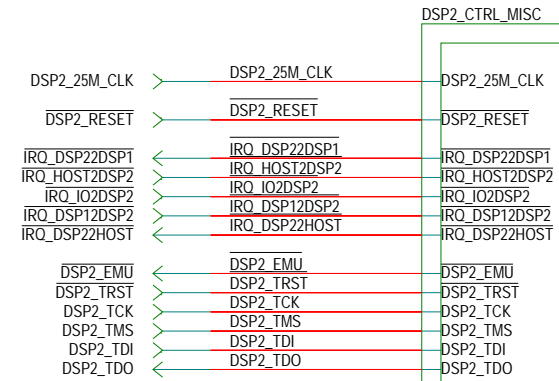
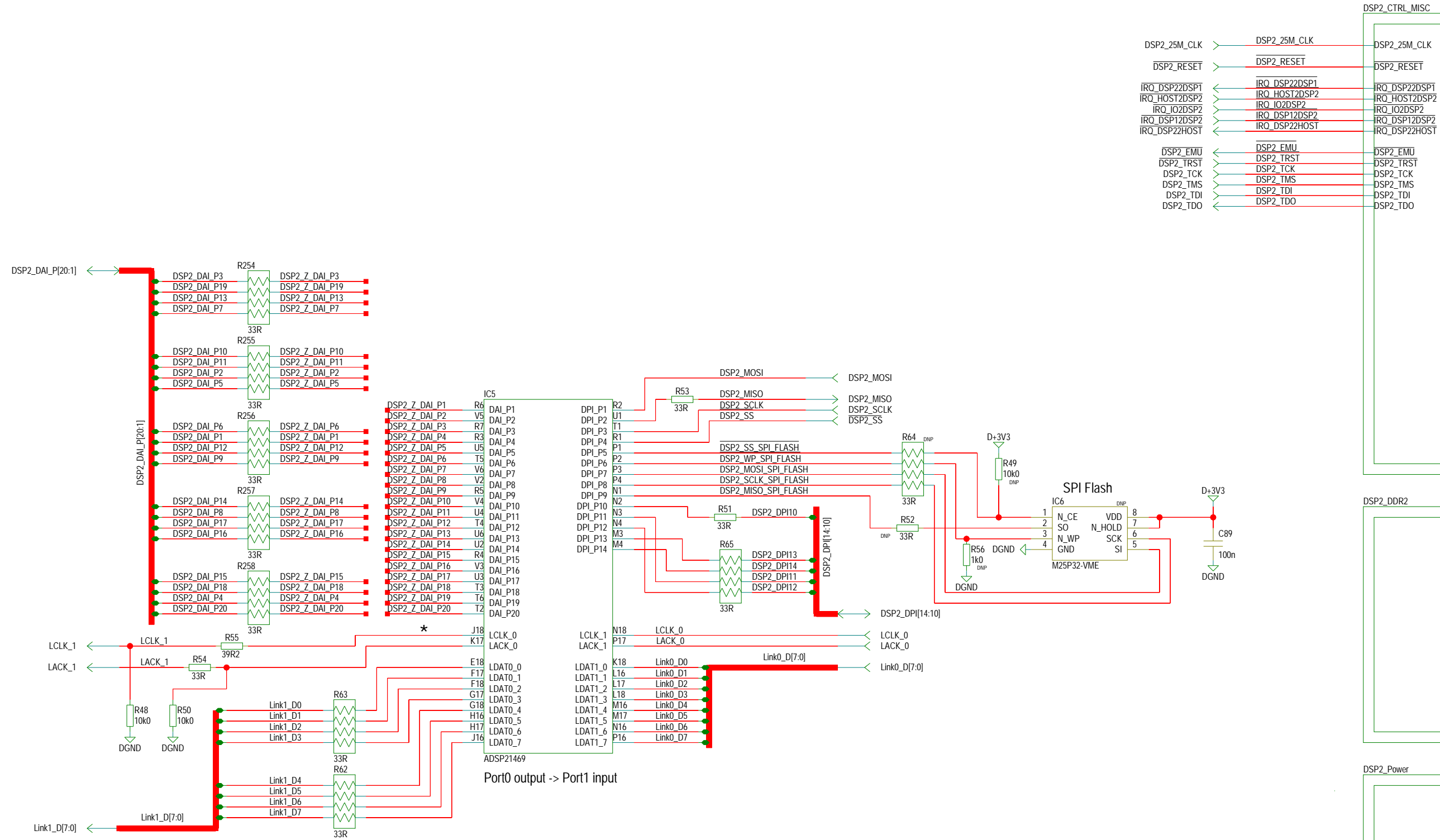
tc electronic a/s				Designer(s) CGF/MFA/MC/BFE
Title TOKYO TX1 MAINBOARD				Module title DSP1
Number P56030	Revision A	Variant A	Version E	Board type MAINBOARD
Date 10-09-2012_14:03	Filename DSP1			Page / of 11 / 20







tc electronic a/s				Designer(s) CGF/MFA/MC/BFE
Title TOKYO TX1 MAINBOARD				Module title DSP1 POWER
Number P56030	Revision A	Variant A	Version E	Board type MAINBOARD
Date 10-09-2012_14:02	Filename DSP1_Power		Page / of 14 / 20	



PAGE 16

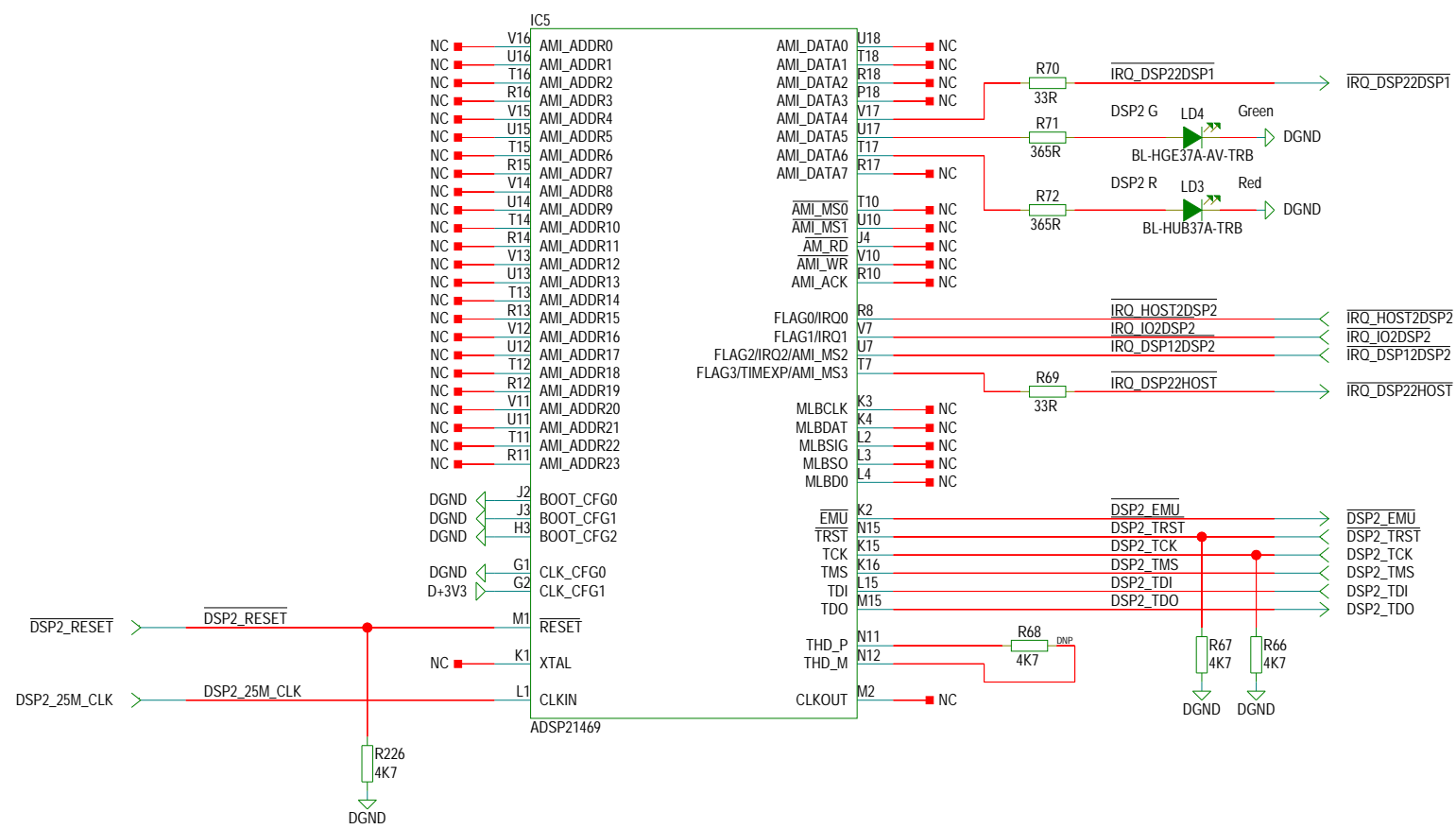
PAGE 17

PAGE 18

High speed constraints

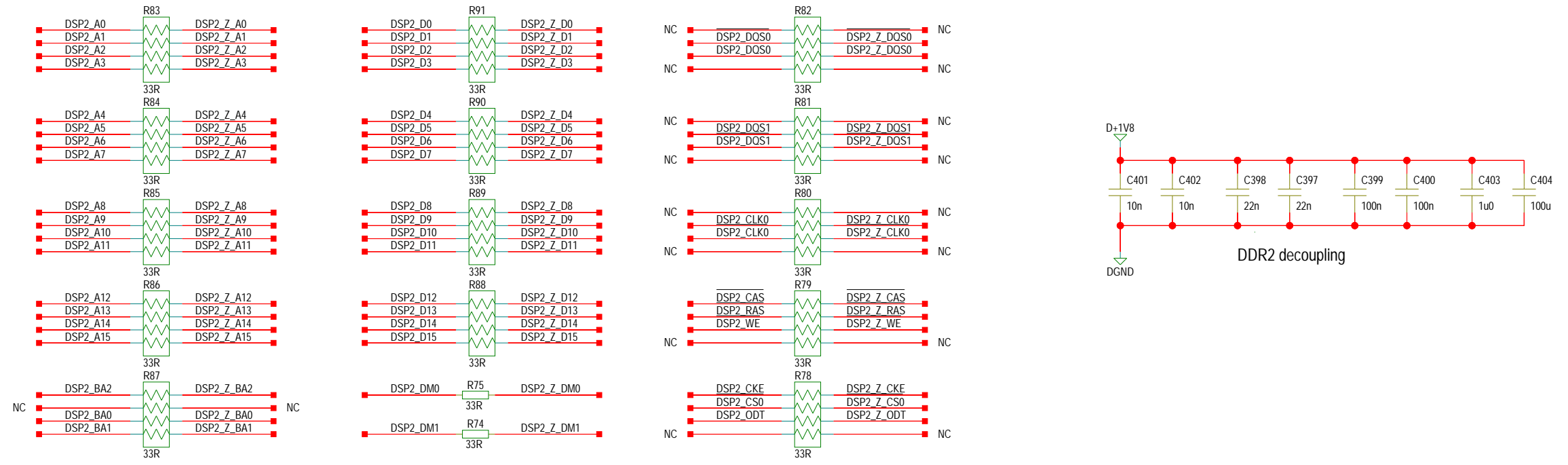
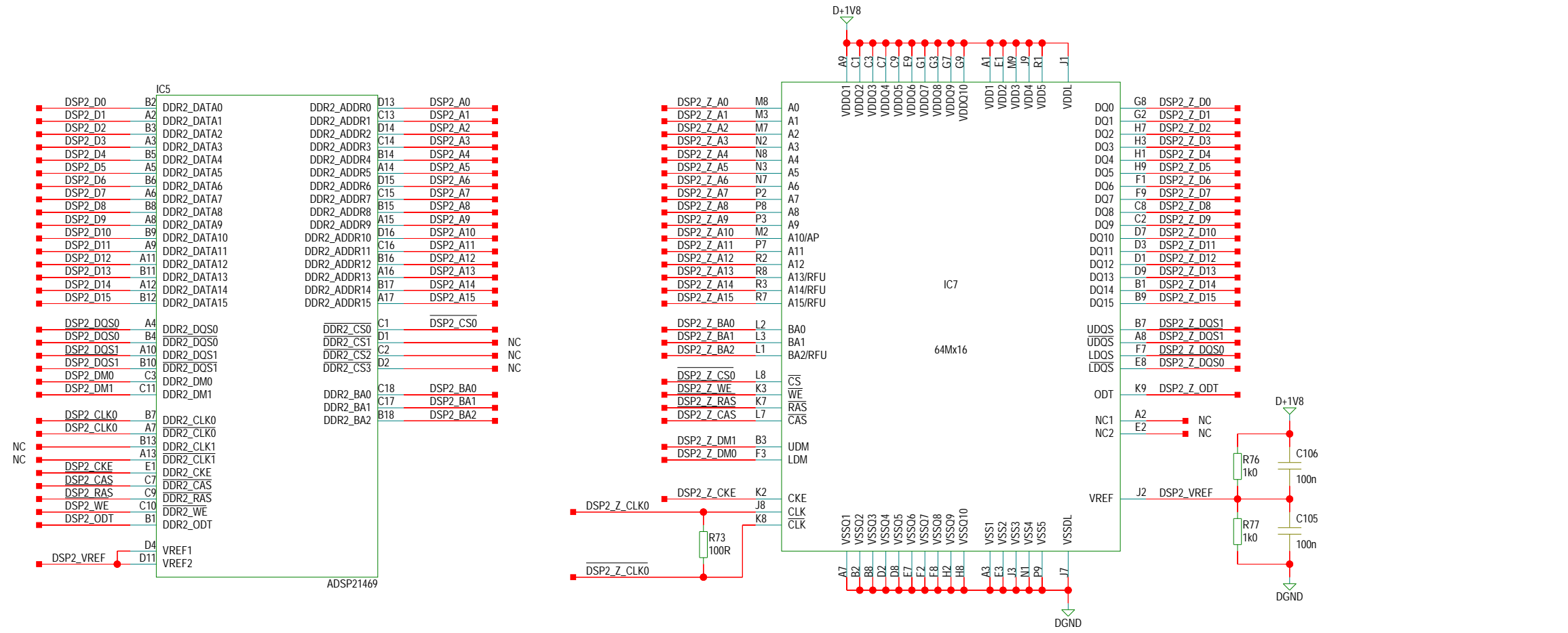
\* Trace < 8mm

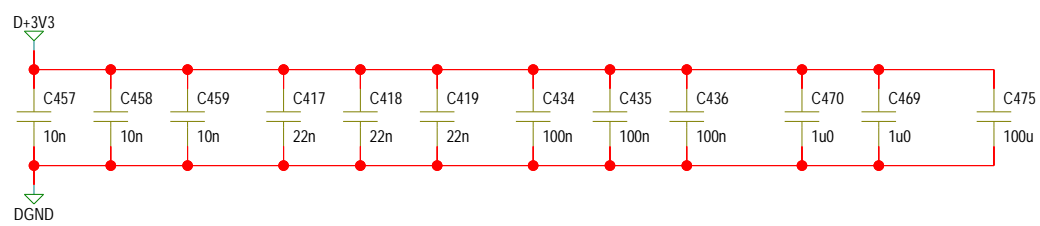
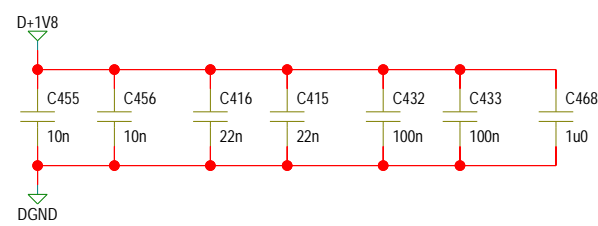
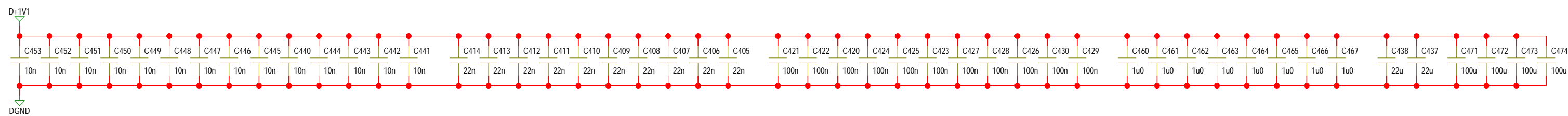
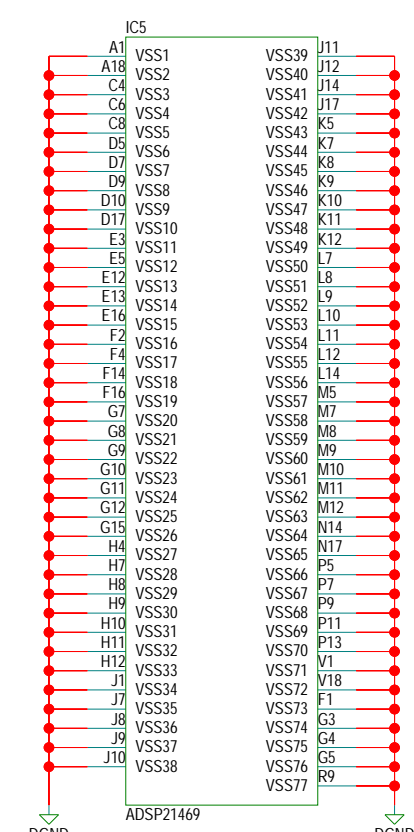
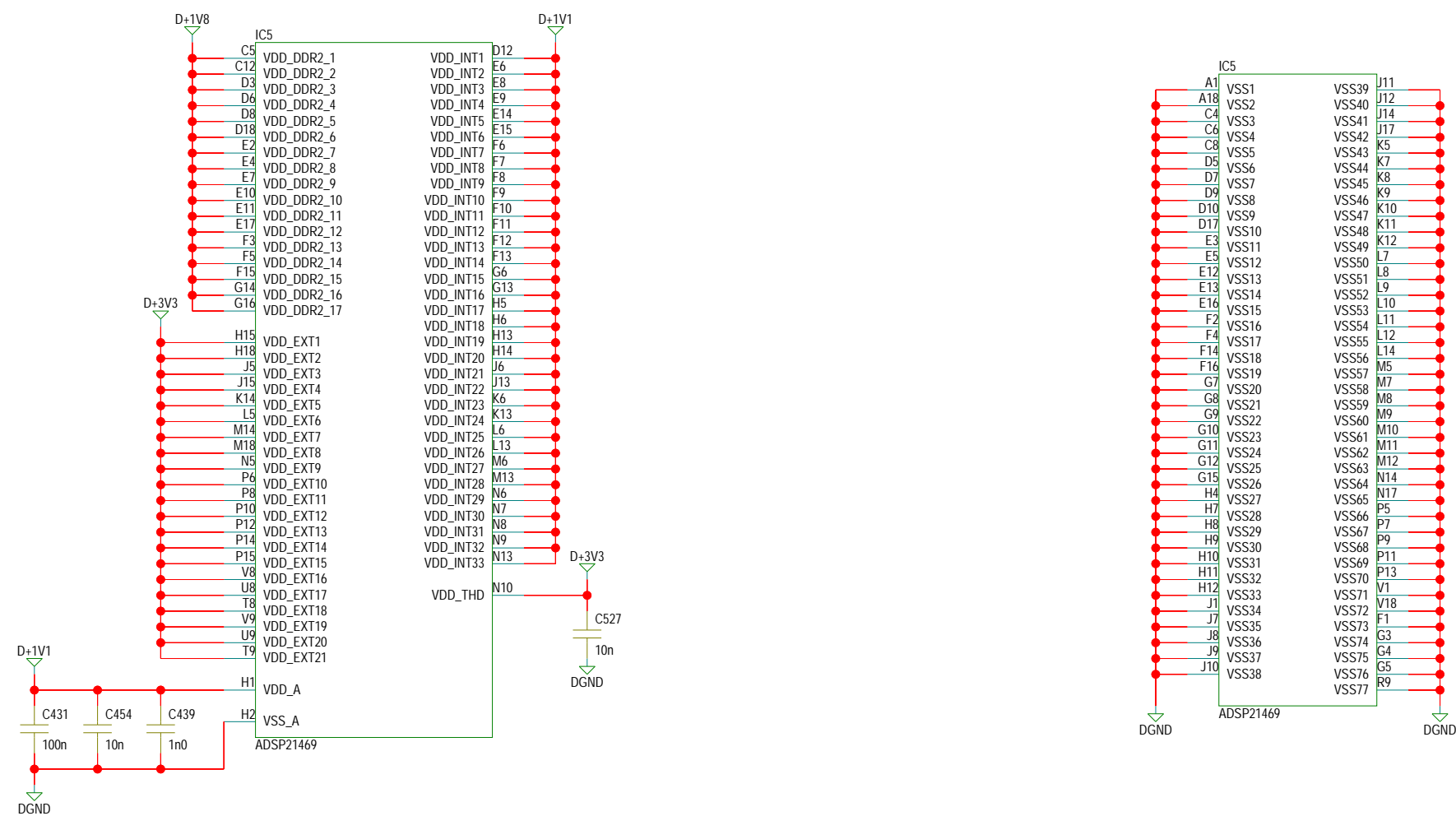
tc electronic a/s				Designer(s)	CGF/MFA/MC/BFE
TOKYO TX1 MAINBOARD				Module title	DSP2
Number	Revision	Variant	Version	Board type	MAINBOARD
P56030	A	A	E		
Date	Filename			Page / of	
10-09-2012_14:03	DSP2			15 / 20	



tc electronic a/s				Designer(s) CGF/MFA/MC/BFE
Title TOKYO TX1 MAINBOARD				Module title DSP2 CTRL & MISC
Number P56030	Revision A	Variant A	Version E	Board type MAINBOARD
Date 10-09-2012_14:03	Filename DSP2_CTRL_MISC			Page / of 16 / 20

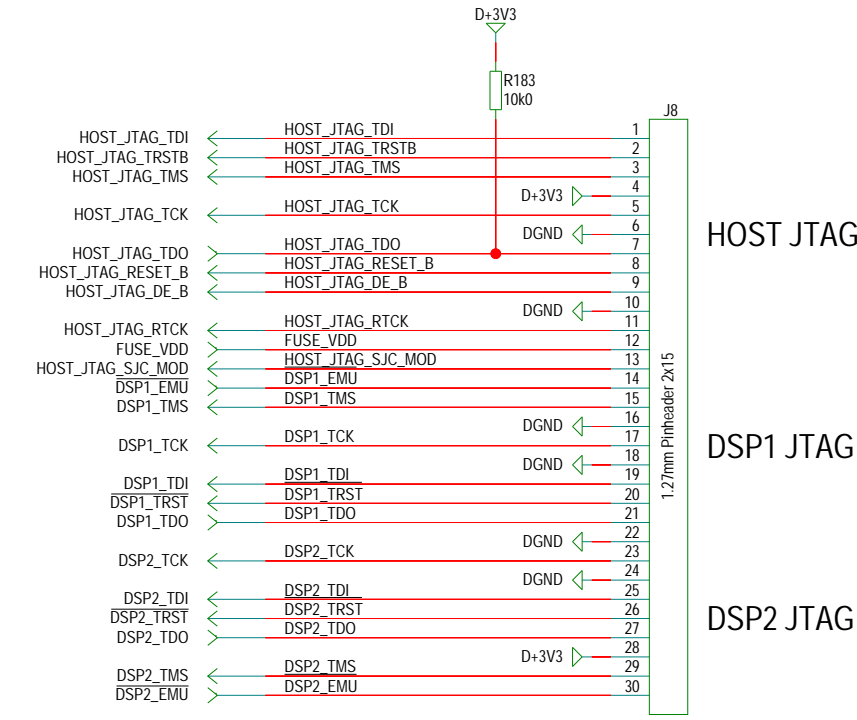
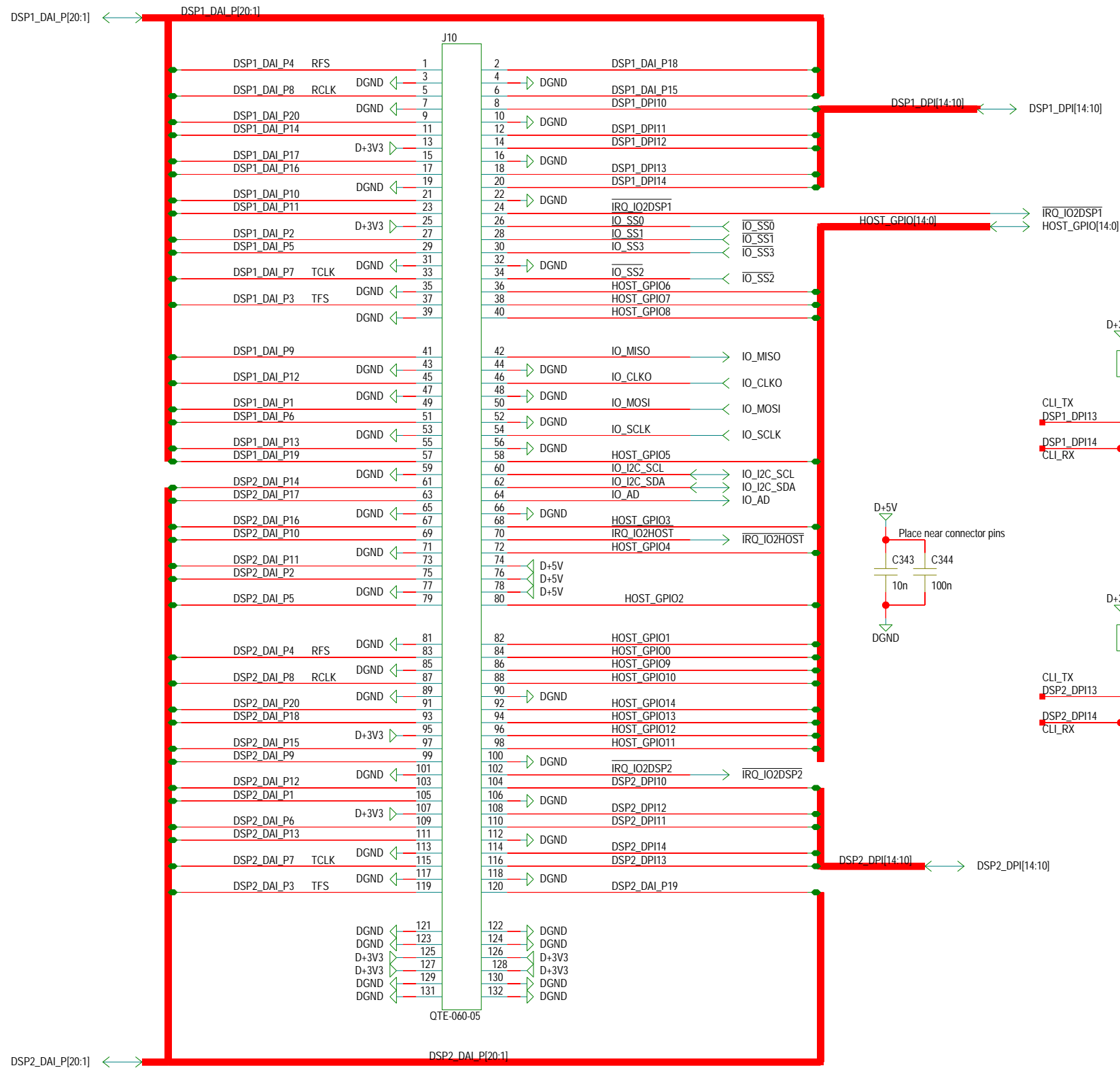




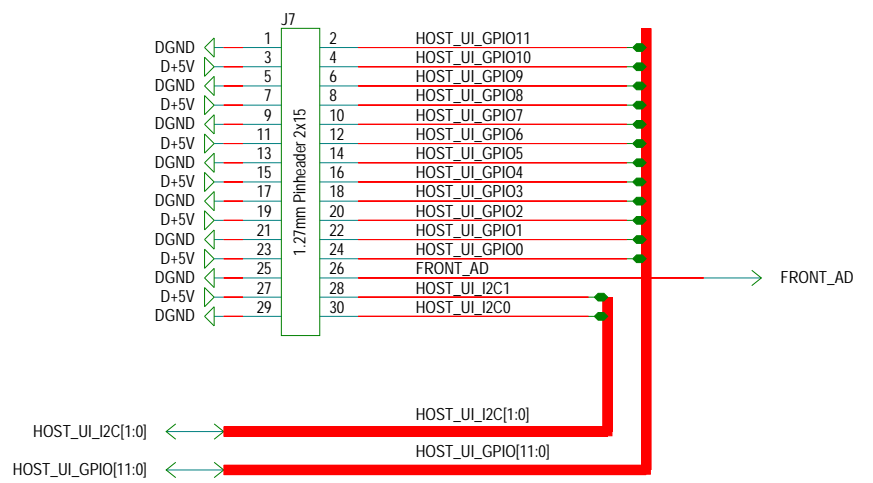


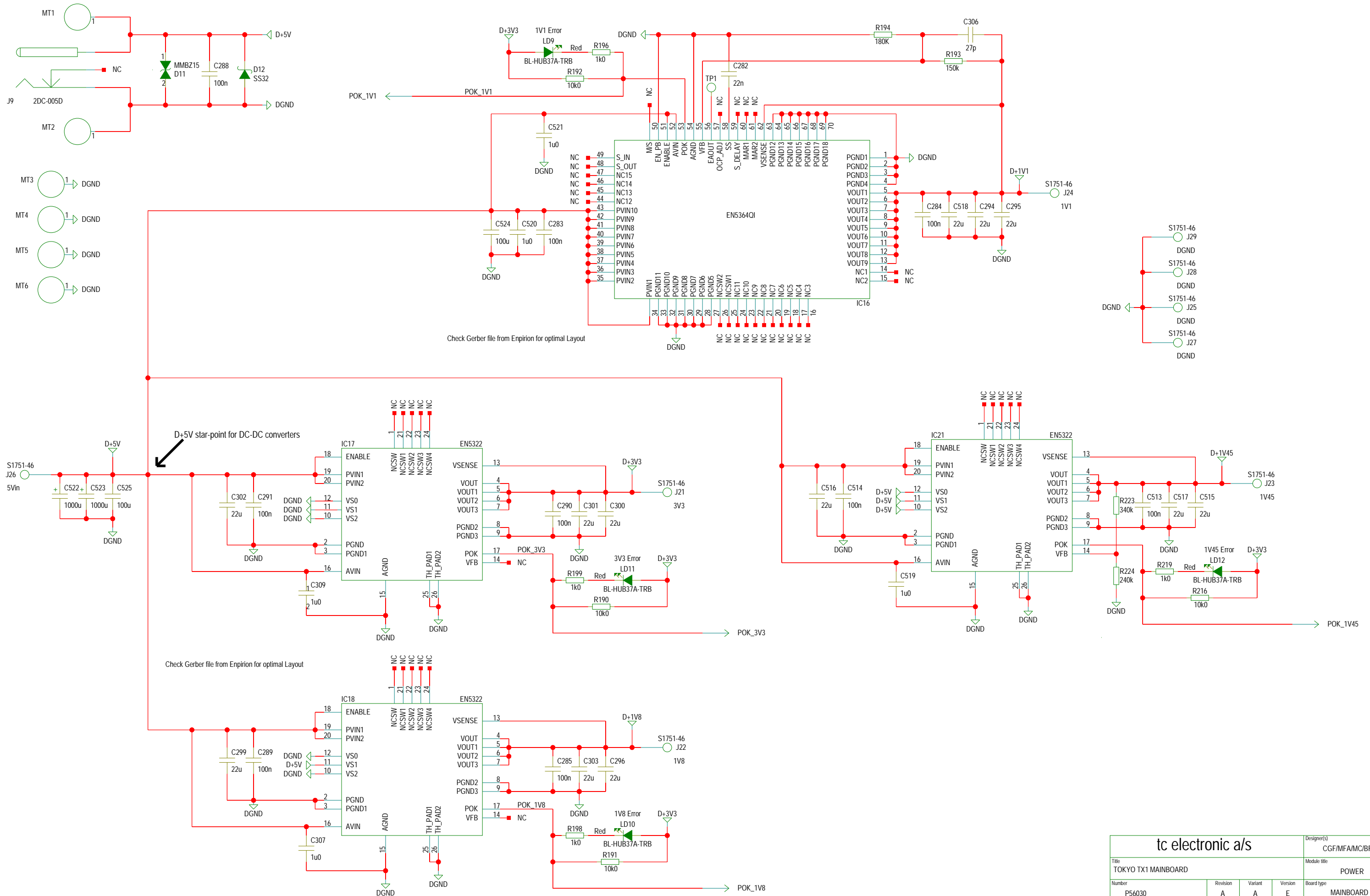
tc electronic a/s				Designer(s) CGF/MFA/MC/BFE
Title TOKYO TX1 MAINBOARD				Module title DSP2 POWER
Number P56030	Revision A	Variant A	Version E	Board type MAINBOARD
Date 10-09-2012_14:02	Filename DSP2_Power		Page / of 18 / 20	

### IO option



### Front UI and power





tc electronic a/s				Designer(s) CGF/MFA/MC/BFE
Title TOKYO TX1 MAINBOARD				Module title POWER
Number P56030	Revision A	Variant A	Version E	Board type MAINBOARD
Date 10-09-2012_14:02	Filename Power			Page / of 20 / 20