Area reserved for document binding

Best printed and viewed on sheetsize A4

TP9 PA0 PA0 = I2SCK (bitclock)
TP10 PA1 PA1 = I2SWS (wordsync)
TP11 PA2 PA2 = I2SDI (Data into ARM)
TP12 PA3 PA3 = I2SDO (Data out of ARM)
TP13 PA4 PA4 = I2SMCK (Masterclock)

IC2
SAMG51G18A

PA9	A1	PA9/URXD0/NPCS1/WKUP6	PA1/TIOB0/WKUP1	D5	PA1
DGND	A2	GND1	PA5/RXD/WKUP4	D6	PA5
TSW1	A3	PA24/WKUP11	VDDCORE2	D7	D+1V2
PB8	A4	PB8/TWD1/WKUP14/XOUT32	PB2/URXD1/NPCS1/AD6/WKUP12	E1	ADC1
PB9	A5	PB9/TWCK1/WKUP15/XIN	PB0/TWD2/AD4	E2	ADC0
TSW2/TDI	A6	PB4/TDI	PA18/PCK2/AD1	E3	PA18
D+1V8	A7	VDDIO1	PA14/SPCK/WKUP8	E4	PA14
	B1	PB11/TWCK11/TWCK21	PA10/UTXD0	E5	PA10
TWCK	B2	TDO/TRACESWO	TST	E6	
TP15	B3	TCK/SWCLK	PA7/XIN32	E7	
TP6	B4	PB7/TCK/SWCLK	PA20/TCLK2/AD3	F1	PA20
PA2	B5	PA2/TCLK0/WKUP2	PA19/TCLK1/AD2	F2	PA19
	B6	JTAGSEL	PA17/PCK1/AD0	F3	PA17
RESET	B7	NRST	PA21/TIOA2/PCK1/WKUP9	F4	PA21
ERASE	C1	PB12/ERASE	PA23/TIOA1/WKUP3	F5	PA23
D+1V2	C2	VDDCORE1	PA16/CTS/TIOB1	F6	PA16
PA11	C3	PA11/NPCS0/WKUP7	PA8/ADTRG/WKUP5/XOUT32	F7	
PA12	C4	PA12/MISO	VDDIO2	G1	D+1V8
PB6/TMS/SWDIO	C5	PB6/TMS/SWDIO	VDDOUT	G2	D+1V2
PA4	C6	PA4/TWCK0	GND2	G3	DGND
PA3	C7	PA3/TWD0	VDDIO3	G4	D+1V8
PA0	D1	PA0/TIOA0/WKUP0	PA22/TIOB2/WKUP10	G5	PA22
PA13	D2	PA13/MOSI	PA15/RTS/SCK	G6	PA15
BAT_MON	D3	PB3/UTXD1/PCK2/AD7/WKUP13	PA6/TXD/PCK0	G7	PA6
BOOTSTRAP	D4	PB1/TWCK2/AD5			
TWD		PB10/TWD11/TWD21			

C21 20p DGND
X1 32.768kHz
C22 20p DGND

S2 SKSCL R24 1k00 TSW1 TP4
S1 SKSCL R25 1k00 TSW2/TDI TP5
TP22 R56 1k00 PWR_SW/TMS/SWDIO DGND
TP14 RESET ERASE

Piezo Element

M2 N/A
J1 J4 J6 SM02B-SRSS-TB J7 J8 J9
R32 4k75 R35 4k75 C27 18n R33 4k75 R34 4k75 R38 0R0 R39 0R0
C30 1n0 R16 200k C29 1n0 R15 200k
D+1V8 IC3 AZV832 ADC0
R17 200k R14 200k R5 10k0 R2 100k
IC3 AZV832 ADC1 R1 100k
C31 1n0 C35 1n0

TP18 Pos. terminal J3 J2 Neg. Terminal J5 Neg. Terminal
TP2 C32 4u7 C33 4u7 DGND
Q6 4DMG1023UW R13 470k
S3 SKSCL R30 1k00 TP1 R31 100k PWR_SW/TMS/SWDIO
R20 100k C9 100n
Q10 BC847CDXV TP17 R18 22k BOOTSTRAP R19 22k

TP7 D+3V0 C23 4u7 C13 100n R21 100k TP19 BAT_MON R22 100k C18 100n
U1 TPS622402 VIN SW EN FB GND L1 2.2uH C34 27p R55 470k R54 240k C24 4u7
TP8 D+1V8

TWCK TWD TP20 TP21
R27 1k00 R26 1k00
IC1 BMA220
SDO SDx VDDIO VDDD INT CAP0 SCK PS CSB GND2 GND1 VDDA

C5 C20 C4 C14 C2 C15 C3 C16 C26 C37 C28 C36 C7 C6
4u7 100n 22u
VDDOUT VDDOUT VDDCORE_1 VDDCORE_1 VDDIO_1 VDDIO_2 VDDIO_2 VDDIO_3 OPAMP General General General Accelerometer Accelerometer

PA23 PA22 PA21 PA20 PA19 PA18 PA17
PA[17:23]
R50 R51 R52 R53 R47 R48 R49 100k
Q7 Q8 Q9 Q10 BC847CDXV
C12 100n
DMG1023UW Q3 Q4 Q5 Q6
R23 270R C16
270RR9 C15 C14 C13 C12
270RR6 C11 C10 C9 C8
270RR7 C7 C6 C5 C4
270RR8 C3 C2 C1 C0
PA16 PA15 PA14 PA13 PA12 PA11 PA10 PA9 PB9 PB8 PA6 PA5 PA4 PA3 PA2 PA1 PA0

LD100 LD200 LD300 LD400 LD500 LD600 LD700
D100 D200 D300 D400 D500 D600 D700
Red Green
C[16:0]
R40 R41 R42 R43 R44 R45 100k

Guitar
Bass
Bass
Guitar
High Arrow
Low Arrow
#
R46 100k

M1 P22582-E

tc electronic		Designer(s)	HJE/PRO
Title	1282-FLIPPER	Module title	MODULE TITLE
Number	P22582	Revision E / Variant A / Version B	Board type MAINBOARD
Date	07/09/2015:08:56	Filename	Page / of 1 / 1