Service Note

Products	Description:	Service note no.
Finalizer 96, DBMax, D22, P2, DB2 and LM2	**Noise when using Sample Rate Conversion**	**SNo-T0160**

Reason for note:

We have encountered a problem on a few Finalizer 96 products. The error has been reported at sample-rate conversion with digital input and sample-rate conversion to the local clock at a similar frequency. The error occurs as noise on the audio output in right channel.

However the error is also potentially present on all other Wizard products with a sample-rate converter (DBMax, D22, P2, DB2 and LM2) since the release of Wizard Mainboards with a Xilinx FPGA. This FPGA replaced the old DARC3 circuit back in December 2007.

Actions:

The error is eliminated by inverting PLL_64FS inside the FPGA and this unfortunately can only be done at TC DK.

If a product comes in with an error like mentioned, it is therefore necessary to ship it to TCDK for repair.

IMPORTANT: