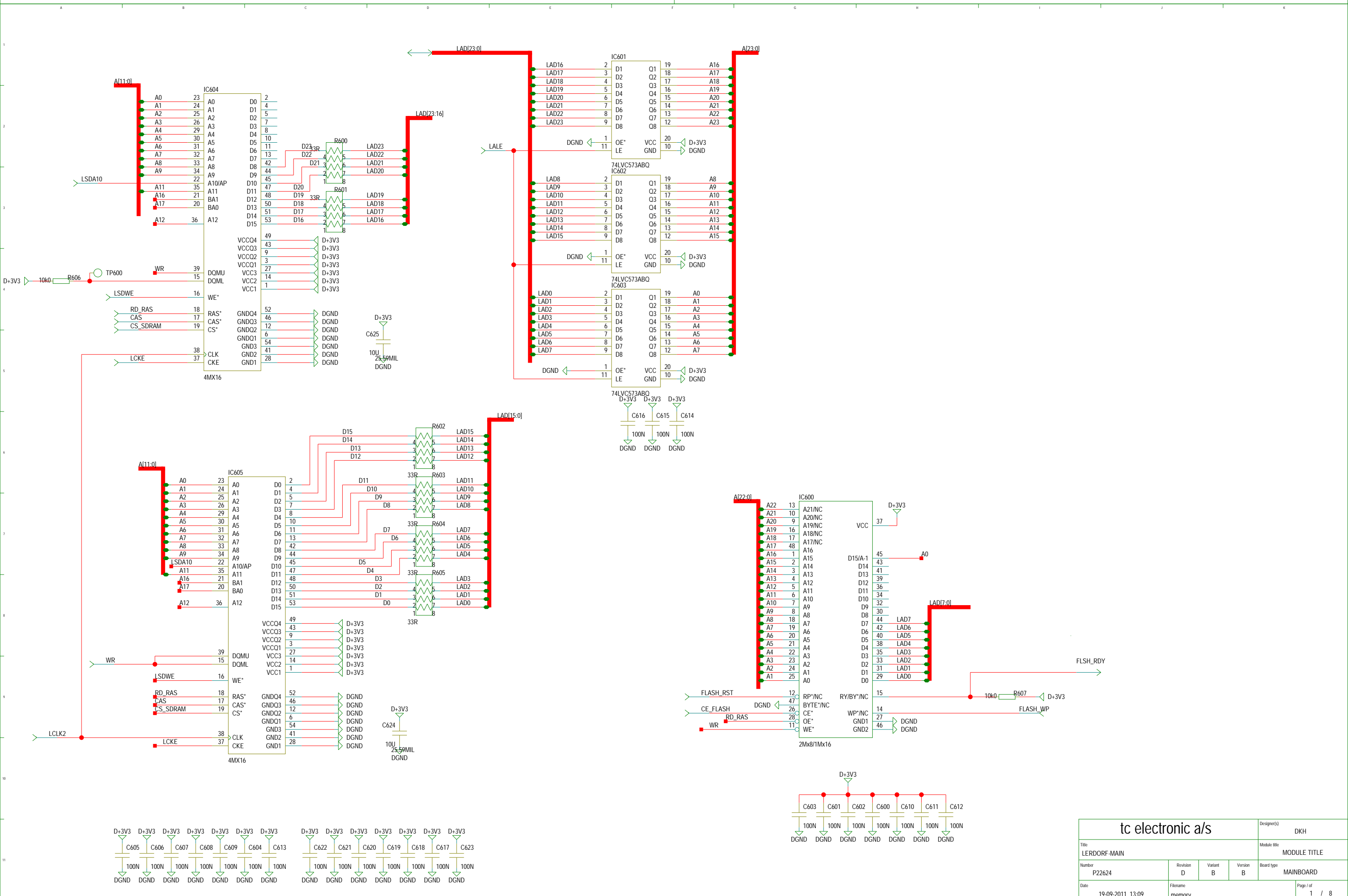
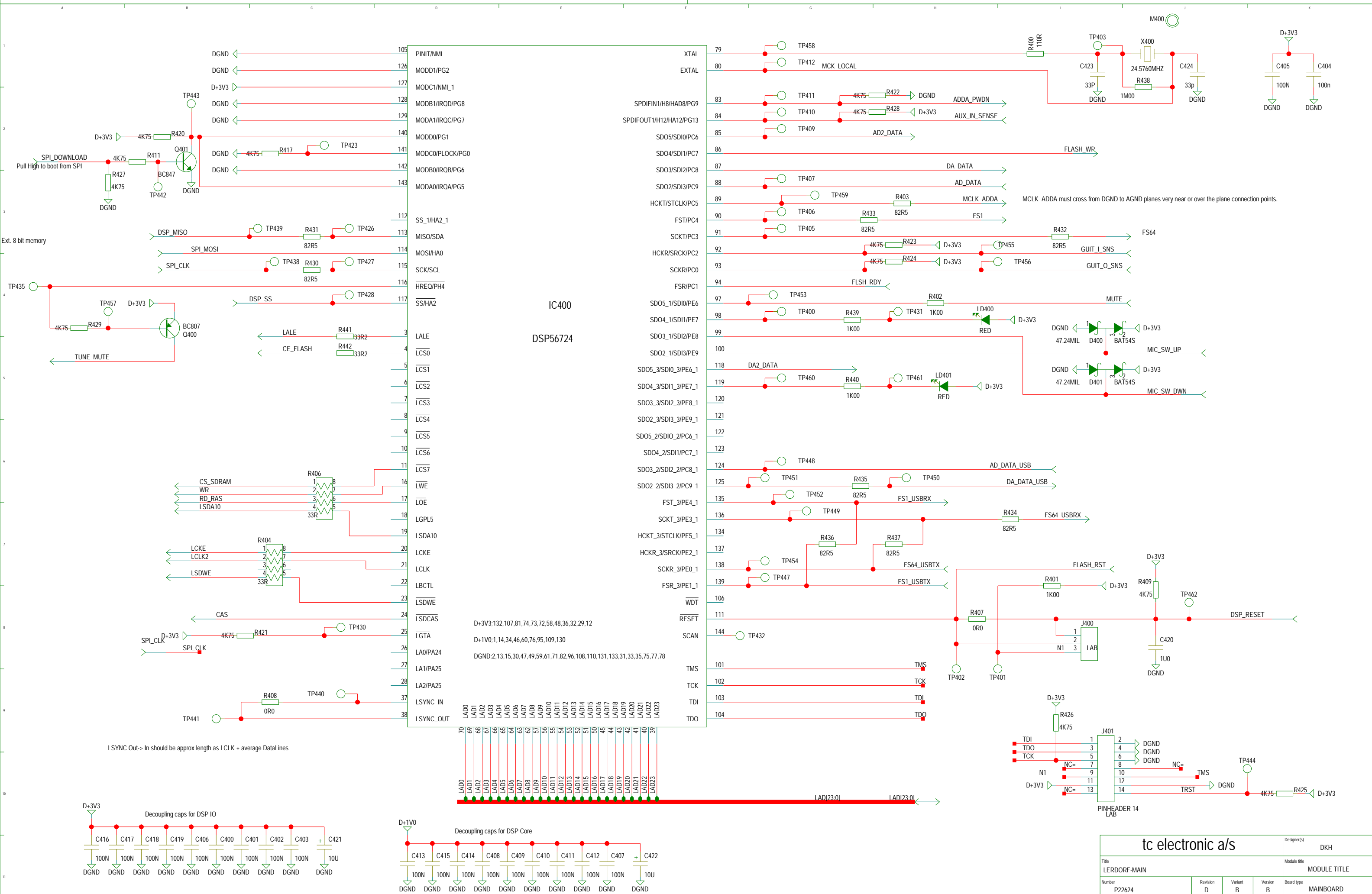


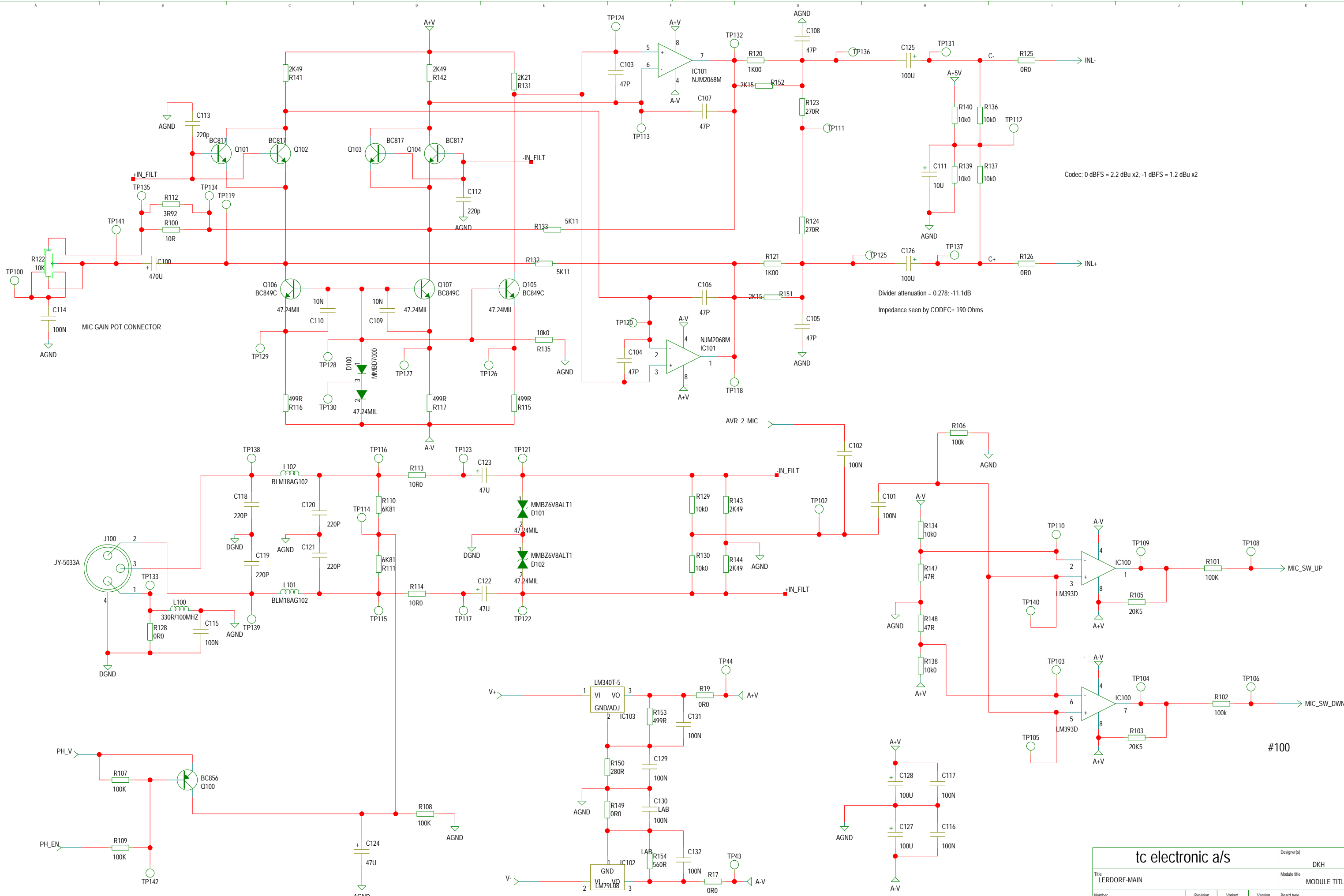
P22624-D  
M1

tc electronic a/s				Designer(s) DKH
Title LERDORF-MAIN				Module title MODULE TITLE
Number P22624	Revision D	Variant B	Version B	Board type MAINBOARD
Date 19-09-2011_13:10	Filename p22624-D			Page / of 1 / 8





<b>tc electronic a/s</b>				Designer(s)	DKH
Title				Module title	
LERDORF-MAIN				MODULE TITLE	
Number	Revision	Variant	Version	Board type	
P22624	D	B	B	MAINBOARD	
Date	Filename	Page / of			
19-09-2011_13:09	dsp720	1 / 8			

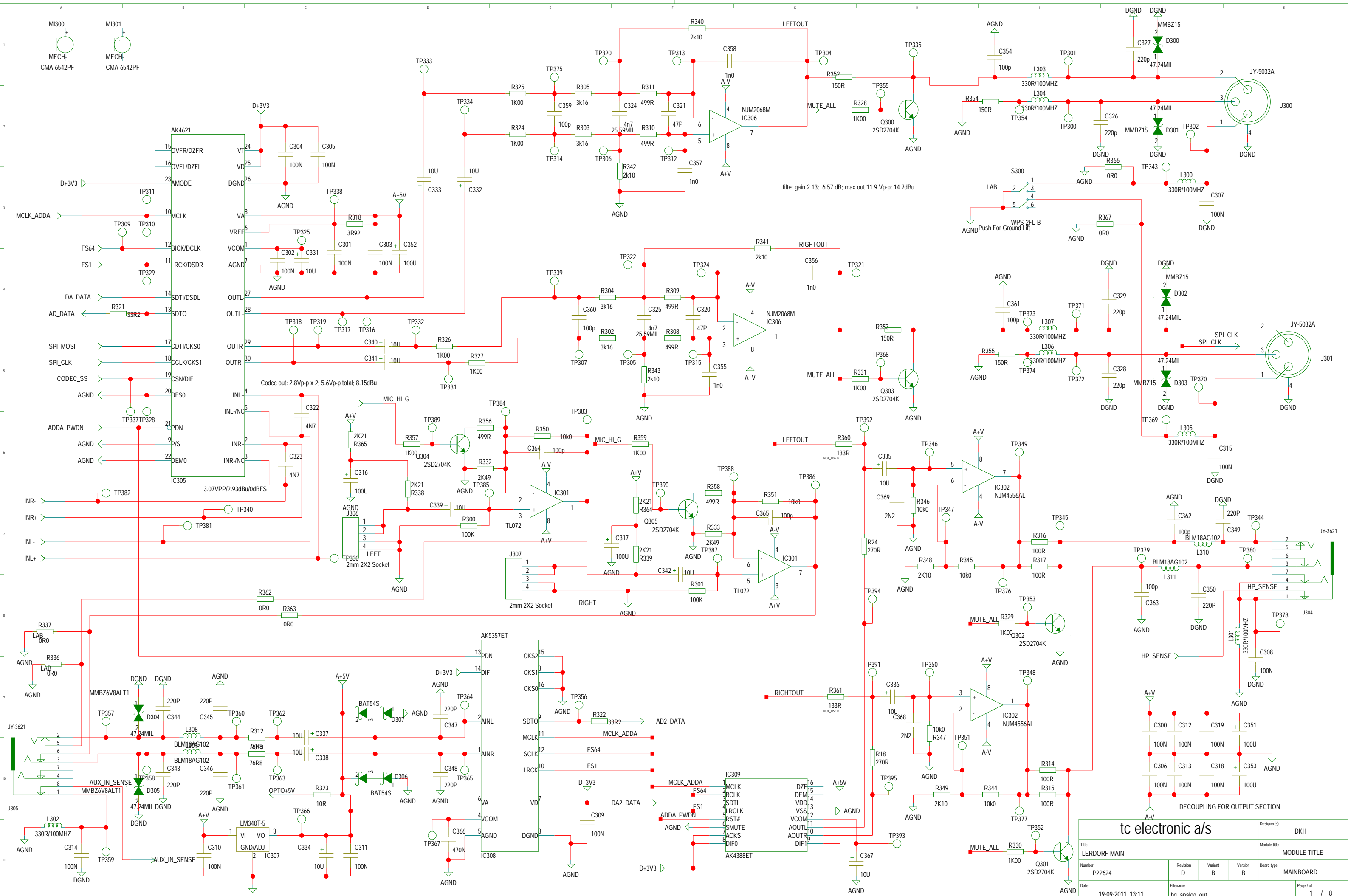


Codec: 0 dBFS = 2.2 dBu x2, -1 dBFS = 1.2 dBu x2

Divider attenuation = 0.278; -11.1dB  
Impedance seen by CODEC= 190 Ohms

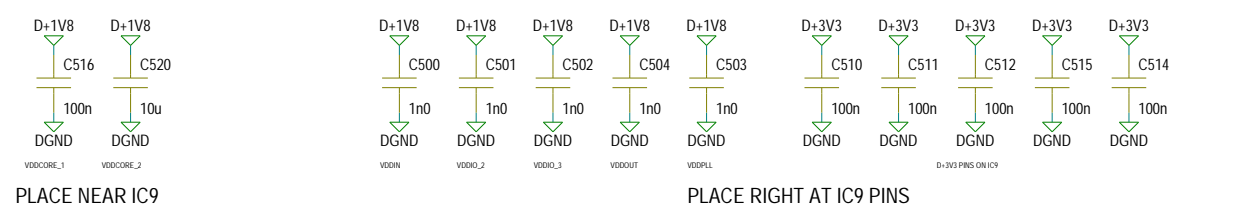
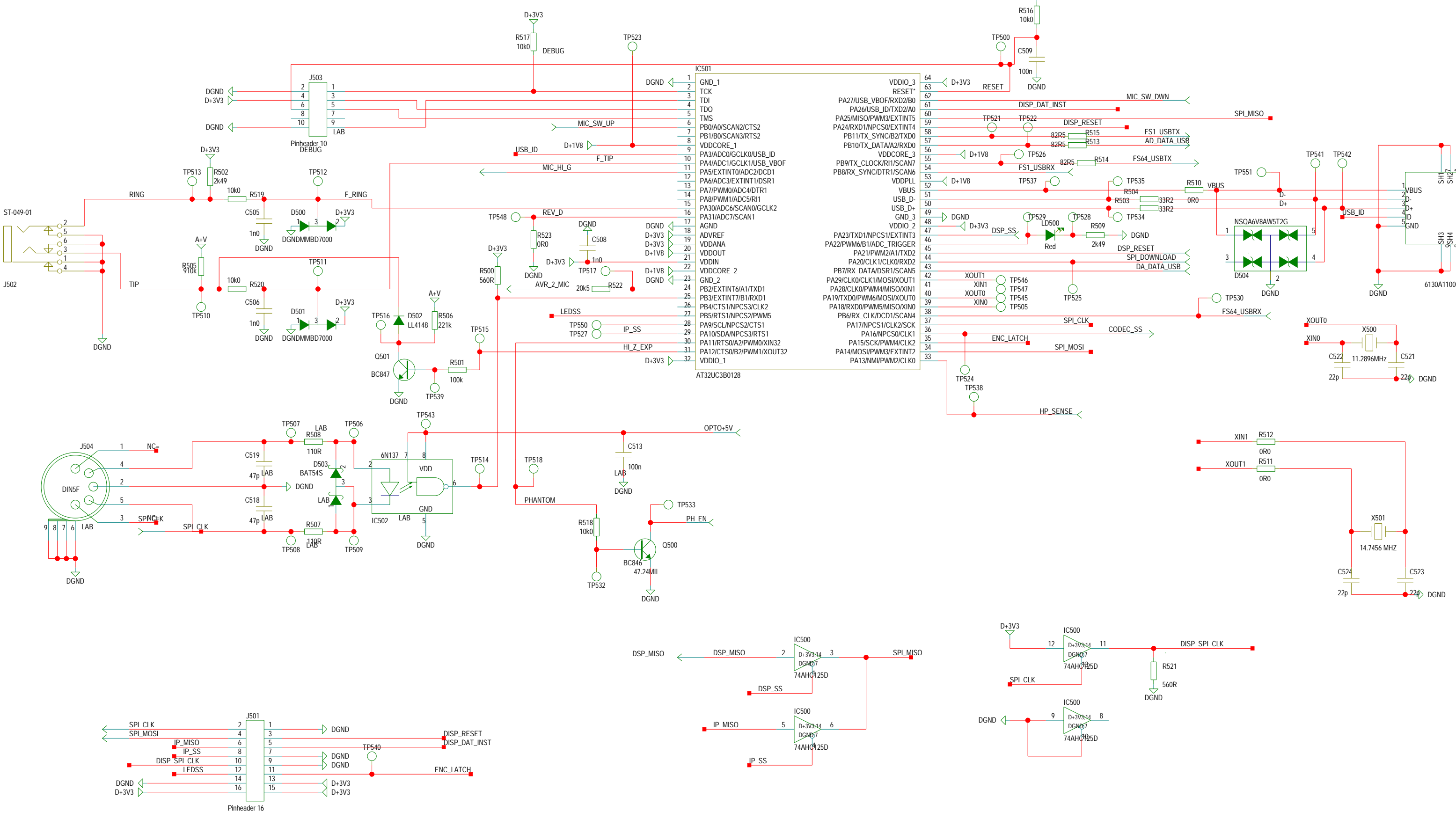
#100

tc electronic a/s				Designer(s)	DKH
Title LERDORF-MAIN				Module title	MODULE TITLE
Number	Revision	Variant	Version	Board type	MAINBOARD
P22624	D	B	B		



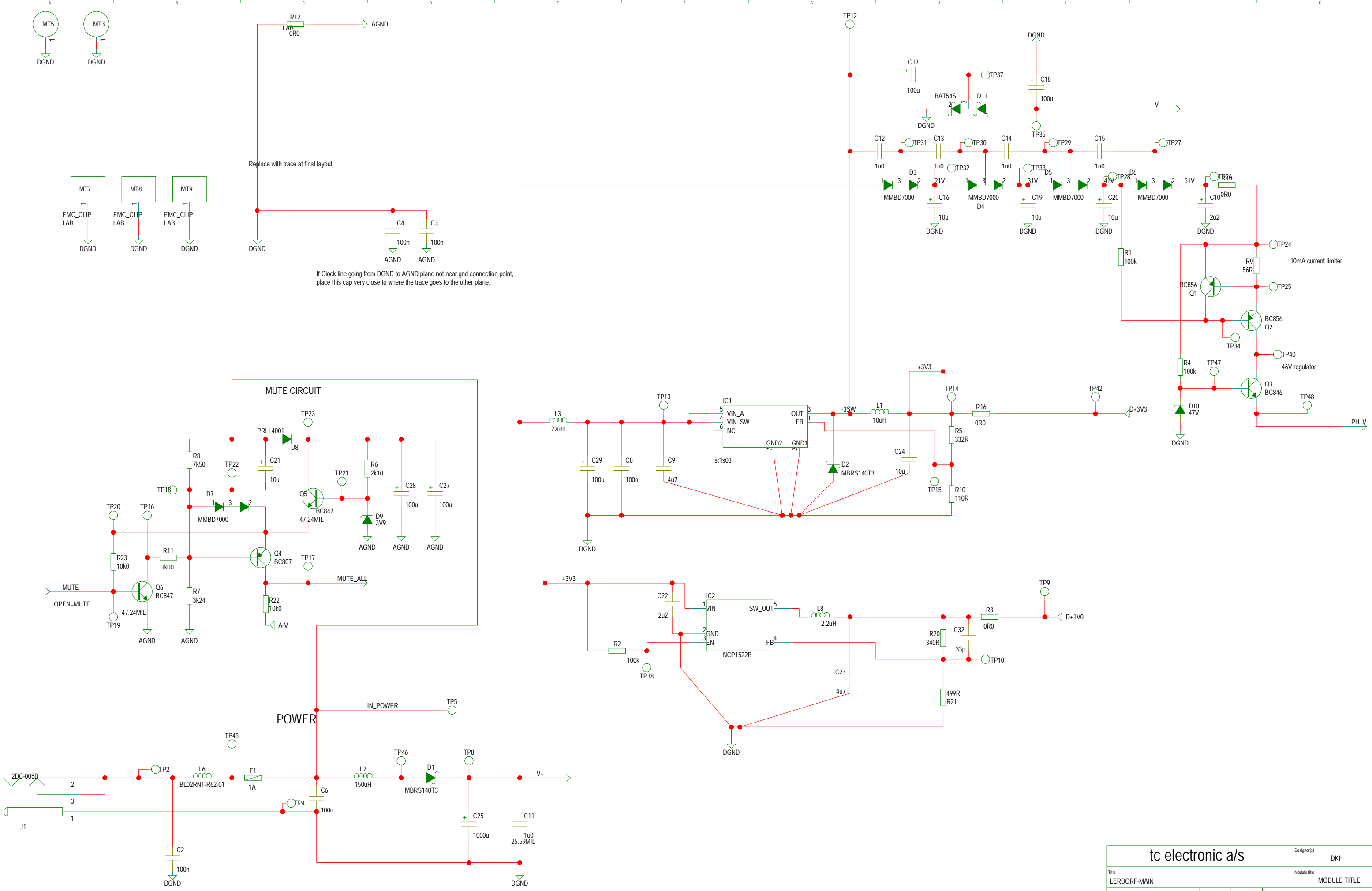
<b>tc electronic a/s</b>				Designer(s)	DKH
Title LERDORF-MAIN				Module title MODULE TITLE	
Number P22624	Revision D	Variant B	Version B	Board type MAINBOARD	
Date 19-09-2011_13:11	Filename hg_analog_out			Page / of 1 / 8	





tc electronic a/s				Designer(s)	DKH
LERDORF-MAIN				Module title	MODULE TITLE
Number	Revision	Variant	Version	Board type	MAINBOARD
P22624	D	B	B		
Date	Filename			Page / of	1 / 8
19-09-2011_13:10	usb_ui				





Replace with trace at final layout

If Clock line going from DGND to AGND plane not near gnd connection point, place this cap very close to where the trace goes to the other plane.

tc electronic a/s				Designer(s)	DKH
LERDORF-MAIN				Module title	MODULE TITLE
Number	Revision	Variant	Version	Board type	MAINBOARD
P22624	D	B	B		
Date	Filename			Page / of	1 / 8
19-09-2011_13:10	interface				