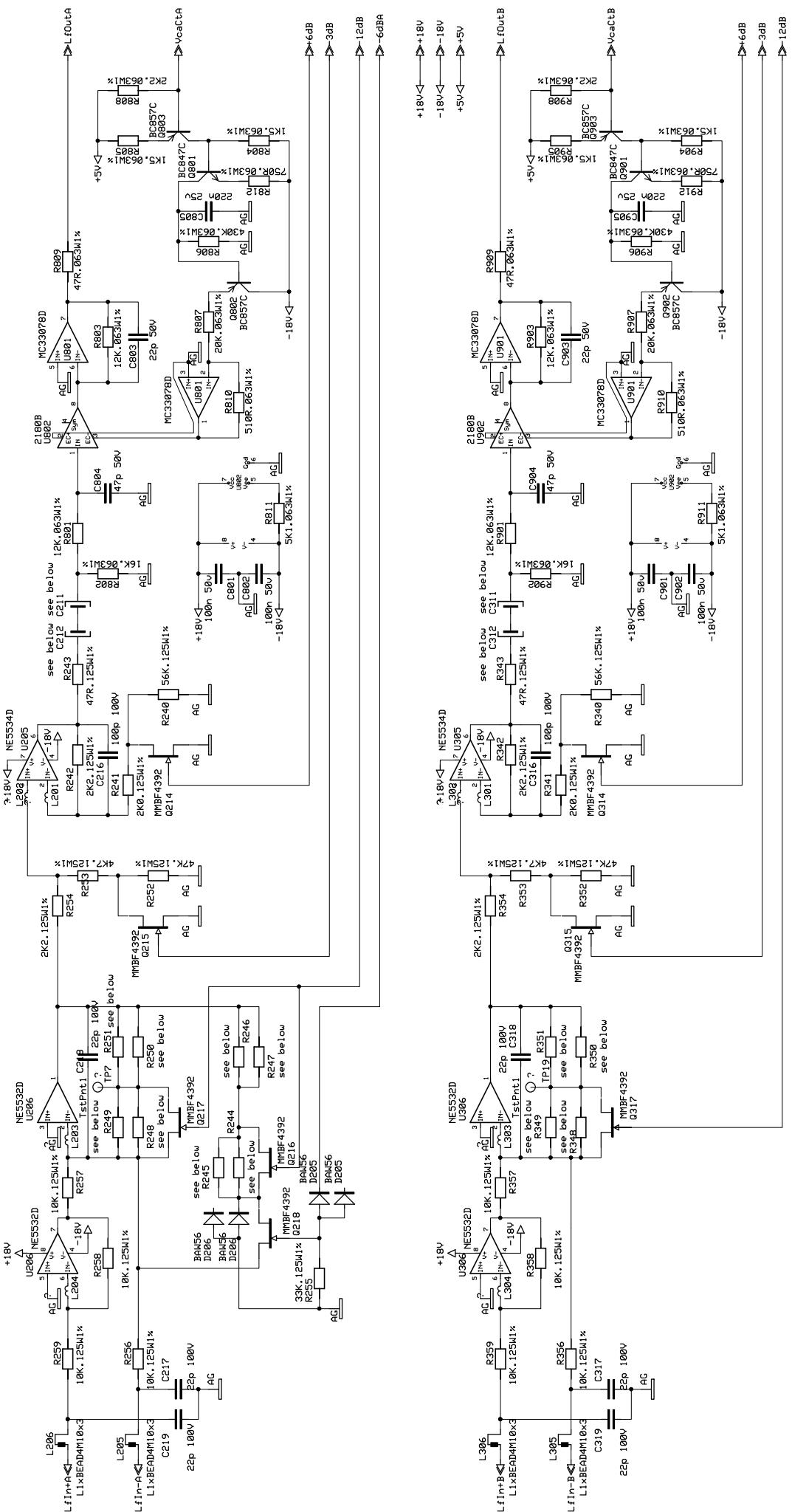


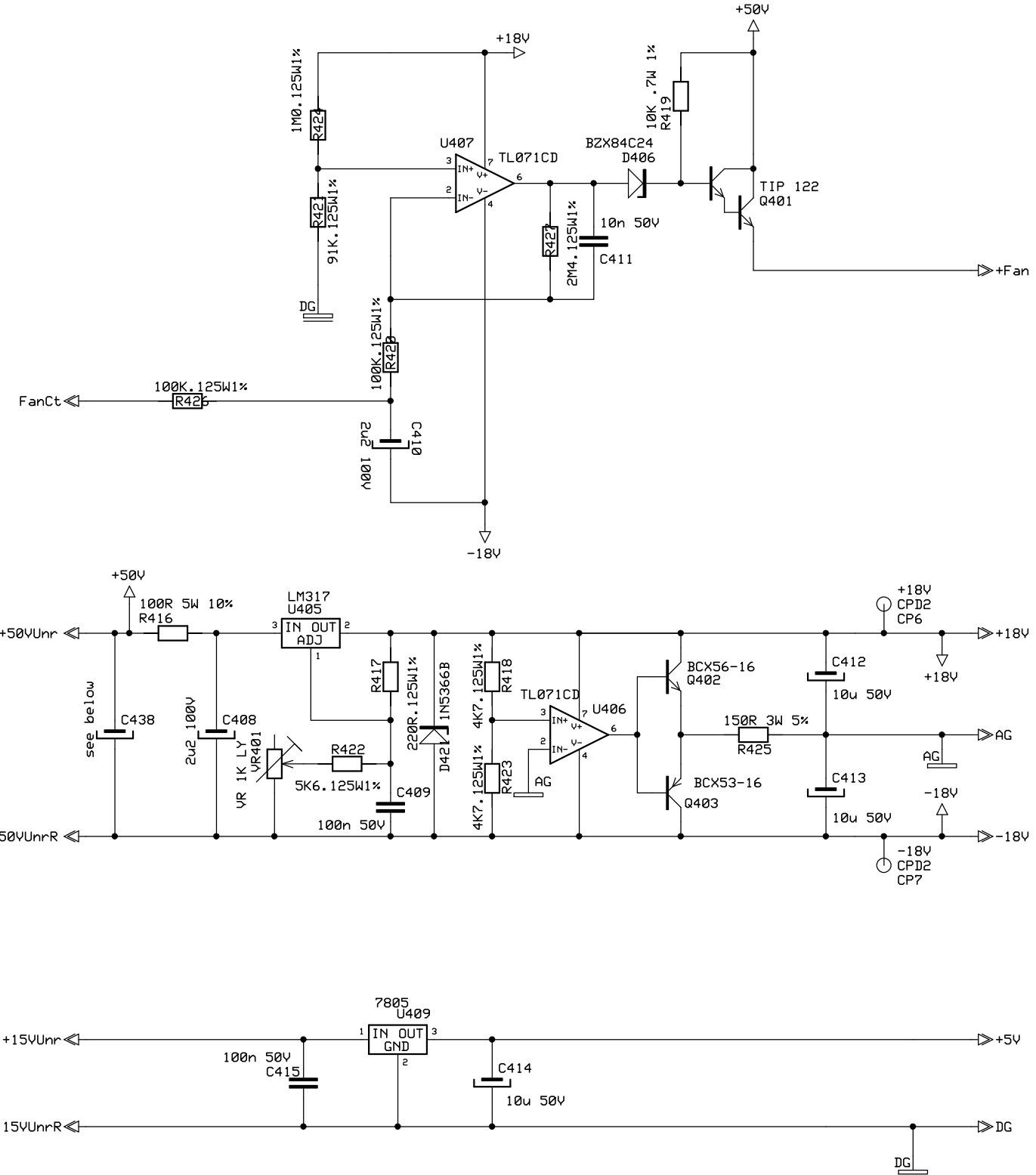
DESIGN	DRAWN	CHECKED	REPLACES	REPLACED BY

LABGRUPPEN  
KUNGSSBACKA SWEDEN

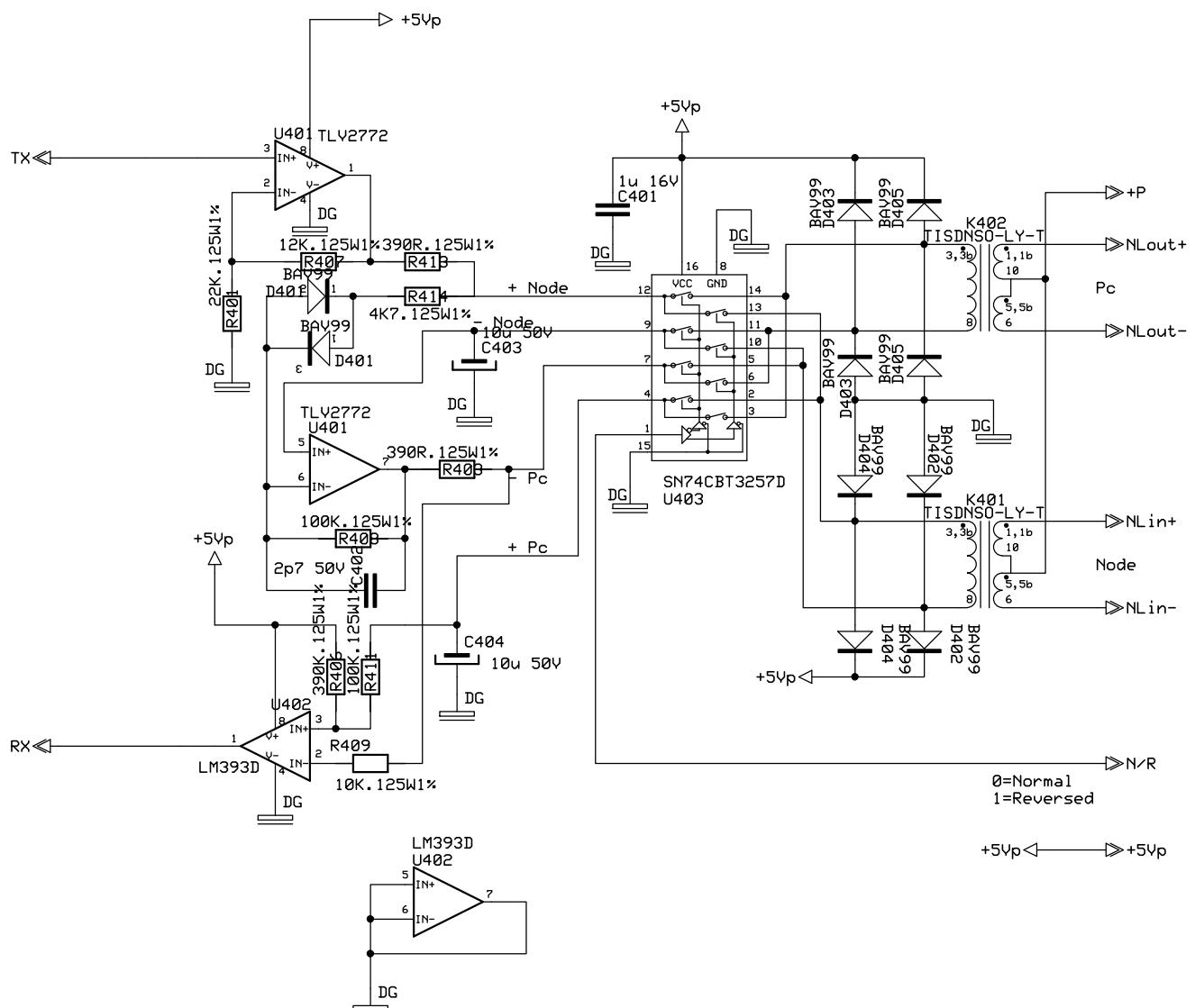


DESIGN	DRAWN	CHECKED	REPLACES	REPLACED BY

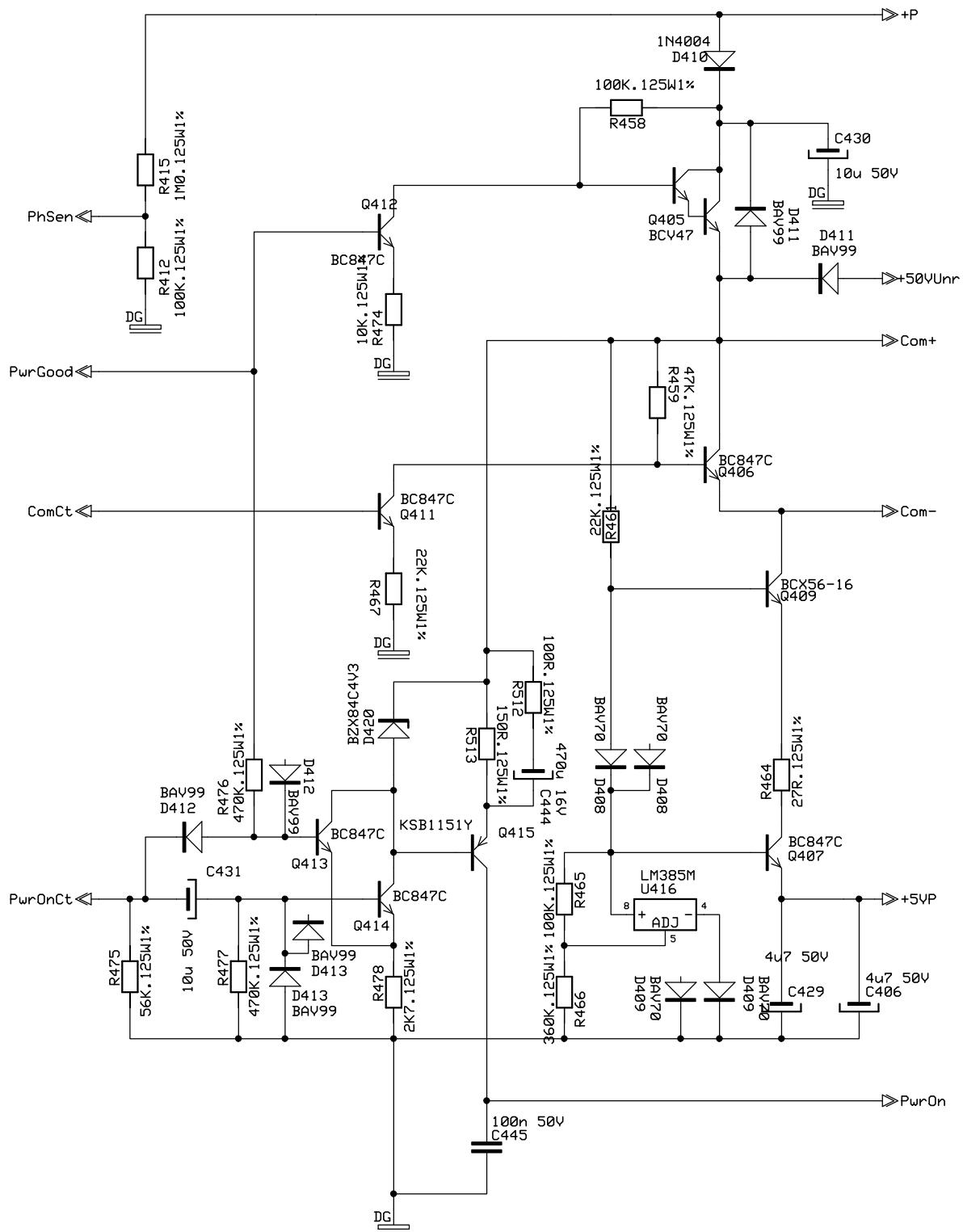
LABGRUPPEN  
KUNGSSBACKA SWEDEN



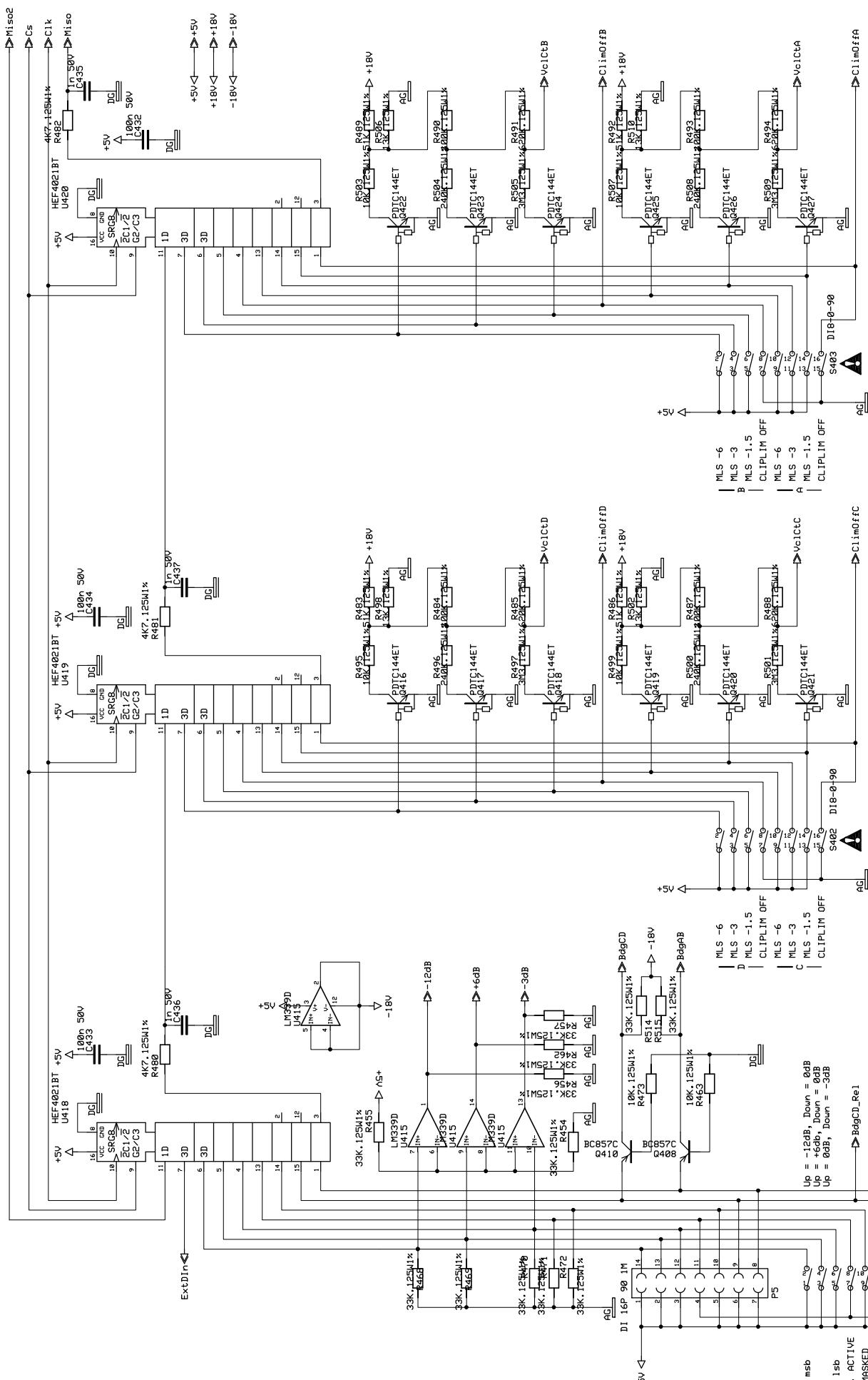
DESIGN KA	DRAWN	CHECKED	LYPsB	REPLACES	REPLACED BY
LABGRUPPEN KUNGSBACKA SWEDEN			Low voltage power supply type1	DATE	PAGE



DESIGN	DRAWN	CHECKED	NLI	REPLACES	REPLACED BY
LABGRUPPEN KUNGSBACKA SWEDEN			Nomad link interface	DATE DRAWING NO	PAGE



DESIGN KA	DRAWN	CHECKED	NLIPs	REPLACES	REPLACED BY
LABGRUPPEN KUNGSBACKA SWEDEN			Nomad Link Interface Power Supply	DATE PAGE	

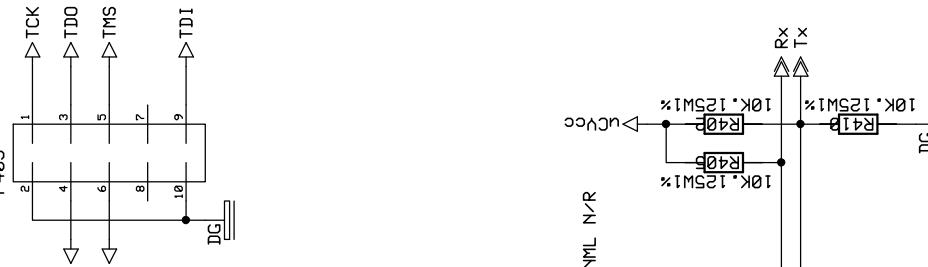


LABGRUPPEN  
KUNGSSBACKA SWEDEN

DESIGN	DRAIN	CHECKED	REPLACES	REPLACED BY
			DATE	PAGE
			DRAWING NO	
LÄBGRUPPEN KUNGSSBACKA SWEDEN				

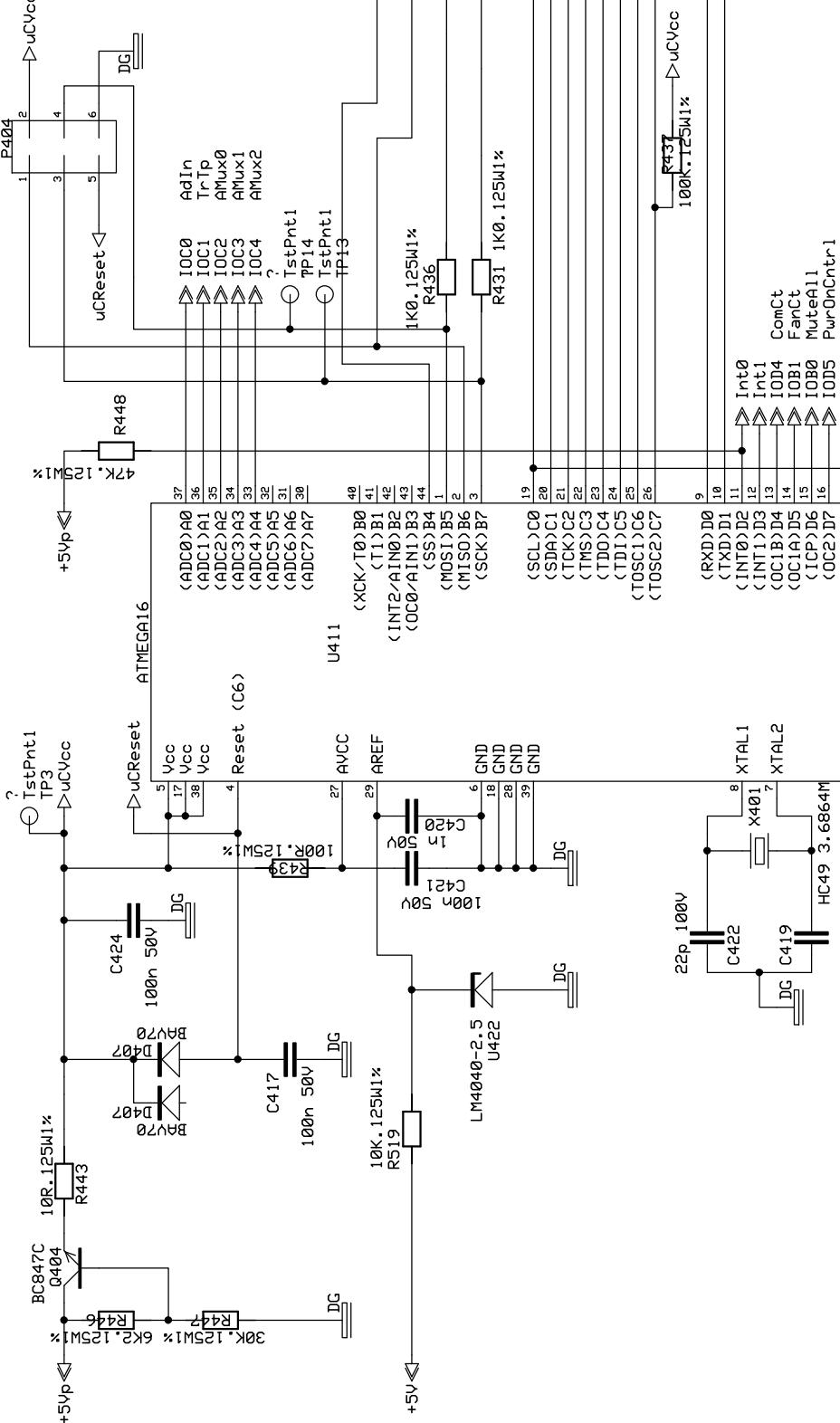
## JTAG Port

see below



## ISP Port

$\mu$ H2x31M



DESIGN	DRAWN	CHECKED	UPC <b>B</b>
--------	-------	---------	--------------

**UPC**B****

REPLACES  
DATE

REPLACED BY  
PAGE

Micro Processor unit type**B**  
DRAWING NO

UPC**B**

**LABGRUPPEN**  
KUNGSSBACKA SWEDEN