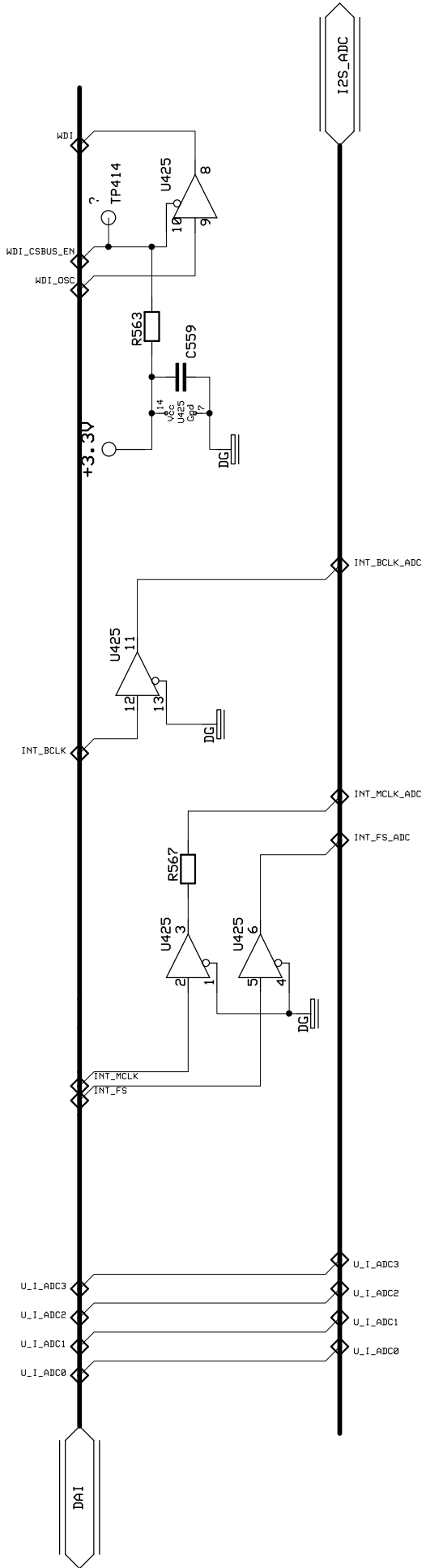
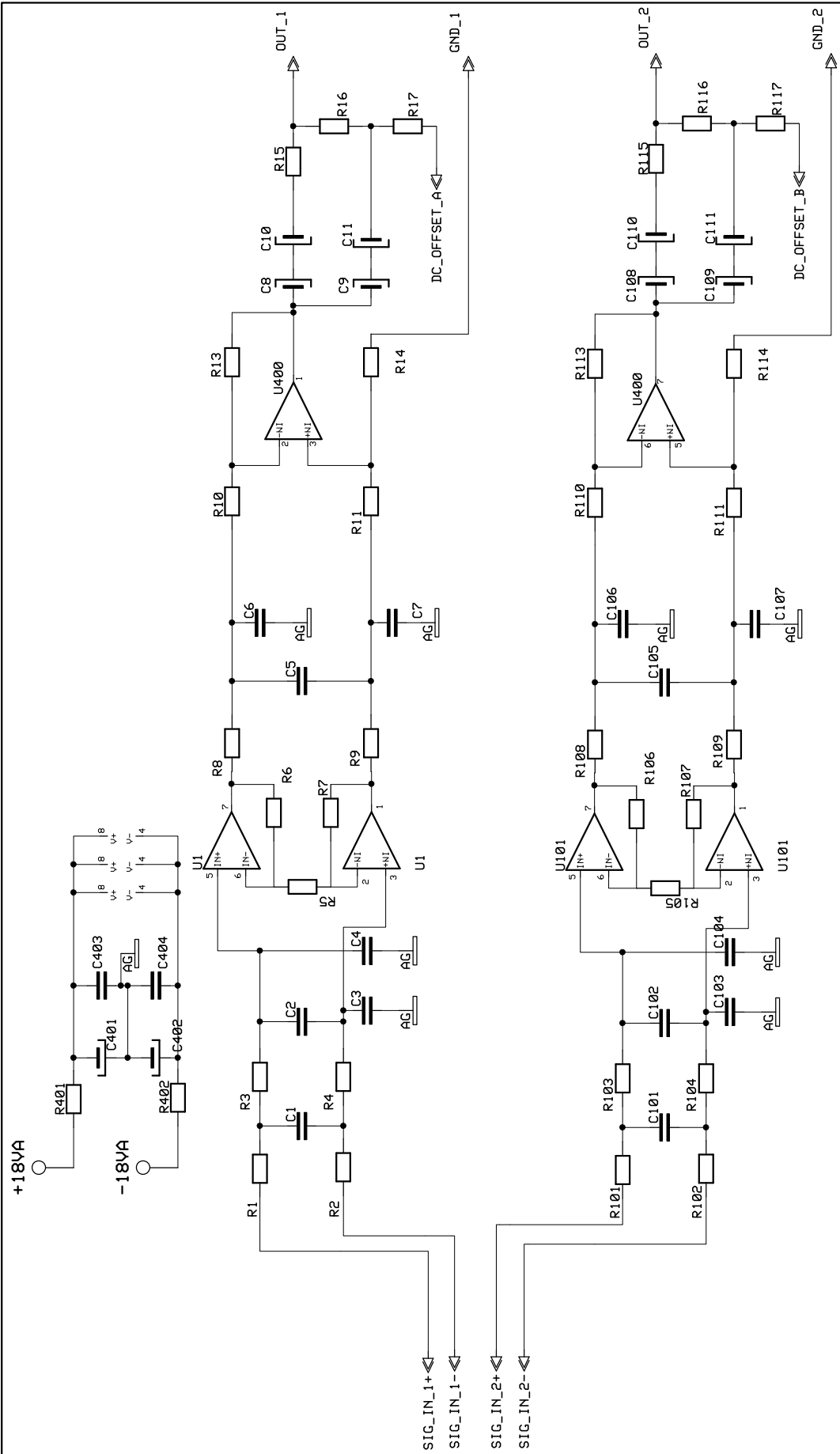
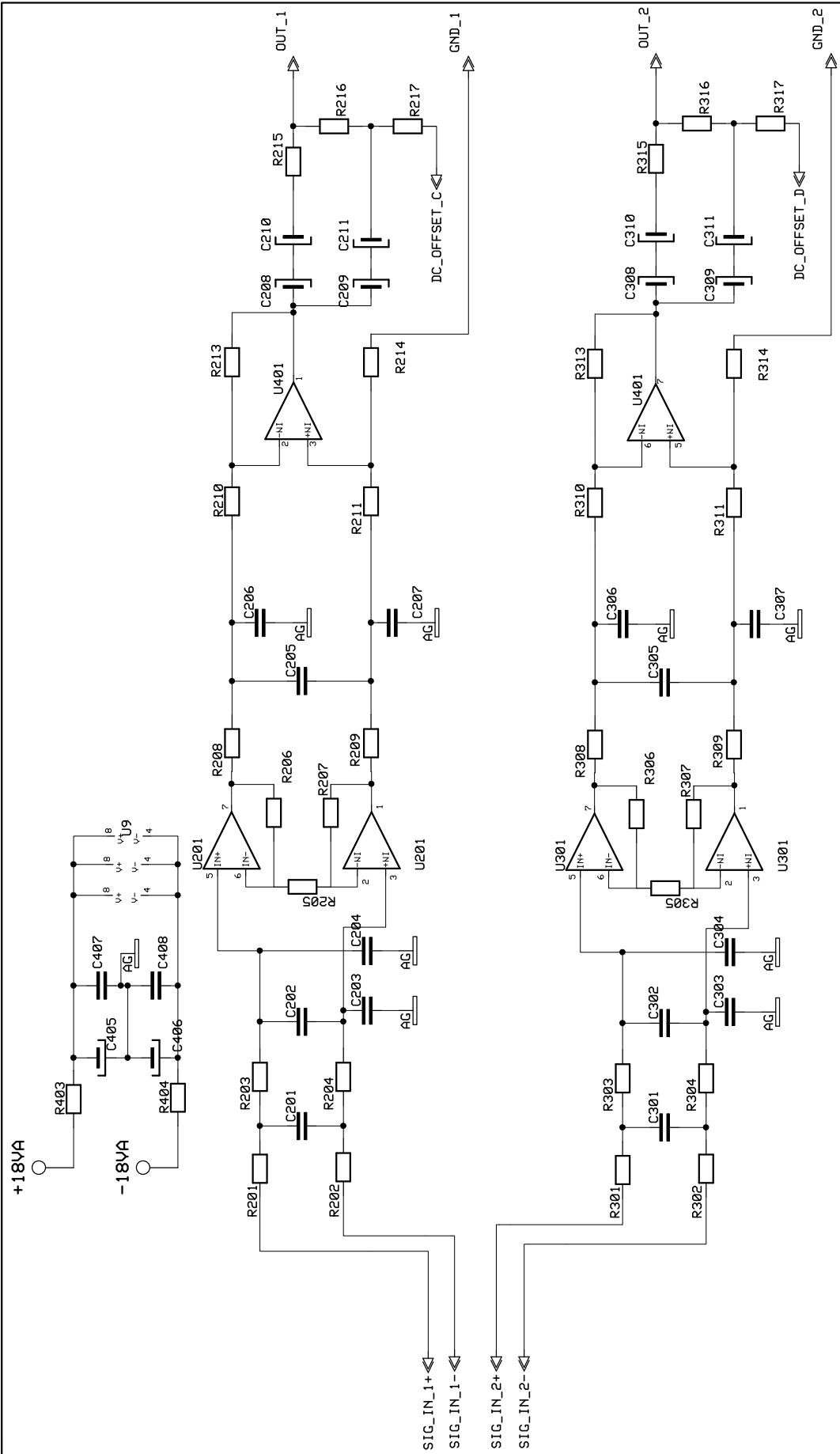




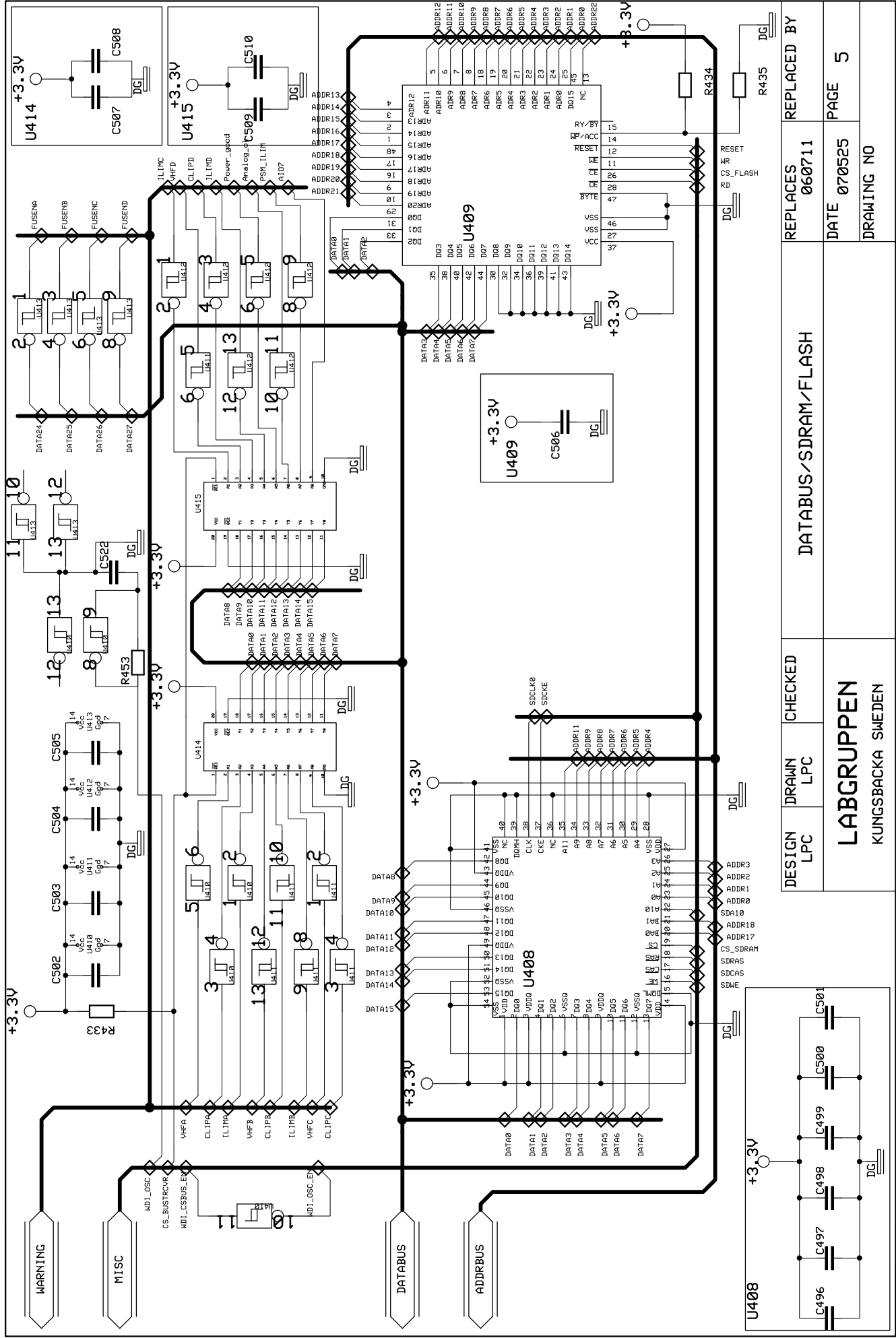
DESIGN LPC	DRAWN LPC	CHECKED	ADC_I2S	REPLACES 060711	REPLACED BY
LABGRUPPEN KUNGSBACKA SWEDEN			DATE 070525	PAGE 2	
			DRAWING NO		



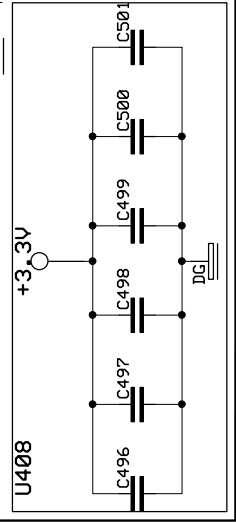
DESIGN BA	DRAWN LPC	CHECKED	D/A_AB	REPLACES 060711	REPLACED BY
LABGRUPPEN KUNGSBACKA SWEDEN				DATE 070525	PAGE 3
D/A conversion Ch. A & B				DRAWING NO	

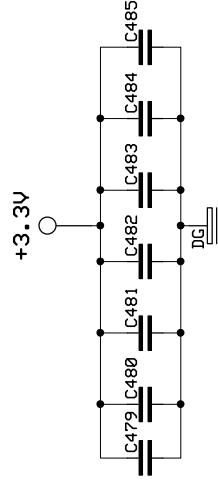
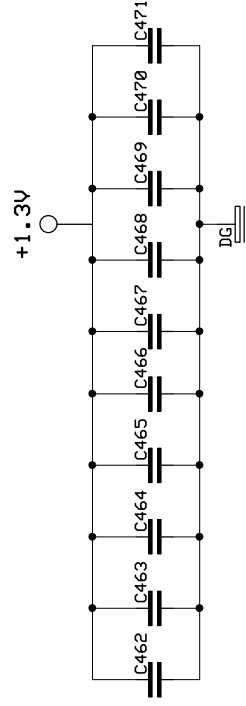
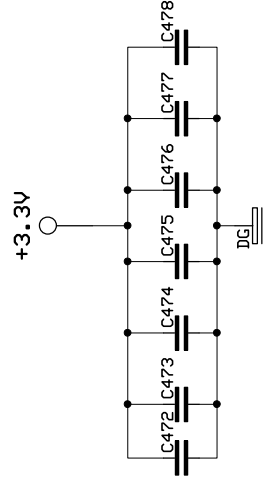
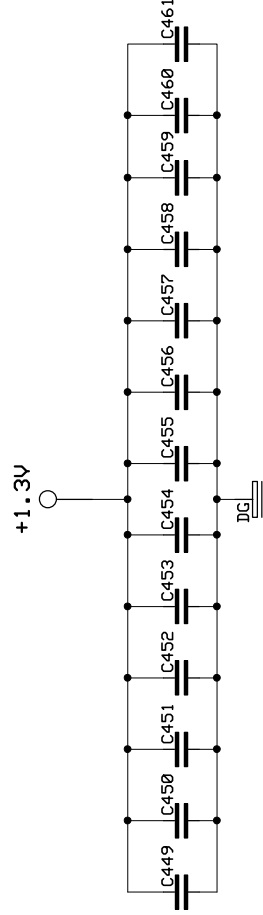


DESIGN BA	DRAWN LPC	CHECKED	D/A_CD		REPLACES 060711	REPLACED BY
LABGRUPPEN KUNGSBACKA SWEDEN			D/A conversion Ch. C & D		DATE 070525	PAGE 4
					DRAWING NO	

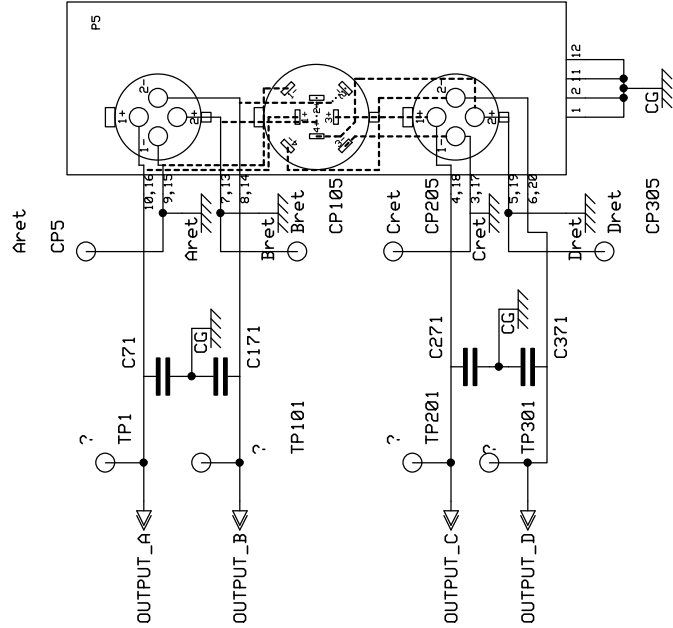


DESIGN	DRAWN	CHECKED	REPLACES	REPLACED BY
LPC	LPC		060711	
LABGRUPPEN KUNGSBACKA SWEDEN			DATE	PAGE
			070525	5
			DRAWING NO	

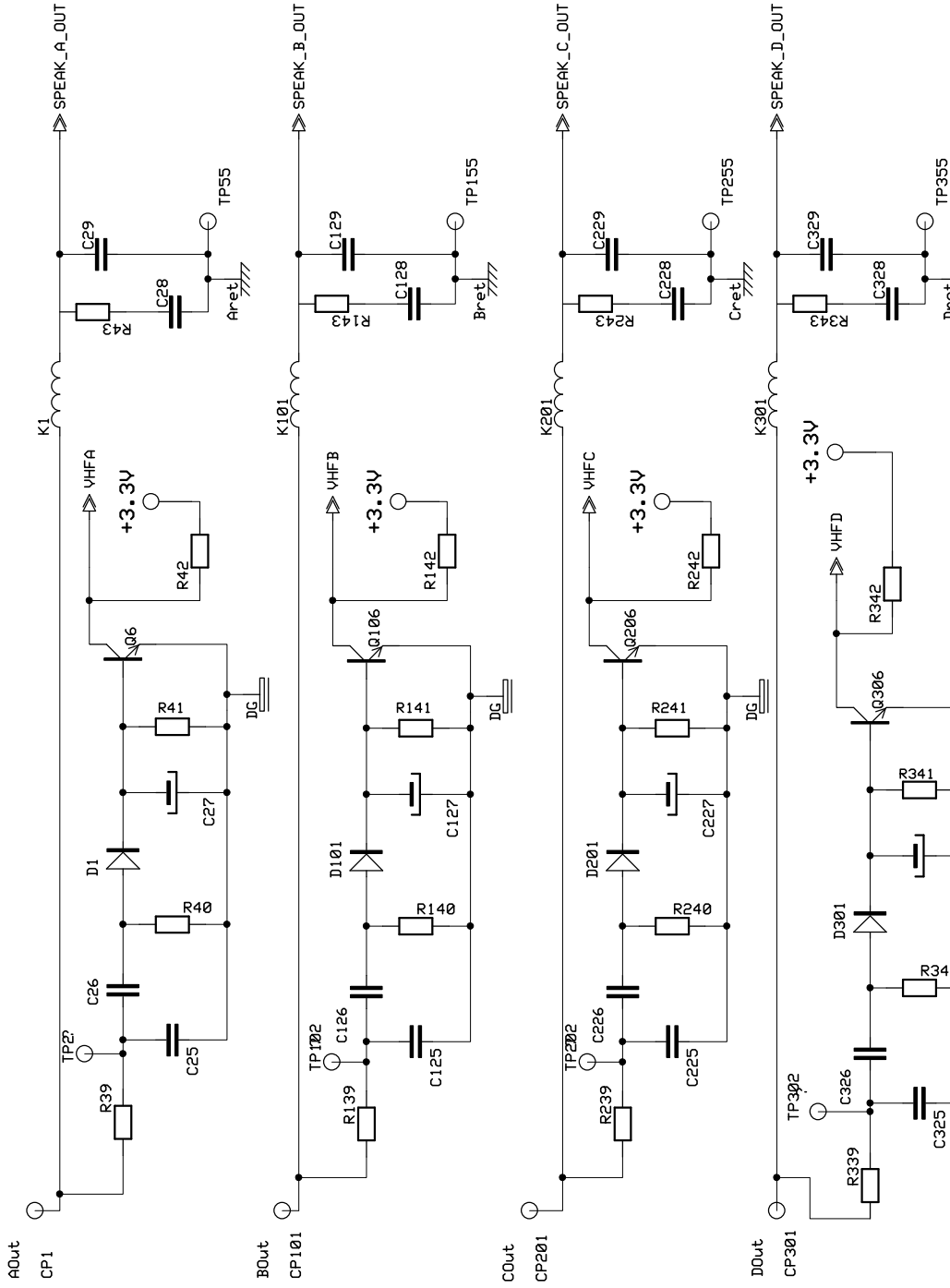




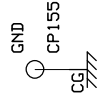
DESIGN LPC	DRAWN LPC	CHECKED	SHARC Decoupling	REPLACES 060711	REPLACED BY
LABGRUPPEN KUNGSBACKA SWEDEN				DATE 070525	PAGE 7
				DRAWING NO	

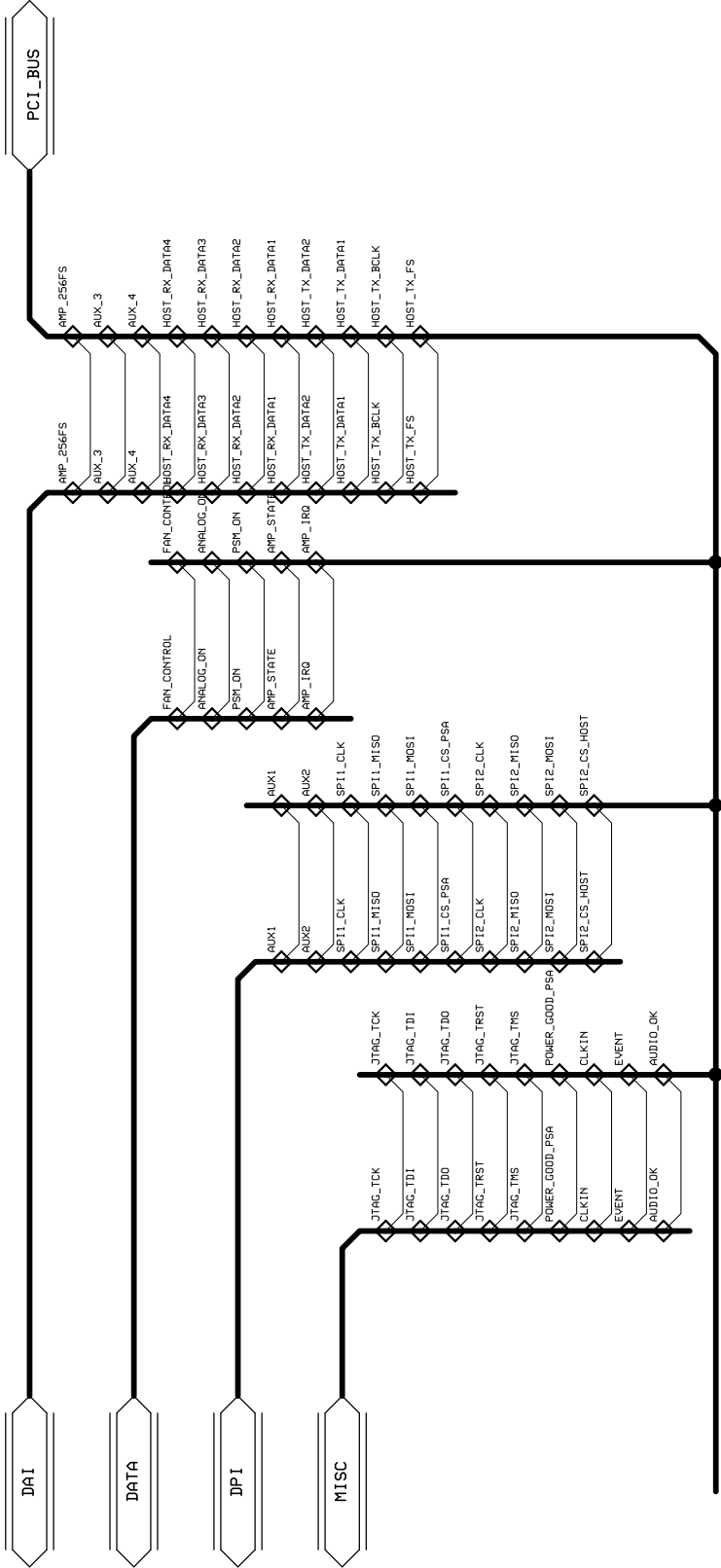


DESIGN	DRAWN	CHECKED	Output	REPLACES Ø70525	REPLACE
LABGRUPPEN KUNGSBACKA SWEDEN			Output connector	DATE 080229	PAGE 9
				DRAWING NO	

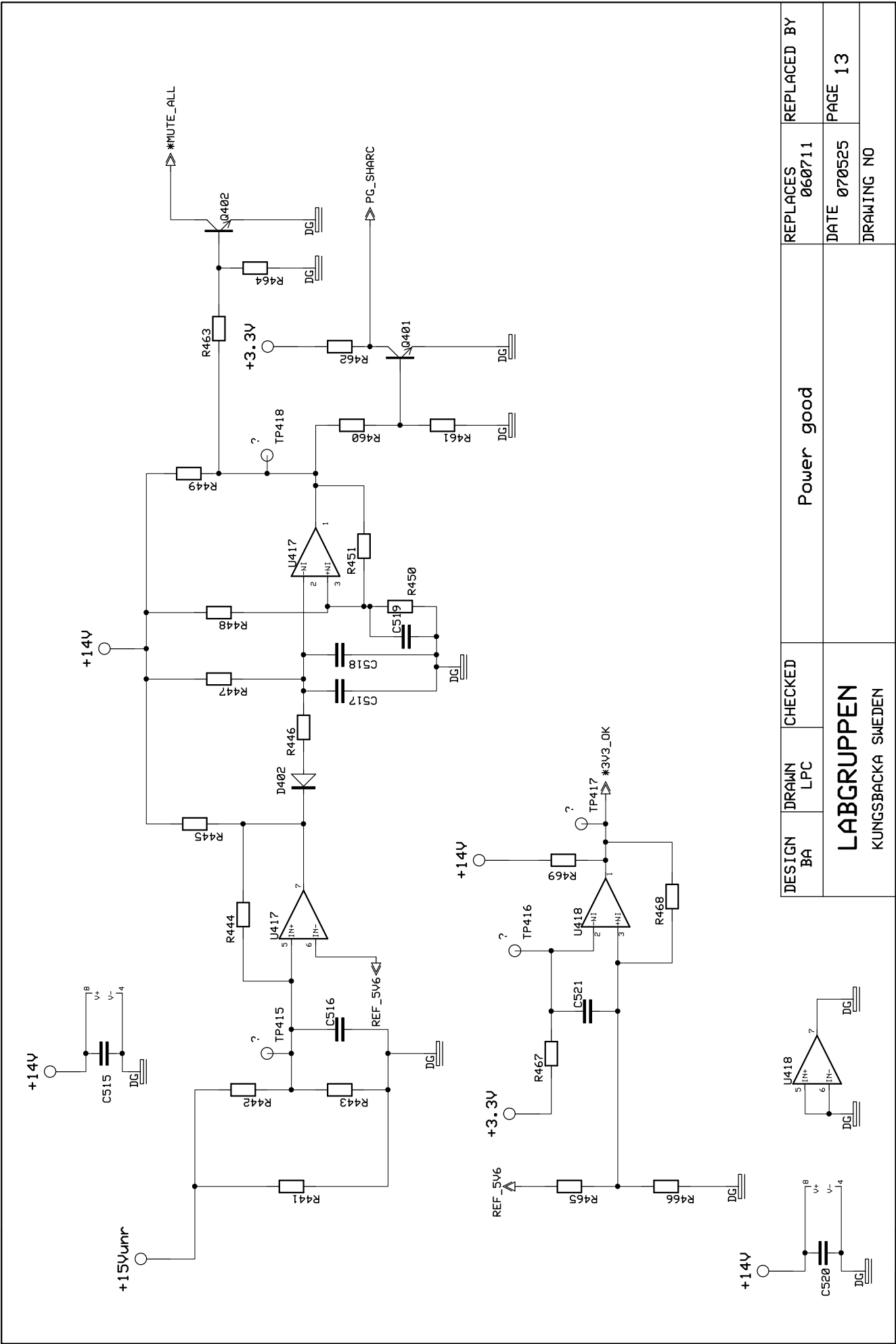


DESIGN LPC	DRAWN LPC	CHECKED	OUTPUT_FLTR	REPLACES 070525	REPLACED BY
LABGRUPPEN KUNGSBACKA SWEDEN			Output filter and VHF	DATE 080229	PAGE 10
				DRAWING NO	

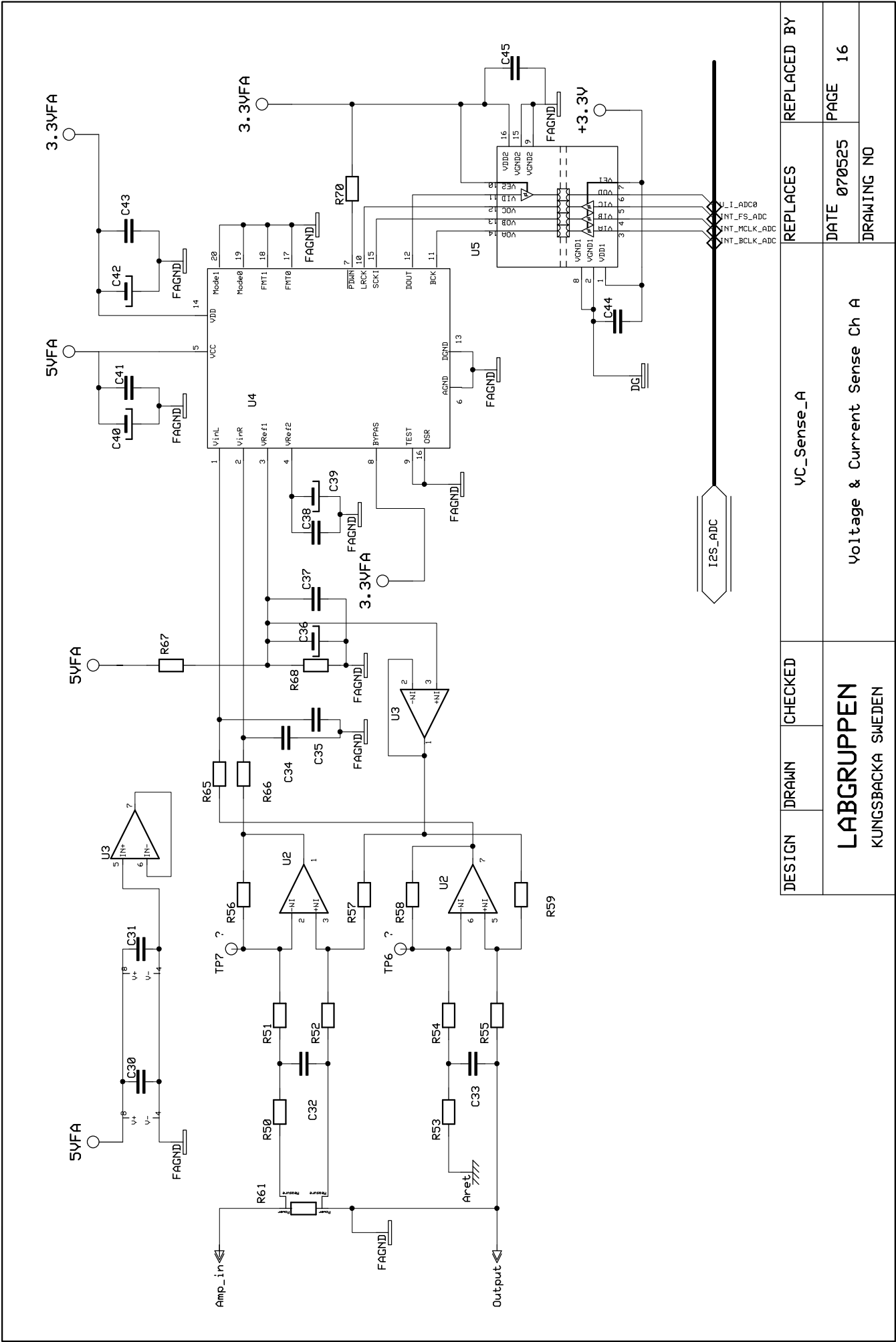




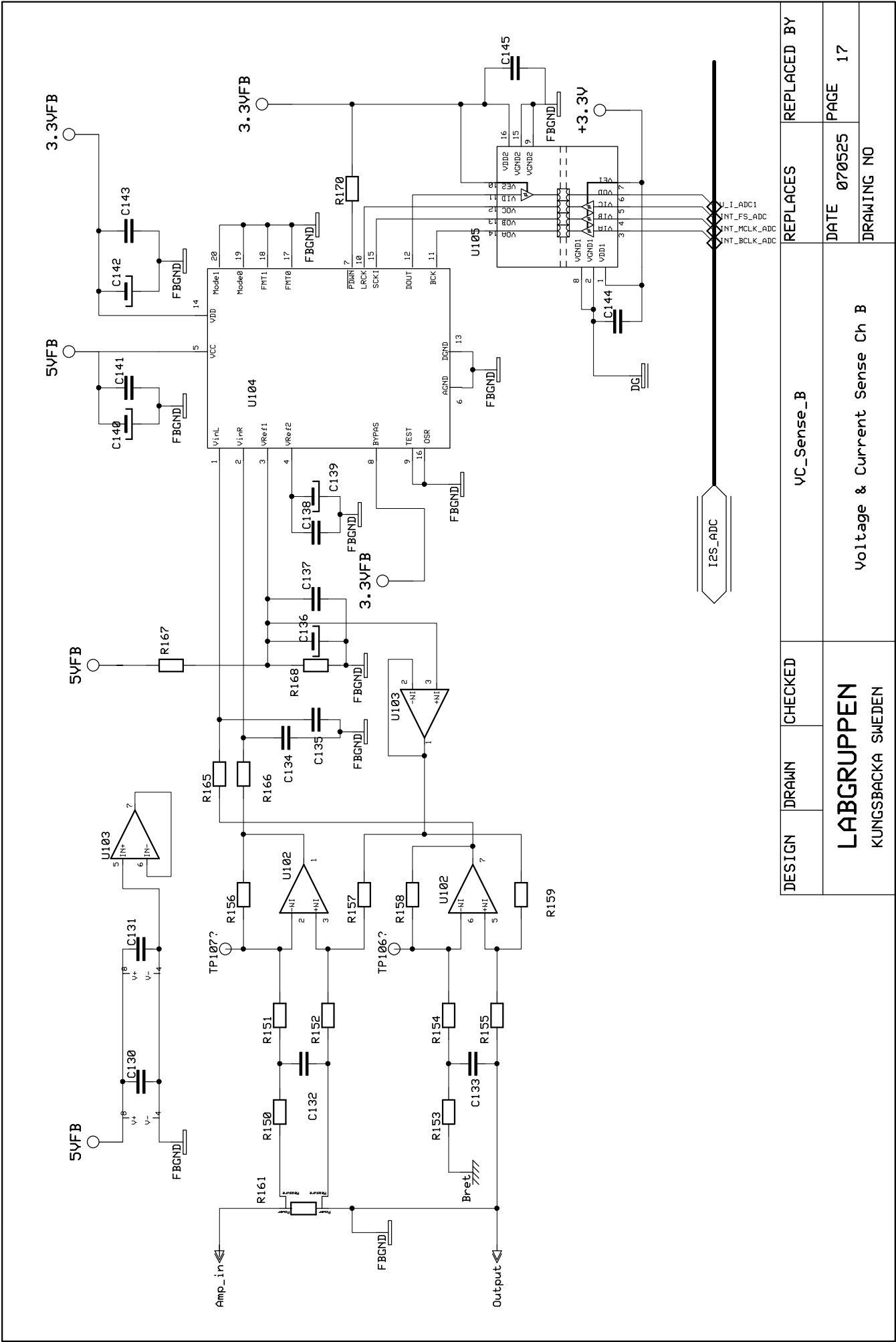
DESIGN LPC	DRAWN LPC	CHECKED	PCI_BUS		REPLACES 060711	REPLACED BY
LABGRUPPEN KUNGSBACKA SWEDEN					DATE 070525	PAGE 12
					DRAWING NO	



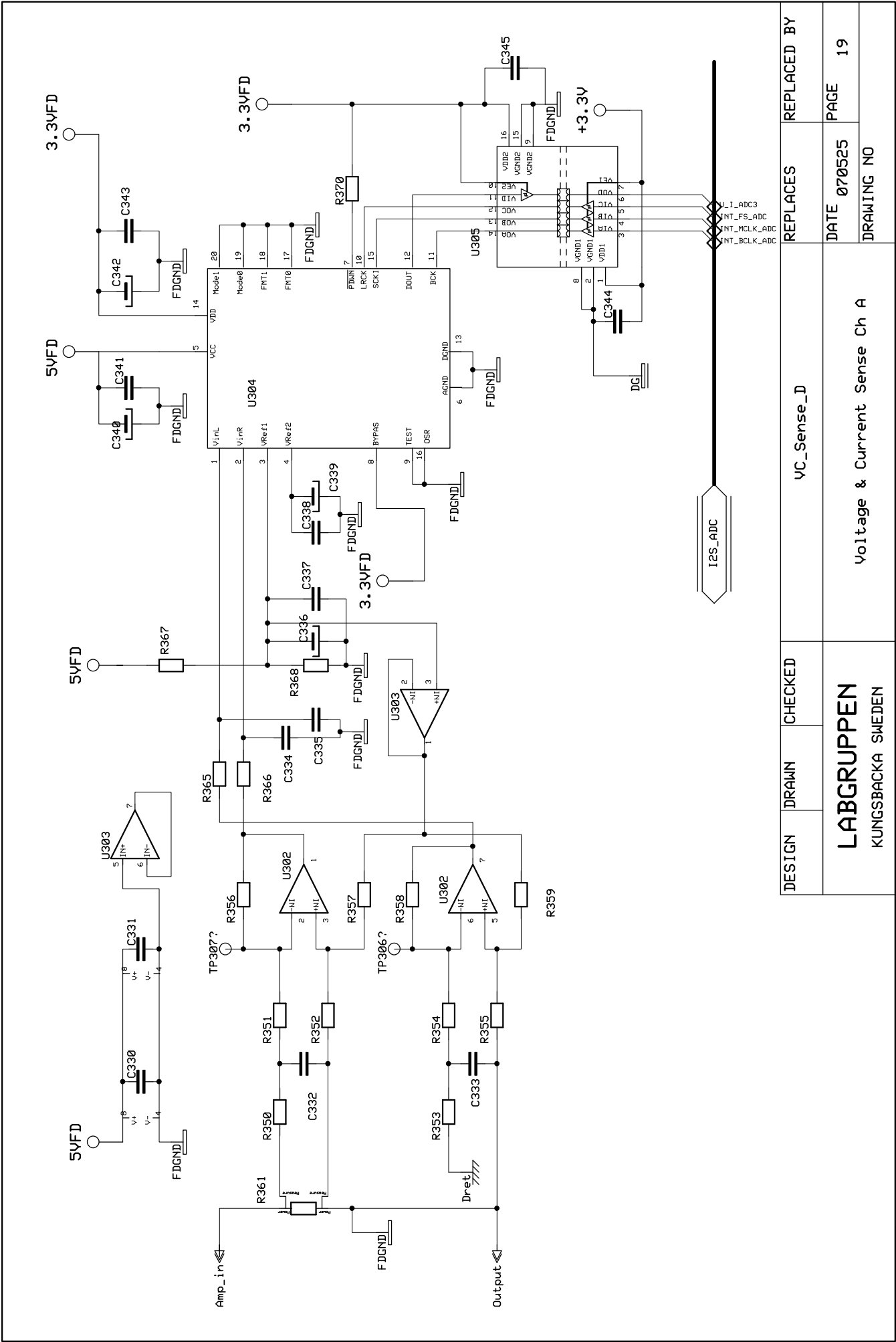
DESIGN	BA	DRAWN	LPC	CHECKED	Power good	REPLACES	060711	REPLACED BY
LABGRUPPEN						DATE	070525	PAGE 13
KUNGSBACKA SWEDEN						DRAWING NO		



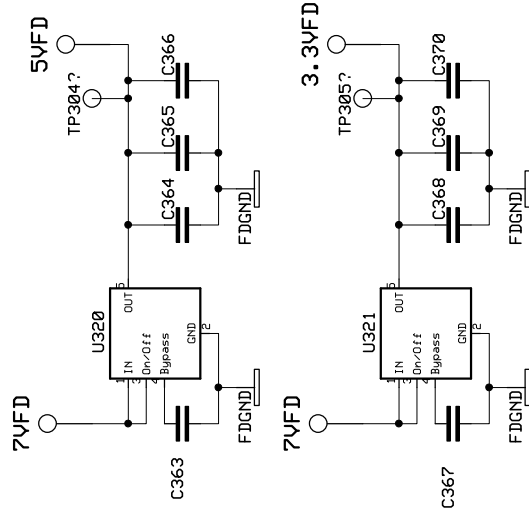
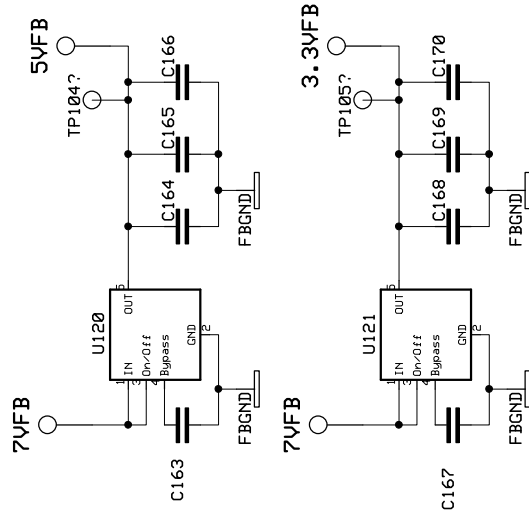
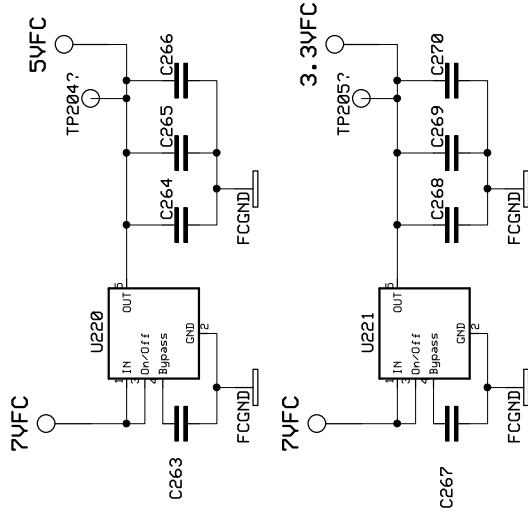
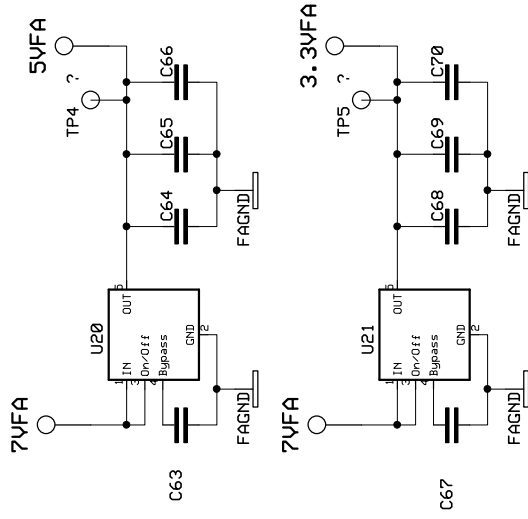
DESIGN	DRAWN	CHECKED	REPLACES	REPLACED BY
LABGRUPPEN KUNGSBACKA SWEDEN			VC_Sense_A	
			DATE 070525	PAGE 16
			DRAWING NO	



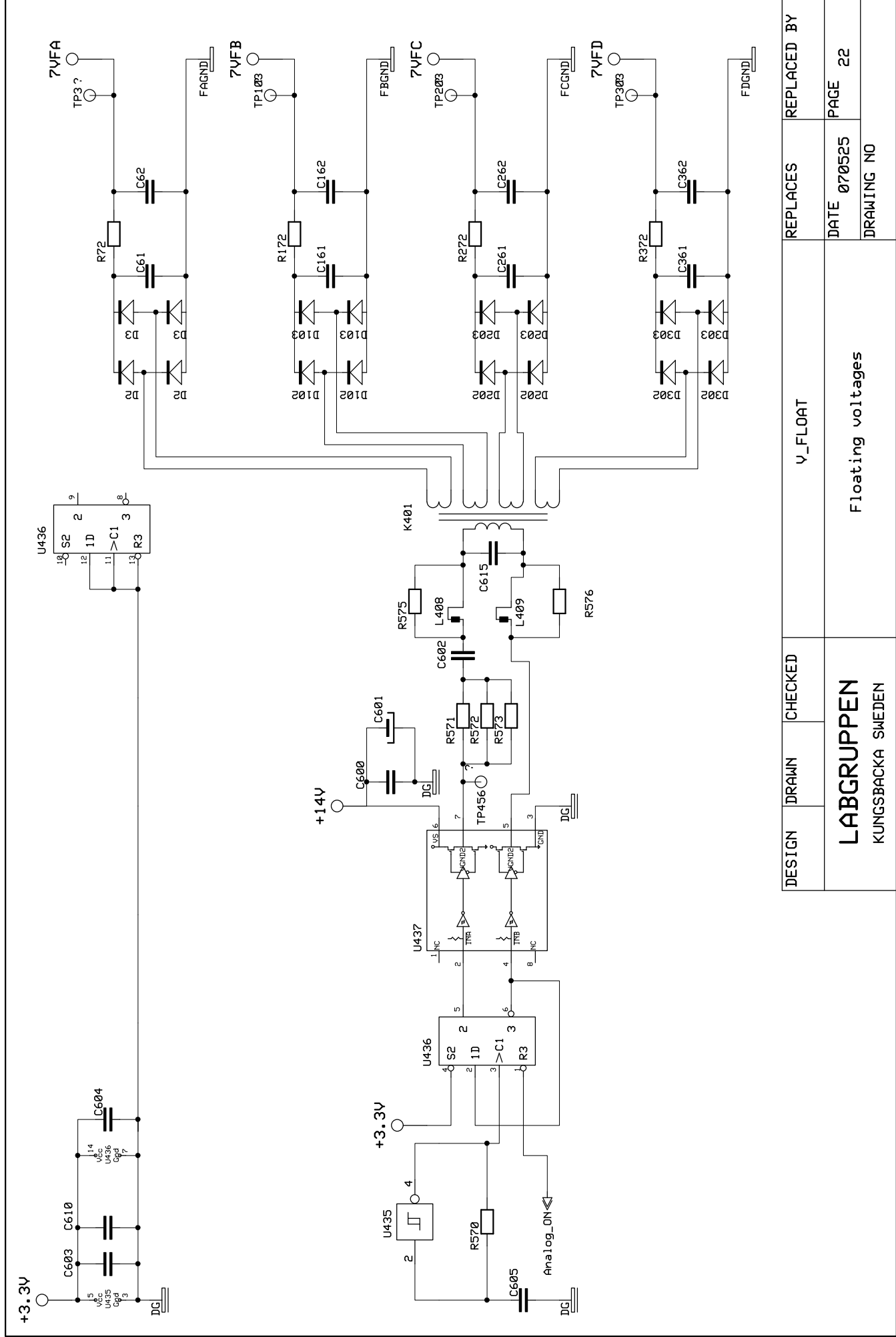
DESIGN	DRAWN	CHECKED	REPLACES	REPLACED BY
LABGRUPPEN KUNGSBACKA SWEDEN			VC_Sense_B	
			DATE 070525	PAGE 17
			DRAWING NO	



DESIGN	DRAWN	CHECKED	VC_Sense_D	REPLACES	REPLACED BY
LABGRUPPEN KUNGSBACKA SWEDEN			Voltage & Current Sense Ch A	DATE 070525	PAGE 19
				DRAWING NO	



DESIGN	DRAWN	CHECKED	vF_conv		REPLACES	REPLACED BY
LABGRUPPEN KUNGSBACKA SWEDEN			Floating voltages regulators		DATE 070525	PAGE 20
					DRAWING NO	



DESIGN	DRAWN	CHECKED	V_FLOAT		REPLACES	REPLACED BY
LABGRUPPEN KUNGSBACKA SWEDEN			Floating voltages		DATE 070525	PAGE 22
					DRAWING NO	